

# **Course Curriculum**

(Course Structure and Syllabi)  
for  
**Bachelor of Technology**  
in  
**Electronics and Communication Engineering**  
(Second Year Onwards)



Department of Electronics and Communication Engineering  
**National Institute of Technology Hamirpur**

Hamirpur, Himachal Pradesh – 177005 (India)

2024

## Curriculum for B. Tech. Programme

Course No.	Semester 3	Credits	Course Type
MA-203	Basic Sciences	3	Discipline Core
EC-211 to EC-215	Engineering Courses	14	Discipline Core
EC-216, EC-217	Engineering Course (Lab)	2	Discipline Core
EC-218	Basic Engineering Skills	1	Discipline Core
	<b>Total</b>	<b>20</b>	

Course No.	Semester 4	Credits	Course Type
EC-221 to EC-224	Engineering Course	13	Discipline Core
EC-241 to EC-243	Engineering Course	3	Discipline Elective
EC-225 to EC-227	Engineering Course (Lab)	3	Discipline Core
SA-201	LA/CA	1	Institute Elective
	<b>Total</b>	<b>20</b>	

Second Year													
3 <sup>rd</sup> Semester							4 <sup>th</sup> Semester						
SN	Code	Subject	L	T	P	C	SN	Code	Subject	L	T	P	C
1	EC-211	Digital Electronics and Logic Design	3	0	0	3	1	EC-221	Linear Integrated Circuits	3	0	0	3
2	EC-212	Analog Electronics	3	0	0	3	2	EC-222	Analog Communication Systems	3	0	0	3
3	EC-213	Communication Theory	3	0	0	3	3	EC-223	Microprocessors and Microcontrollers	3	0	0	3
4	EC-214	VLSI Technology	3	0	0	3	4	EC-224	Electromagnetic Field Theory	3	1	0	4
5	EC-215	Network Analysis and Synthesis	2	0	0	2	5	EC-241 to EC-243	Discipline Elective -I	3	0	0	3
6	MA-203	Engineering Mathematics-III	3	0	0	3	6	EC-225	Linear Integrated Circuits Lab	0	0	2	1
7	EC-216	Digital Electronics and Logic Design Lab	0	0	2	1	7	EC-226	Analog Communication Lab	0	0	2	1
8	EC-217	Analog Electronics Lab	0	0	2	1	8	EC-227	Microprocessors and Microcontrollers Lab	0	0	2	1
9	EC-218	Electronics Workshop	0	0	2	1	9	SA-201	LA/CA (Institute Elective)	0	0	2	1
Total Hours = 23			20				Total Hours = 24			20			

#### Discipline Elective-I

EC-241	Electronic Device Modeling
EC-242	Nano Technology
EC-243	Digital Arithmetic

## Curriculum for B. Tech. Programme

Course No.	Semester 5	Credits	Course Type
EC-301 to EC-304	Open Elective	3	Institute Electives
EC-311 to EC-314	Engineering Course	12	Discipline Core
EC-351 to EC-353	Engineering Course	3	Discipline Elective
EC-315, EC-316	Engineering Course (Lab)	2	Discipline Core
	<b>Total</b>	<b>20</b>	

Course No.	Semester 6	Credits	Course Type
EC-321 to EC-323	Engineering Course	8	Discipline Core
EC-341 to EC-343, EC-361 to EC-363	Engineering Course	6	Discipline Elective
EC-381 to EC-382	Engineering Course	2	Stream Core
HS-321	HSS Course	2	Institute Core
EC-324, EC-325	Engineering Course (Lab)	2	Discipline Core
	<b>Total</b>	<b>20</b>	

Third Year													
5 <sup>th</sup> Semester							6 <sup>th</sup> Semester						
SN	Code	Subject	L	T	P	C	SN	Code	Subject	L	T	P	C
1	EC-311	Microwave Devices and Systems	3	0	0	3	1	EC-321	Digital Signal Processing	3	0	0	3
2	EC-312	Digital Communication and Systems	3	0	0	3	2	EC-322	Antenna and Wave Propagation	3	0	0	3
3	EC-313	Control Systems	3	0	0	3	3	EC-323	Electronic Measurements and Instrumentation	2	0	0	2
4	EC-314	VLSI Design	3	0	0	3	4	EC-341 to EC-343	Discipline Elective-III	3	0	0	3
5	EC-301 to EC-304	Open Elective	3	0	0	3	5	EC-361 to EC-363	Discipline Elective-IV	3	0	0	3
6	EC-351 to EC-353	Discipline Elective-II	3	0	0	3	6	EC-381 to EC-382	Stream Core-I	2	0	0	2
7	EC-315	Digital Communication Lab	0	0	2	1	7	HS-321	Engineering Economics and Accountancy	2	0	0	2
8	EC-316	VLSI Design Lab	0	0	2	1	8	EC-324	Digital Signal Processing Lab	0	0	2	1
							9	EC-325	Antenna and Microwave Systems Lab	0	0	2	1
		Total Hours = 22				20			Total Hours = 22				20

#### Open Elective

EC-301	Microcontroller and Embedded System
EC-302	MEMS and Sensor Design
EC-303	Communication Systems
EC-304	VLSI Design and Techniques

#### Discipline Elective-IV

EC-361	Digital Image Processing
EC-362	Speech Processing
EC-363	Estimation and Detection Theory

#### Discipline Elective-II

EC-351	Switching and Finite Automata Theory
EC-352	Data Structures and Algorithms
EC-353	Object Oriented Programming

#### Stream Core-I

EC-381	Hardware Description Language
EC-382	Simulation of Communication Systems

#### Discipline Elective-III

EC-341	Data Communication and Computer Networks
EC-342	Spread Spectrum and Wideband Communication
EC-343	Satellite Communication and Radar

## Curriculum for B. Tech. Programme

Course No.	Semester 7	Credits	Course Type
EC-411 to EC-413	Engineering Courses	9	Discipline Core
EC-431 to EC-433	Engineering Courses	3	Discipline Elective
EC-451 to EC-452, EC-471 to EC-472	Engineering Courses	4	Stream Core
EC-414, EC-415	Engineering Course (Lab)	2	Discipline Core
EC-416	Engineering Courses	2	Discipline Core
	<b>Total</b>	<b>20</b>	

Course No.	Semester 8	Credits	Course Type
EC-461 to EC-469, EC-480 to EC-489	Free Elective/Engineering Course/Open Elective Course (Courses available in other departments in the even semester)	6	Free Electives/Stream Elective (offered by Department/Institute Elective (Open Elective)
EC-498	Holistic Assessment	2	Institute Core
EC-499	UG Project*	12	Discipline Core
	<b>Total</b>	<b>20</b>	

\* Students opting for internship will complete the UG project and the remaining credit requirements will be fulfilled by opting Free Elective Courses.

Fourth Year													
7 <sup>th</sup> Semester							8 <sup>th</sup> Semester						
SN	Code	Subject	L	T	P	C	SN	Code	Subject	L	T	P	C
1	EC-411	Computer Architecture and Organization	3	0	0	3	1	EC-461 to EC-469	***Stream Elective-I	3	0	0	3
2	EC-412	Wireless Communication	3	0	0	3	2	EC-480 to EC-489	***Stream Elective-II	3	0	0	3
3	EC-413	Optical Fiber Communication	3	0	0	3	3	EC-498	General Proficiency	0	0	0	2
4	EC-431 to EC-433	Discipline Elective-V	3	0	0	3	4	EC-499	UG Project	0	0	0	12
5	EC-451 to EC-452	Stream Core-II	2	0	0	2	5						
6	EC-471 to EC-472	Stream Core-III	2	0	0	2							
7	EC-414	Optical Fiber Communication Lab	0	0	2	1	6						
8	EC-415	Communication Systems and Network Simulation Lab	0	0	2	1	7						
9	EC-416	Industrial Training Evaluation*	0	0	4	2	8						
		Total Hours = 24				20			Total Hours = 06				20

\*\*\* Students opting for the internship will complete the UG project and the remaining credit requirements will be fulfilled by opting Free Elective Courses. For free electives, students may register for courses from portals like MOOCs, SWAYAM etc. The credits earned in these courses will be included in the marks sheet of the students.

\* The students should undergo vocational training during summer vacations after the sixth semester.

#### Discipline Elective-V

- EC-431      FPGA SoC Design
- EC-432      Neural Network and Deep Learning
- EC-433      VLSI Testing

#### Stream Core-II

- EC-451      Information Theory and Coding
- EC-452      Analog IC Design

#### Stream Core-III

- EC-471      Artificial Intelligence and Machine Learning
- EC-472      Embedded Systems

**Stream Elective-I**

EC-461	Applied Linear Algebra for Signal Processing, Data Analytics, and Machine Learning
EC-462	RF and Microwave Circuit Design
EC-463	Low Power VLSI Design Techniques
EC-464	VLSI Interconnects and Packaging
EC-465	Internet of Things and Applications
EC-466	Wavelet Transform and Applications
EC-467	RF Microelectronics
EC-468	Embedded Networking and Device Driver Programming
EC-469	VLSI Verification

**Stream Elective-II**

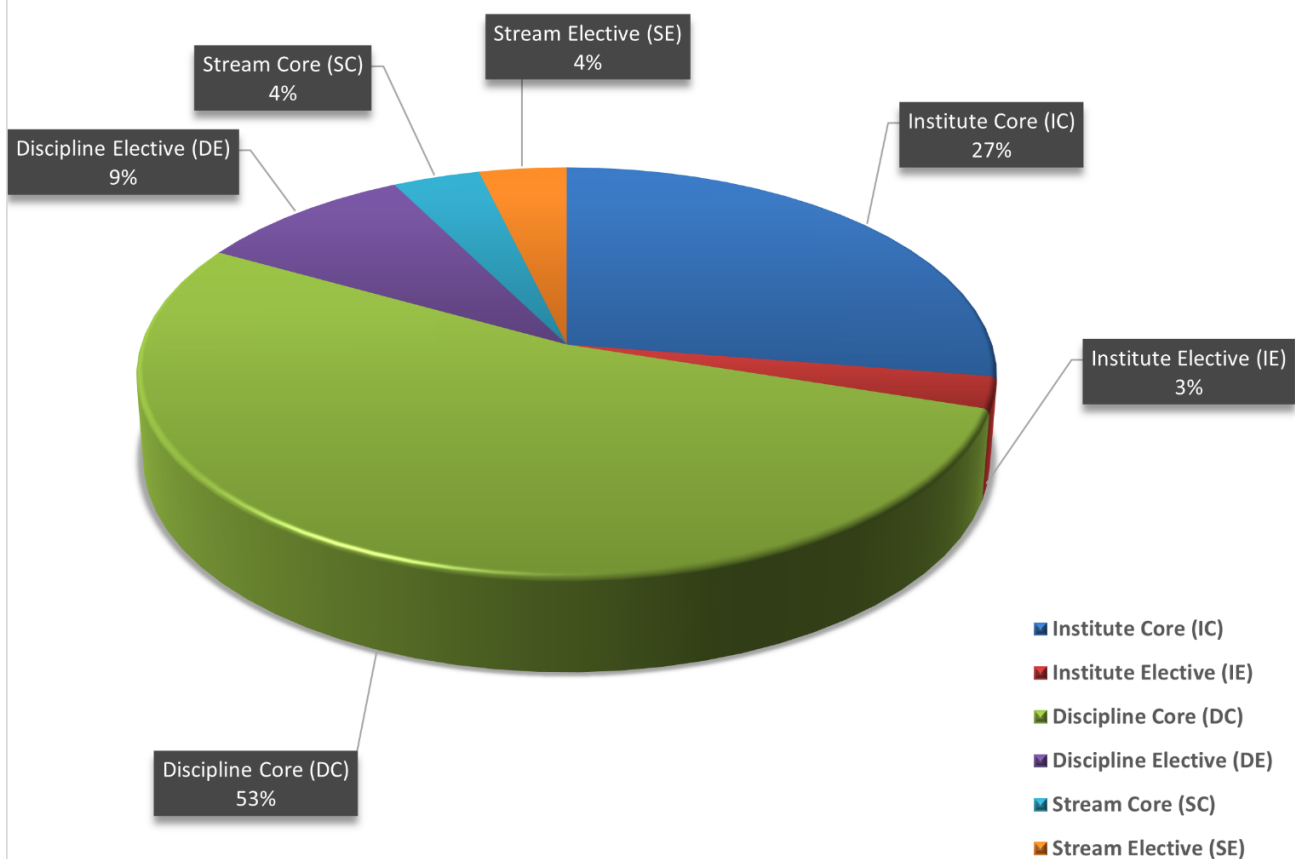
EC-480	Compound Semiconductors: Properties & Applications
EC-481	Adaptive Signal Processing
EC-482	Advanced Antenna and Electromagnetic Metamaterials: Concept and Applications
EC-483	Nano Electronics: Devices and Materials
EC-484	Characterization of Semiconductor Materials & Devices
EC-485	Embedded Hardware Design
EC-486	Electromagnetic Interference and Compatibility
EC-487	Nanophotonics and Plasmonics: Concept and Applications
EC-488	Advanced IC Design
EC-489	Electronic Packaging



## Type of Courses and Credits in Each Semester

Semester									
Type of Course	1 <sup>st</sup>	2 <sup>nd</sup>	3 <sup>rd</sup>	4 <sup>th</sup>	5 <sup>th</sup>	6 <sup>th</sup>	7 <sup>th</sup>	8 <sup>th</sup>	Total
IC	20	20	0	0	0	2	0	2	44
IE	0	0	0	1	3	0	0	4-10*	4
DC	0	0	20	16	14	10	13	12	85
DE	0	0	0	3	3	6	3	0	15
SC	0	0	0	0	0	2	4	0	6
SE	0	0	0	0	0	0	0	0-6*	6
Credits	20	20	20	20	20	20	20	20	160
									<b>Total: 160</b>
* Students are free to choose any combination out of Free Electives, IE and SE for 6 credits									

### Share of credits based on the requirement of the programme



Course Name: <b>Digital Electronics and Logic Design</b>		
Course Code: <b>EC-211</b>		
Course Type: <b>Core</b>		
Contact Hours/Week: <b>3L</b>		Course Credits: <b>03</b>
<b>Course Objectives</b> <ul style="list-style-type: none"> <li>● To impart knowledge about the concept of digital design, number system and codes.</li> <li>● To introduce the fundamental concepts related to design of combinational logic circuits.</li> <li>● To enable the students to understand the design of Sequential Circuits.</li> </ul>		
<b>Unit Number</b>	<b>Course Content</b>	<b>Contact Hours</b>
UNIT-01	<b>Introduction:</b> Analog versus Digital, Number systems, Binary Arithmetic (Addition, Subtraction, Multiplication and Division), Diminished radix and radix complements; BCD codes, Excess-3 code, Gray code, Hamming code, Error Detection and Correction. Digital to analog converter, Analog to digital converter	<b>08L</b>
UNIT-02	<b>Logic Gates and Logic Families:</b> Digital Logic Gates, characteristics of logic families, Various Logic Families: RTL, DTL, TTL, ECL, MOS and CMOS; Working and their characteristics.	<b>06L</b>
UNIT-03	<b>Combinational Logic Design:</b> Boolean Algebra, Basic Theorems and Properties of Boolean Algebra, Representation of function in SOP and POS form, NAND and NOR implementation Minimization of Logical functions: Karnaugh- Map method, Tabulation method, VEM method, Iterative Consensus & Generalized Consensus method, Design of Combinational circuits.	<b>12L</b>
UNIT-04	<b>MSI and PLD Components:</b> Binary Adder and Subtractor; Decoders and Encoders; Multiplexers and DE-Multiplexers circuits; Programmable Read Only Memory, Programmable Logic Arrays, Programmable Array Logic; Implementation of Combinatorial Logic using these devices, Semiconductor Memories.	<b>06L</b>
UNIT-05	<b>Sequential Logic Design:</b> Introduction and Classification of Sequential circuits, Latches & Flip-flops: Excitation Table of flip-flops, Interconversion of flip-flops, Design of Synchronous & Asynchronous Sequential circuits, Registers, Counters, finite state machine	<b>08L</b>
<b>Course Outcomes</b> Upon successful completion of the course, the students will be able to CO1: Understand about the concept of digital system. CO2: Apply principles of minimization techniques to simplify digital functions. CO3: Design and analyse the combinational electronic circuit based on digital logic. CO4: Design and analyse the sequential electronic circuit based on digital logic.		
<b>Books and References</b> <ol style="list-style-type: none"> <li>1. Digital Design: M. Morris Mano, Prentice Hall of India.</li> <li>2. Digital Principle and Applications: Malvino and Leach, Tata Mc-Graw Hill.</li> <li>3. Fundamentals of Digital Electronics: Anand Kumar, Prentice Hall of India.</li> <li>4. Modern Digital Electronic: R.P.Jain Tata Mc-Graw Hill.</li> <li>5. Digital Systems: Principles and Applications R. J. Tocci, Prentice Hall</li> </ol>		

Course Name: <b>Analog Electronics</b>		
Course Code: <b>EC-212</b>		
Course Type: <b>Core</b>		
Contact Hours/Week: <b>3L</b>		Course Credits: <b>03</b>
<b>Course Objectives</b> <ul style="list-style-type: none"> <li>● To introduce the fundamental concepts relevant to bipolar junction transistor.</li> <li>● To impart knowledge about the electrical modeling and analysis of small- and large-signal amplifiers.</li> <li>● To enable the students to understand the factors that cause the gain to roll-off at high frequencies.</li> </ul>		
<b>Unit Number</b>	<b>Course Content</b>	<b>Contact Hours</b>
UNIT-01	<b>Low Frequency Transistor Amplifier:</b> Equivalent Circuit of BJT using h-parameter for CB, CE and CC & configuration, Calculation of Transistor Parameter for CB, CE & CC using h-parameters, Comparison of Transistor Amplifier Configuration	<b>05L</b>
UNIT-02	<b>Multistage Amplifier:</b> General Cascaded System, RC Coupled Amplifier and its Frequency Response, Merits and Demerits, Cascade Amplifier, Darlington Compound Configuration, Multistage Frequency Effect	<b>05L</b>
UNIT-03	<b>High Frequency Response of Transistor Amplifier:</b> High Frequency Model for CE Configuration, Approximate CE High Frequency Model with Resistive Load, CE Short Circuit Current Gain, HF Current Gain with Resistive Load	<b>06L</b>
UNIT-04	<b>Large Signal Amplifier:</b> Analysis and Design of class A, B, AB, C Amplifiers, Push-pull Amplifiers, Transformer Less Output Stages, Distortion Calculations	<b>05L</b>
UNIT-05	<b>Tuned Amplifier:</b> General Behavior of Tuned Amplifiers, Series and Parallel Resonant Circuit, Calculations of Circuit Impedance at Resonance, Variation of Impedance with Frequency, Q Factor of a Circuit & Coil, Bandwidth of Series and Parallel Resonant Circuit	<b>05L</b>
UNIT-06	<b>Feedback Amplifier:</b> Feedback concept, Characteristics of Negative and Positive Feedback, Effect of Negative and Positive Feedback on Input Impedance, Output Impedance, Gain, Noise and Frequency Response	<b>05L</b>
UNIT-07	<b>Oscillators:</b> Classification of Oscillators, Frequency Stability of Oscillatory Circuits, Hartley Oscillator, Colpitt Oscillators, Clapp Oscillator, Crystal Oscillator, Phase Shift Oscillator and Wien Bridge Oscillator	<b>05L</b>
<b>Course Outcomes</b> Upon successful completion of the course, the students will be able to CO1: Develop the ability to analyze and design analog electronic circuits using discrete components. CO2: Understand the use of small-signal models to predict gain and behavior in transistor amplifier. CO3: Describe the design trade-offs in analog amplifier circuits. CO4: Design tuned amplifiers and apply them in a communications system.		
<b>Books and References</b> <ol style="list-style-type: none"> <li>1. Integrated Electronics: Analog and Digital Circuits and Systems by J. Millman and C. Halkias, McGraw-Hill, Inc.</li> <li>2. Electronic Devices &amp; Circuit Theory by R. Boylestad and L. Nashelsky, Pearson.</li> <li>3. Microelectronic Circuits by A. Sedra and K. Smith, Oxford University Press.</li> <li>4. Electronic Fundamental Applications: Integrated and Discrete Systems by J.D. Ryder, Prentice Hall.</li> </ol>		

Course Name: <b>Communication Theory</b>		
Course Code: <b>EC-213</b>		
Course Type: <b>Core</b>		
Contact Hours/Week: <b>3L</b>		Course Credits: <b>03</b>
<b>Course Objectives</b> <ul style="list-style-type: none"> <li>• To understand the basic components of communication systems.</li> <li>• To prepare the mathematical background for communication signal analysis.</li> <li>• To analyze signals in the presence of various types of noise and estimation of channel capacity.</li> </ul>		
Unit Number	Course Content	Contact Hours
UNIT-01	<b>Frequency and Time Domain Representation and Analysis:</b> Introduction to Information, Messages & Signals, Classification of Signals, The Discrete and Continuous Spectrum, Power Spectrum, Energy Density Spectrum, Dirac Delta Functions, Sampling Theory and Approximations, Convolution of Signals, LTI Systems.	<b>10L</b>
UNIT-02	<b>Random Signal Theory:</b> Discrete Probability Theory, Continuous Random Variables, Statistically Independent Random Variables, Probability Density Functions of Sums, Transformation of Density Functions, Ergodic Process, Correlation Functions, Spectral Density, and White Noise.	<b>10L</b>
UNIT-03	<b>Noise:</b> Atmospheric, Thermal, Shot and Partition noise, Noise Figure and Experimental Determination of Noise Figure, Shot Noise in Temperature Limited Diodes and Space Charge Limited Diodes, Pulse Response, and Digital Noise.	<b>05L</b>
UNIT-04	<b>Transmission Through Networks:</b> Networks with Random Input, Auto-correlations, Spectral Density and Probability Density Input-output Relationships, Optimum System and Non-linear Systems, Maximum Criterion, Equivalent Noise Bandwidth.	<b>05L</b>
UNIT-05	<b>Basic Information Theory:</b> Definition of Information, Units of Information, Entropy, Uncertainty and Information Rate of Communication, Redundancy, Relation Between System Capacity and Information Content of Messages, Shannon's Theorem, Discrete Noisy Channel, Channel Capacity for Different Discrete Channels.	<b>06L</b>
<b>Course Outcomes</b> Upon successful completion of the course, the students will be able to CO1: Perform the time and frequency domain analysis of the signals in a communication system. CO2: Analyze the performance of communication system and the need of information theory for information transfer. CO3: Select the blocks in a design of communication system and system capacity.		
<b>Books and References</b> <ol style="list-style-type: none"> <li>1. Elements of Communication Theory by J. C. Hancock, McGraw-Hill Education Publisher.</li> <li>2. Principles of Communication System by Taub &amp; Schilling, McGraw-Hill Education Publisher.</li> <li>3. Communication Systems by S. Haykin, Wiley Publication.</li> </ol>		

Course Name: <b>VLSI Technology</b>		
Course Code: <b>EC-214</b>		
Course Type: <b>Core</b>		
Contact Hours/Week: <b>3L</b>		Course Credits: <b>03</b>
<b>Course Objectives</b> <ul style="list-style-type: none"> <li>To impart knowledge about the miniaturization of Electronic Systems.</li> <li>To introduce the fundamental concepts relevant to VLSI fabrication.</li> <li>To enable the students to understand the various VLSI fabrication techniques.</li> </ul>		
Unit Number	Course Content	Contact Hours
UNIT-01	<b>Introduction to VLSI:</b> Concept Miniaturization of Electronic Systems & its impact on characterization.	<b>03L</b>
UNIT-02	<p><b>Monolithic Fabrication Techniques:</b> Crystal growth: Source of silicon; Single crystalline and Poly crystalline; Requirement of purity for electronics industry; Electronics grade silicon production; Crystal growth techniques: Bridgeman method, Float zone method, Czochralski method, Modified Czochralski method; refining; Silicon Wafer Preparation &amp; Crystal Defects.</p> <p><b>Epitaxial Process:</b> Need of epitaxial layer; vapors phase epitaxy -reactor design, Chemistry of epitaxial process, Transport mechanism doping &amp; auto doping; selective epitaxy, Epitaxial process induced defects, Molecular beam epitaxy, Merits and demerits among epitaxial processes; recent trends in Epitaxy.</p> <p><b>Oxidation:</b> Importance of oxidation; types of oxidation techniques; growth mechanism &amp; kinetics; factors affecting the growth mechanisms; silicon oxidation model, dry &amp; wet oxidation; oxidation induced faults; recent trends in oxidation.</p> <p><b>Lithography:</b> Basic steps in lithography; Lithography techniques-optical lithography, Electron beam lithography, X-ray lithography, Ion beam lithography; resists and mask preparation of respective lithographies, Printing techniques-contact, Proximity printing and projection printing; merits and demerits of lithographies; recent trends in lithography at nano regime.</p> <p><b>Etching:</b> Performance metrics of etching; types of etching- wet and dry etching; dry etching techniques-ion beam or ion-milling, Sputter ion plasma etching and reactive ion etching (RIE); merits and demerits of etching; etching induced defects; recent trends in epitaxy.</p> <p><b>Diffusion and Ion Implantation:</b> Diffusion mechanisms; diffusion reactor; diffusion profile; diffusion kinetics; parameters affecting diffusion profile; Dopants and their behavior, choice of dopants; Ion Implantation- reactor design, impurity distribution profile, Properties of ion implantation, Low energy and high energy ion implantation.</p> <p><b>Metallization:</b> Desired properties of metallization for VLSI; metallization choices; metallization techniques — vacuum evaporation, Sputtering.</p>	<b>21L</b>
UNIT-03	<b>Packaging of VLSI Chip:</b> Introduction to packaging; packaging process; package design considerations, Various packages types.	<b>04L</b>
UNIT-04	<b>Isolation Techniques in Monolithic Components:</b> Isolation techniques in Diodes, BJT and MOSFETs (Enhancement and depletion mode)	<b>04L</b>
UNIT-05	<b>Monolithic Components- Prototype Fabrication:</b> Prototype fabrication of Diodes, npn BJT, pnp BJT, MOSFETs (Enhancement and depletion mode), n-MOS, p-MOS, CMOS, Resistors and Capacitors.	<b>04L</b>
<b>Course Outcomes</b> Upon successful completion of the course, the students will be able to CO1: Identify the material properties and ambient conditions for chips fabrication. CO2: Describe the analysis of technology scaling. CO3: Understand the complexities involved in the integrated circuits. CO4: Apply principles to Identify and Analyze the various steps for the fabrication of various components CO5: Assess the various reliability issues in VLSI technology.		
<b>Books and References</b> <ol style="list-style-type: none"> <li>VLSI Technology by S.M. Sze, Tata Mc-Graw Hill</li> <li>VLSI Fabrication Principles by S.K. Gandhi, John Wiley &amp; Sons</li> <li>Integrated Circuits by K. R. Botkar, Khanna Publishers</li> <li>Micromachined Transducer by G.T.A. Kovacs, McGraw Hill, 1998</li> <li>Principles of Microelectronics Technology by D. Nagchoudhary, PHI</li> </ol>		

Course Name: <b>Network Analysis and Synthesis</b> Course Code: <b>EC-215</b> Course Type: <b>Core</b>		
Contact Hours/Week: <b>2L</b>		Course Credits: <b>02</b>
<b>Course Objectives</b> The basic objective of this course is to introduce the fundamental theory and mathematics for the analysis of electrical circuits, frequency response and transfer function of circuits. The students will be able extend these fundamental principles into a way of thinking for problem solving in mathematics, science, and engineering.		
<b>Unit Number</b>	<b>Course Content</b>	<b>Contact Hours</b>
UNIT-01	<b>NETWORK THEOREMS</b> Superposition and Reciprocity theorem, Thevenin's and Norton's theorem, Millman's theorem, maximum power transfer theorem, compensation, Tellegan's theorem, analysis of circuits using theorems.	07L
UNIT-02	<b>TRANSIENT ANALYSIS OF NETWORKS</b> Network elements, Transient response of R-L, R-C, R-L-C for DC and sinusoidal excitation, Initial condition, Solution using differential equation approach and Laplace transform method.	07L
UNIT-03	<b>NETWORK ANALYSIS</b> State variable method, Analytic and numerical solutions, Two Port Networks, Graph theoretic analysis for large scale networks, Formulation and solution of network graph of simple networks, State space representation, Analysis using PSPICE.	07L
UNIT-04	<b>NETWORK SYNTHESIS</b> Network realizability, Causality and stability, Hurwitz Polynomials, Positive real functions, Properties of RC, RL & LC networks, Foster and Cauer forms of realization, Transmission zeroes, Synthesis of transfer functions.	07L
<b>Course Outcomes</b> Upon successful completion of the course, the students will be able to CO1: Apply different network theorems to solve complex circuit problems. CO2: Identify the transient and steady state response of different types of circuits. CO3: Solve network problems using graphical method. CO4: Realise the network for a given response		
<b>Books and References</b> 1. Network Analysis and Synthesis by <u>Franklin F. Kuo</u> , Wiley; Second edition, 2006 2. Network Analysis, by M. E. V. Valkenburg, Pearson, 3rd edition., 2015. 3. Network and systems by D.Roy – Choudhary, New Age; Second edition 2013		

Course Name: <b>Engineering Mathematics-III</b>		
Course Code: <b>MA-203</b>		
Course Type: <b>Core</b>		
Contact Hours/Week: <b>3L</b>		Course Credits: <b>03</b>
<b>Course Objectives</b> <ul style="list-style-type: none"> <li>To introduce the fundamental concepts relevant to function of complex variable, numerical differentiation and integration and numerical solution of linear, non-linear and system of equations.</li> <li>To have the idea of evaluation of real integrals using complex variable.</li> <li>To understand the concept of approximating &amp; interpolating polynomials and finding values of function at arbitrary point.</li> <li>To impart knowledge of various numerical technique to solve ODE.</li> </ul>		
Unit Number	Course Content	Contact Hours
UNIT-01	<b>Functions of Complex Variable</b> Applications of De Moivre's theorem, Exponential, Circular, Hyperbolic and Logarithmic functions of a complex variable, Inverse Hyperbolic functions, Real and imaginary parts of Circular and Hyperbolic functions, Summation of the series-'C+iS' method. Limit and derivative of complex functions, Cauchy-Riemann equations, Analytic functions and its applications, Complex integration, Cauchy's theorem, Cauchy's integral formula, Series of complex function, Taylor series, singularities and Laurent's series, Cauchy's residue theorem and its application for the evaluation of real definite integrals.	12L
UNIT-02	<b>Interpolation</b> Least square curve fit and trigonometric approximations, Finite differences and difference operators, Newton's interpolation formulae, Gauss forward and backward formulae, Sterling and Bessel's formulae, Lagrange's interpolation.	06L
UNIT-03	<b>Numerical Integration</b> Integration by trapezoidal and Simpson's rules 1/3 and 3/8 rule, Romberg integration, and Gaussian quadrature rule, Numerical integration of function of two variables.	05L
UNIT-04	<b>Numerical Solution of Ordinary Differential Equations</b> Taylor series method, Picard's method, Euler's method, Modified Euler's method, Runge-Kutta method. Predictor corrector methods, Adam Bashforth and Milne's method, convergence criteria, Finite difference method.	07L
UNIT-05	<b>Numerical Solution of Linear and Non Linear Equations</b> Non Linear Equations: Bisection Method, Regula Falsi Method, Newton-Raphson Method, Iteration method. Linear Equations: Jacobi and Gauss Seidal Iteration methods, Relaxation method.	06 L
<b>Course Outcomes</b> Upon successful completion of the course, the student will be able to: CO1: Understand and analyze the concept of Numerical Solution of Linear and Non-Linear Equations, Ordinary Differential Equations and Function of complex variables. CO2: Identify an appropriate technique to solve the linear, non-linear equations, ordinary differential equations. CO3: Formulate the problems on related topics and solve analytically. CO4: Apply the concepts of linear, non-linear equations, differential equations and complex analysis in various engineering problems. CO5: Demonstrate the concepts through examples and applications.		
<b>Books and References</b> <ol style="list-style-type: none"> <li>Complex variables and Applications: by R. V. Churchill, T. J. Brown &amp; R. F. Verhey, McGraw Hill.</li> <li>A first course in complex analysis with applications: by Dennis D. Zill &amp; P. D. Shanahan, Jones and Bartlett.</li> <li>Numerical Methods for Scientific and Engineering Computations: by M. K. Jain, S. R. K. Iyenger and R. K. Jain, New Age International Publishers, New Delhi</li> <li>Numerical Methods for Engineers and Scientists (2<sup>nd</sup> Ed.): by J D Hoffman, CRC Press.</li> <li>Numerical Analysis Mathematics and Scientific computing (3<sup>rd</sup> ed.): by D. Kincaid and W. Cheney, American Mathematical Society.</li> </ol>		

Course Name: **Digital Electronics and Logic Design Lab**  
Course Code: **EC-216**

Contact Hours/Week: **2P**

Course Credits: **01**

**Course Objectives**

- Familiarization with digital integrated circuits and equipment.
- Implementation and design of combinational logic circuits using different gates.
- To understand concepts of sequential circuits and to analyze and design sequential circuits.

**List of Experiments**

1. To study about the logic gates and verify their truth table.
2. Realization of AND and OR gates using
  - (i) Diodes and resistors.
  - (ii) Universal gates
3. Design and implement half adder and full adder circuits and verifies the truth table using logic gates.
4. Design and implement half subtractor and full subtractor circuits and verifies the truth table using logic gates.
5. Design and implement 4-bit binary to gray code converter and gray to binary code converter circuits.
6. Design and implement BCD to excess-3 code converter and excess-3 to BCD code converter.
7. Design and implement
  - (i) 2-Bit magnitude comparator using basic gates
  - (ii) 8-Bit magnitude comparator using IC 7485
8. Design and implement multiplexer and demultiplexer using logic gates and study of IC 74150 and IC 74154.
9. Design and implementation of the function using multiplexer
  - (i)  $F(A,B,C)=\sum m(1,2,5,6)$
  - (ii)  $F(A,B,C)=\sum m(0,2,5,6,7)$
10. Design and implement encoder and decoder using logic gates and study of IC 7445 and IC 74147.
11. Realization of SR, JK, D and T flip flop using gates.
12. Design and implement 3-bit synchronous up counter.
13. Design and implement 3-bit asynchronous up/down counter.
14. Design BCD to seven segment display with decoder Using IC 7447.

**Course Outcomes**

Upon successful completion of the course, the students will be able to

- CO1: Understand the digital signals, applications of ICs and logic circuits.
- CO2: Develop skills for designing combinational logic circuits and their practical implementation on breadboard.
- CO3: Analyze, design and implement sequential logic circuits.



Course Name: <b>Analog Electronics Lab</b>	
Course Code: <b>EC-217</b>	
Contact Hours/Week: <b>2P</b>	Course Credits: <b>01</b>
<b>Course Objectives</b> <ul style="list-style-type: none"> <li>• To provide skills for designing various oscillator circuits</li> <li>• To provide skills for understanding frequency stability in amplifiers</li> <li>• To enable the students to plot the characteristic property of various transducers</li> </ul>	
<ol style="list-style-type: none"> <li>1. To study the working of Hartley Oscillator and measure the frequency of oscillations</li> <li>2. To study the working of Colpit's Oscillator and measure the frequency of oscillations</li> <li>3. To study the functioning of Crystal Oscillator and measure the frequency of oscillations</li> <li>4. To study the frequency response of two-stage RC coupled amplifier and find the voltage gain</li> <li>5. To identify the type of feedback used in an amplifier and determine the voltage gain</li> <li>6. To study the push-pull amplifier and plot the frequency response</li> <li>7. To study the transformer coupled amplifier and determine the frequency response</li> <li>8. To study the voltage gain and frequency response of FET amplifier</li> <li>9. To study the astable, monostable and bistable multivibrators and their timing parameters.</li> </ol> <p>Note: The concerned Course Coordinator may prepare the actual list of experiments/problems at the start of semester based on above generic list.</p>	
<b>Course Outcomes</b> Upon successful completion of the course, the students will be able to CO1: Analyze and design analog electronic circuits using discrete components CO2: Design and implement an analog circuit project application utilizing knowledge and skills learned CO3: Establish the biasing of an FET amplifier CO4: Calculate power efficiency of large-signal amplifier	

Course Name: **Electronics Workshop**  
Course Code: **EC-218**

Contact Hours/Week: **2P**

Course Credits: **01**

**Course Objectives**

- To understand basic electronic components and hardware systems.
- Familiarization and hands-on with various instruments and tools used in electronic system repair and service.
- Hands-on training with familiarization, identification, testing, assembling, dismantling, fabrication and repairing electronic systems.

**List of Experiments**

1. Familiarization/Identification of electronic components with specification (Functionality, type, size, color coding, package, symbol, cost etc. [Active, Passive, Electrical, Electronic, Electro-mechanical, Wires, Cables, Connectors, Fuses, Switches, Relays, Crystals, Displays, Fasteners, Heat sink etc.])Pulse Code Modulation & Demodulation.
2. Drawing of electronic circuit diagrams using BIS/IEEE symbols and introduction to EDA tools, Interpret data sheets of discrete components and IC's, Estimation and costing.
3. Familiarization/Application of testing instruments and commonly used tools and instruments such as : Multimeter, Function generator, Power supply, CRO, Soldering iron, De-soldering pump, Pliers, Cutters, Wire strippers, Screw drivers, Tweezers, Crimping tool, Hot air soldering and de-soldering station etc.
4. Testing of electronic components [Resistor, Capacitor, Diode, Transistor, UJT and JFET using multimeter.
5. Inter-connection methods and soldering practice. [Bread board, Wrapping, Crimping, Soldering - types - selection of materials and safety precautions, soldering practice in connectors and general purpose PCB, Crimping.]
6. Printed circuit boards (PCB) [Types, Single sided, Double sided, PTH, Processing methods, Design and fabrication of a single sided PCB for a simple circuit with manual etching (Ferric chloride) and drilling.]
7. Mini Project: Designing and assembling of any electronic circuit/system on general purpose PCB, test and show the functioning.

**Note:** The concerned Course Coordinator will prepare the actual list of experiments/problems at the start of semester based on above generic list.

**Course Outcomes**

Upon successful completion of the course, the students will be able to

CO1: Identify and test the active and passive electronic components

CO2: Test, assemble, dis-assemble, test and repair electronic hardware systems.

CO3: Use the electronic workshop tools efficiently and effectively.

Course Name: <b>Linear Integrated Circuits</b>		
Course Code: <b>EC-221</b>		
Course Type: <b>Core</b>		
Contact Hours/Week: <b>3L</b>		Course Credits: <b>03</b>
<b>Course Objectives</b> <ul style="list-style-type: none"> <li>To impart strong foundation of IC based design.</li> <li>To introduce the various applications of operational amplifiers and its integration with other devices.</li> <li>To learn circuits design using op amps for power management, signal conditioning and communication</li> </ul>		
<b>Unit Number</b>	<b>Course Content</b>	<b>Contact Hours</b>
UNIT-01	<b>Differential And Cascode Amplifiers</b> Emitter coupled differential amplifiers & its circuit configurations, FET differential amplifier, Differential amplifier with swamping resistor, constant current bias & current mirror. Cascade differential amplifier stages. Level translator, Cascode configuration	8L
UNIT-02	<b>Introduction to Operational Amplifiers</b> The basic operational amplifier & its schematic symbol, Block diagram representation of OP-AMP, Power supply requirements of an OP-AMP, Evolution of OP-AMP, Specification of a typical OP-AMP(741).	3L
UNIT-03	<b>The Practical Op-Amp And Its Frequency Response</b> Input offset voltage, input bias current, input offset current. Total output offset voltage, thermal drift, error voltage, variation of OP-AMP parameter with temperature & supply voltage. Supply voltage rejection ration (SVRR), CMRR-Measurement of OP-AMP parameters, Frequency response of compensator networks, Open loop voltage gain as a function of frequency, Slew rate, causes of slew rates and its effects in application	6L
UNIT-04	<b>Operational Amplifier Configurations &amp; Linear Application</b> Open loop OP-AMP configurations- The differential amplifier, inverting amplifier, non-inverting amplifier, negative feedback configurations - inverting and non-inverting amplifiers, voltage followers & high input impedance configuration, differential amplifiers, closed loop frequency response & circuit stability, single supply operation of OP-AMP, summing, scaling and averaging amplifier, voltage to current & current to voltage converters, integrators & differentiators, logarithmic & anti logarithmic amplifiers	6L
UNIT-05	<b>Active Filters &amp; Oscillators</b> Advantages of active filters, classification of filters, response characteristics of butter worth, chebyshev, causal filters, first order and second order butter worth filters- low pass and high pass types. Band pass & band reject filters. Oscillator principles, types of oscillators - phase shift, Wien Bridge & quadrature. Square wave, triangular wave and saw tooth wave generators, voltage controlled oscillator	5L
UNIT-06	<b>Comparators &amp; Converters</b> Basic comparator & its characteristics, zero crossing detector, voltage limiters, clippers & clampers, small signal half wave & full wave rectifiers, sample and hold circuit, ADC, DAC	5L
UNIT-07	<b>Voltage Regulators</b> Fixed Voltage Regulator, Adjustable voltage regulators, Switching regulators, special regulators	3L
<b>Course Outcomes</b> Upon successful completion of this course students will be able to : CO1: Understand and design the basic circuits using op-amp and perform operations and their troubleshooting CO2: To learn how to detect, amplify, store, create and manipulate signals using operational amplifiers\ CO3: To design and analyze the responses of IC based designed circuits in the area of power management, signal conditioning, analog and digital communication CO4: To develop IC based projects in the above areas		
<b>Books and References</b> <ol style="list-style-type: none"> <li>OP-AMP and Linear Integrated Circuits, Ramakant A. Gayakwad, PHI Publication.</li> <li>Design with Operation Amplifiers and Analog Integrated circuits, Sergei Franco, TMH.</li> <li>Integrated Electronics: Analog and Digital Circuits &amp; System, Millman &amp; Halkias, TMH.</li> <li>Linear Integrated Circuits, D. Choudhari, S. Jain, New Age International limited.</li> </ol>		

Course Name: <b>Analog Communication Systems</b>		
Course Code: <b>EC-222</b>		
Course Type: <b>Core</b>		
Contact Hours/Week: <b>3L</b>		Course Credits: <b>03</b>
<b>Course Objectives</b> <ul style="list-style-type: none"> <li>To introduce the concepts of analog communication systems.</li> <li>To equip students with various issues related to analog communication such as modulation, demodulation, transmitters and receivers and noise performance.</li> <li>Differentiate between different modulation techniques and necessities of the same.</li> </ul>		
<b>Unit Number</b>	<b>Course Content</b>	<b>Contact Hours</b>
UNIT-01	Modulation Techniques Various Frequency Bands Used for Communication, Types of Communication and Need of Modulation. Introduction to AM, FM, PM, Frequency Spectrum of AM Waves, Representation of AM, Power Relation in AM Waves, Need and Description of SSB, Suppression of Carrier, Suppression of Unwanted Sidebands, Independent Sideband System, Vestigial Sideband System, Mathematical Representation of FM, Frequency Spectrum of AM Waves, Phase Modulation, Comparison Between Analog and Digital Modulation, Wideband and Narrow Band FM.	<b>08L</b>
UNIT-02	AM Transmitters and Receivers AM Transmitters: Generation of AM, Low Level and High Level Modulation, AM Transmitter Block Diagram, AM Modulator circuits, DSB S/C Modulator. AM Receiver: Tuned Radio Frequency (TRF) Receiver. Super Heterodyne Receiver, RF Section and Characteristics, Mixers, Frequency Changing and Tracking, IF Rejection and IF Amplifiers. Detection and Automatic Gain Control (AGC), AM Receiver Characteristics.	<b>07L</b>
UNIT-03	FM Transmitters and Receivers FM Transmitters: Basic Requirements and Generation of FM, FM Modulation Methods: Direct Methods, Variable Capacitor Modulator, Varactor Diode Modulator, FET Reactance Modulator, Transistor Reactance Modulator, Pre-emphasis, Direct FM Modulator, AFC in Reactance Modulator, Disadvantages of Direct Method, Indirect Modulators, RC Phase Shift Modulators, Armstrong FM Systems. FM Receivers: Limiters, Single and Double-Tuned Demodulators, Balanced Slope Detector, Foster-Seeley or Phase Discriminator, De-emphasis, Ratio Detector, Block Diagram of FM Receivers, RF Amplifiers, FM Receiver Characteristics.	<b>10L</b>
UNIT-04	SSB Transmitters and Receivers Generator of SSB, Balanced Modulator Circuit, Filter Method, Phase Shift Method, Third Method, Phase Cancellation Method, Demodulation of SSB, Product Demodulator, Diode Detection Technique of SSB.	<b>05L</b>
UNIT-05	Pulse Modulation Techniques Pulse Amplitude Modulation and Demodulation, Pulse Width Modulation and Demodulation, Pulse Position Modulation and Demodulation, Sampling Theorem, Time Division Multiplexing, Frequency Division Multiplexing.	<b>05L</b>
<b>Course Outcomes</b> Upon successful completion of the course, the students will be able to CO1: Differentiate AM and FM transmission. CO2: To analyse various methods of base band /band pass analog transmission and detection. CO3: Gain the knowledge of components of analog communication system		
<b>Books and References</b> <ol style="list-style-type: none"> <li>Electronic communication Systems by G. Kennedy, McGraw-Hill Education Publisher.</li> <li>Principals of Communication System by Taub &amp; Schilling, McGraw-Hill Education Publisher.</li> <li>Electronic communication Systems by S. Haykin, Wiley India Pvt. Limited Publisher.</li> </ol>		

Course Name: <b>Microprocessors and Microcontrollers</b>		
Course Code: <b>EC-223</b>		
Course Type: <b>Core</b>		
Contact Hours/Week: <b>3L</b>		Course Credits: <b>03</b>
<b>Course Objectives</b> To give an in-depth understanding of the architectures of 8085 and 8051 Chips. Knowledge of this course will enable the students to design and develop the 8085 and 8051 based applications in real time.		
Unit Number	Course Content	Contact Hours
UNIT-01	<b>8085 ARCHITECTURE AND ORGANISATION</b> Introduction to Microprocessor, Microcomputer system, Microprocessor operations, Internal Architecture of 8085, System Bus, Pin description of 8085, Need and generation of control signals, types of Registers & Timing and Control Unit.	05L
UNIT-02	<b>INSTRUCTION SET OF 8085</b> Instruction formats, addressing Modes, Timing effect of Addressing modes, Instruction set classification, Instruction Cycle, Machine cycles, Timing diagram, Stack and Subroutine, Interrupt types, interrupt systems and polling, Interrupt control logic, Assembly language programming.	06L
UNIT-03	<b>MEMORY AND I/O DEVICES INTERFACE</b> Serial and Parallel communication interface, Hardware (Circuit level) description of Registers, RAM, ROM and Secondary memories, DMA controller, memory mapped I/O & I/O mapped I/O, Generating Control Signals, Interfacing 2KX8 EPROM, 2KX8 RAM, Interfacing I/O ports to 8085, Hand shake Signals, Block diagram and working of PPI-8255, Interfacing 8255 to 8085 and LED Interface.	06L
UNIT-04	<b>INTRODUCTION TO 8051 MICROCONTROLLER</b> Comparison of Microprocessors and Microcontrollers, Architecture of 8051, Special Function & General Purpose Registers, Pin diagram of 8051, Instruction set, addressing modes, Role of PC and DPTR, Flags and PSW, CPU, Registers, RAM and ROM organization, Special Function Registers, I/O pins, ports and circuits, External memory, Counter and Timers, Serial Transmission and Interrupts programming.	06L
UNIT-05	<b>TIMER / COUNTER AND SERIAL COMMUNICATION IN 8051</b> Timer registers - Timer0, Timer1. Configuration of TMOD, TCON registers. Timer Mode1 and Mode2 programming. Counter mode. Serial communication modes and protocols, RS-232 pin configuration and connection. Serial port programming (Transmitting a character, and receiving a character using serial communication) using Assembly.	06L
UNIT-06	<b>PROGRAMMING AND APPLICATIONS OF 8051</b> Assembly language programming, Jump Loop and Call Instructions, I/O Port Programming, Addressing Modes, Arithmetical and Logical Instructions, Stepper Motor Interfacing, DC motor interfacing, PWM generation using 8051.	05L
<b>Course Outcomes</b> Upon successful completion of the course, the students will be able to CO1: Understand the architecture cum programming models of 8085 and 8051 CO2: Know about the instruction set of 8085 and 8051. CO3: Learn the use of instruction sets in Assembly language programming of 8085 and 8051.		
<b>Books and References</b> <ol style="list-style-type: none"> <li>1. Microprocessor Architecture, Programming &amp; application with 8085 by Ramesh Gaonkar, Fifth Edition, Penram Publications</li> <li>2. The 8051 Microcontroller and Embedded Systems" by Mohammad Ali Mazidi and Janice Gillispie Maszidi, Pearson education, 2003, ISBN- 9788131710265.</li> <li>3. Microprocessors and Microcontrollers by Sunil Mathur and Jeebananda Panda, PHI.</li> </ol>		

Course Name: <b>Electromagnetic Field Theory</b>		
Course Code: <b>EC-224</b>		
Course Type: <b>Core</b>		
Contact Hours/Week: <b>3L+1T</b>		Course Credits: <b>04</b>
<b>Course Objectives</b> <ul style="list-style-type: none"> <li>• To understand the basic concepts of vector analysis, co-ordinate transformation and space derivative.</li> <li>• To introduce the fundamental concepts relevant to the electrostatic field and application of Gauss' law.</li> <li>• To introduce the fundamental concepts relevant to the magnetostatic field and application of Biot-Savart's law.</li> <li>• To introduce the concept of Maxwell's equations and how electromagnetic wave propagates.</li> <li>• To give the understating of wave propagation in guided media.</li> </ul>		
<b>Unit Number</b>	<b>Course Content</b>	<b>Contact Hours</b>
UNIT-01	<b>INTRODUCTION</b> Fundamental of vector algebra, Scalar & vector fields, Introduction and transformation on different coordinate systems: rectangular, cylindrical and spherical coordinate system, introduction to line, Surface and volume integrals, Definition of gradient, Divergent and curl of a vector and their physical significance.	07L
UNIT-02	<b>ELECTROSTATICS</b> Principle of Coulomb's law, Definition of electric field intensity from point charges, Field due to continuous distribution of charges on an infinite and finite line, Electric Field due to an infinite uniformly charged sheet, Gauss's law and its applications, Electric flux density, Potential fields duo to electric dipole, Laplace's and Poison's equations	08L
UNIT-03	<b>MAGNETOSTATICS</b> Definition and explanation on Magnetic Field intensity due to a finite and infinite wire carrying current, Magnetic field intensity on rectangular loop carrying current, Ampere's Circuital law and its applications, Biot-Savart's law, Lorentz force equation for a moving charge, Magnetic Vector Potential	08L
UNIT-04	<b>TIME VARYING EM FIELD</b> Maxwell's equation in differential and integral vector form and their interpretations, Continuity of currents, Conduction and displacement current, Boundary conditions, Helmholtz equations, uniform plane wave in dielectric and conductor media, Skin effect and depth of penetration, reflection and refraction of plane waves at boundaries for normal incidence and surface impedance, Energy Flow and Poynting theorem	10L
UNIT-05	<b>TRANSMISSION LINE</b> Transmission line model, Parameters and properties of transmission line equations, Reflections in transmission lines, Voltage, current and impedance relations-open, Short circuit and matched lines, Standing wave ratio, Impedance matching, Quarter and half wave lines, Single stub and double stub matching.	09L
<b>Course Outcomes</b> Upon successful completion of the course, the students will be able to CO1: Describe the fields using vector algebra and filed transformation from one co-ordinate system to other CO2: Describe the force between charges and equipotential surfaces and electrostatic shielding/ screening. CO3: Describe the magnetic field due to a current element and force on a charge particle due to magnetic field. CO4: Describe the electromagnetic wave phenomenon and power carried by an electromagnetic wave. CO5: Understand how the transmission line theory bridges the gap between circuit theory and field theory.		
<b>Books and References</b> <ol style="list-style-type: none"> <li>1. Elements of Engineering Electromagnetics by Matthew N.O. Sadiku, Oxford University Press.</li> <li>2. Electromagnetic Waves and Radiating Systems by Jordan and Balmain, PHI, Second Ed.</li> <li>3. Engineering Electromagnetics by William Hayt, TATA McGraw-Hill.</li> <li>4. Electromagnetic by J.D. Kraus, McGraw-Hill.</li> </ol>		

Course Name: <b>Linear Integrated Circuits Lab</b>	
Course Code: <b>EC-225</b>	
Contact Hours/Week: <b>2P</b>	Course Credits: <b>01</b>
<b>Course Objectives</b> <ul style="list-style-type: none"> <li>● To learn practical applications of operational amplifier.</li> <li>● To design and develop circuits using operational amplifiers.</li> <li>● To learn how to detect, amplify, store, create and manipulate signals using operational amplifiers.</li> </ul>	
<b>List of Experiments</b> <ol style="list-style-type: none"> <li>1. To demonstrate the relationship between input and output in for the inverting and non-inverting configuration of the Op-Amp. 741.</li> <li>2. To verify the function of OP-Amp's a summer and as a difference amplifier.</li> <li>3. To perform the mathematical operation of differentiation using basic and practical circuit of Op-Amp's.</li> <li>4. To perform the mathematical operation of integration using basic and practical circuit of Op-Amp's.</li> <li>5. To study the half wave and full wave rectifier circuits using Op-Amp's</li> <li>6. To design a first order butter worth low pass and high pass filter and determining its frequency response.</li> <li>7. To plot the frequency response of the band pass filter for a specified frequency range.</li> <li>8. To design a square, triangular and sawtooth wave generator using Op-Amp's.</li> <li>9. To design the Wien Bridge oscillator using Op-Amp's.</li> <li>10. To study the clipping and clamping circuits using operational amplifiers.</li> </ol> <p><b>Note:</b> The concerned Course Coordinator will prepare the actual list of experiments/problems at the start of semester based on above generic list.</p>	
<b>Course Outcomes</b> <p>Upon successful completion of the course, the students will be able to</p> <p>CO1: To learn the basic applications of the operational amplifier</p> <p>CO2: To learn how to detect, amplify, store, create and manipulate signals using operational amplifiers</p> <p>CO3: To design and analyze the responses of IC based designed circuits in the area of power management, signal conditioning, analog and digital communication</p>	

Course Name: **Analog Communication Lab**

Course Code: **EC-226**

Contact Hours/Week: **2P**

Course Credits: **01**

**Course Objectives**

- To understand practical implementation of various analog modulation schemes.
- To analyse and measure the performance of various analog modulation schemes.
- To understand practical implementation of pulse modulation, TDM and FDM.

1. 1.Amplitude Modulation and Demodulation
2. DSB SC Modulation and Demodulation
3. SSB SC Modulation and Demodulation
4. Frequency Modulation and Demodulation
5. To Observe and Measure Frequency Deviation and Modulation Index.
6. Pre Emphasis - De Emphasis.
7. PAM Generation and Reconstruction
8. PWM Generation and Reconstruction
9. PPM Generation and Reconstruction
10. Verification of Sampling Theorem
11. Time Division and Frequency Division Multiplexing
12. Phase Locked Loop

*Note: The concerned Course Coordinator may prepare the actual list of experiments/problems at the start of semester based on above generic list.*

**Course Outcomes**

Upon successful completion of the course, the students will be able to

CO1: Design and implement AM and FM based analog communication systems.

CO2: Design and implement Pulse modulation systems.

CO3: Design and implement FDM and TDM systems.

CO4: Analyse the performance measure of Analog Communication Systems.



Course Name: **Microprocessors and Microcontrollers Lab**  
Course Code: **EC-227**

Contact Hours/Week: **2P**

Course Credits:**01**

**Course Objectives**

- To provide skills for designing flowcharts and writing algorithms
- To provide skills for writing Embedded programs
- To enable the students to debug programs

**List of Experiments**

1. On 8085 kit, find the Factorial of a number
2. On 8085 kit, find if a number is prime or a perfect square
3. On 8051 kit, write a program to perform serial data transfer
4. On 8051 kit, generate square wave for a given frequency and duty cycle
5. On cortex M3, write a program to perform LED blinking
6. On cortex M3, write a program to verify Digitalout
7. On cortex M3, write a program to display clock on 7-segment display
8. On cortex M3, write a program to generate Analog output
9. On cortex M3, write a program to read in Analog input
10. On cortex M3, write a program to debug using serial pc
11. On cortex M3, write a program to generate PWM output
12. On cortex M3, write a program to perform counting on LCD counter
13. On cortex M3, write a program to learn Interrupt function
14. On cortex M3, write a program to understand 12c master and slave communication
15. On Intel Galileo Gen 2, plot a graph for analog input
16. On Intel Galileo Gen 2 write an Array in Arduino

**Note:** The concerned Course Coordinator will prepare the actual list of experiments/problems at the start of semester based on above generic list.

**Course Outcomes**

Upon successful completion of the course, the students will be able to  
CO1: Write algorithms and programming task involved for a given problem  
CO2: Design and develop modular programming skills  
CO3: Trace and debug a program

Course Name: <b>Microwave Devices and Systems</b>		
Course Code: <b>EC-311</b>		
Course Type: <b>Core</b>		
Contact Hours/Week: <b>3L</b>		Course Credits: <b>03</b>
<b>Course Objectives</b> <ul style="list-style-type: none"> <li>• Develop students' understanding of the basic concepts and theories of microwave engineering.</li> <li>• Equip students with the skills and knowledge needed to design, analyse, and optimize microwave systems and devices.</li> <li>• Provide students with practical experience in the use of state-of-the-art microwave test equipment.</li> <li>• Develop students' ability to apply microwave engineering concepts to solve real-world problems in a range of fields.</li> </ul>		
<b>Unit Number</b>	<b>Course Content</b>	<b>Contact Hours</b>
UNIT-01	<b>INTRODUCTION OF MICROWAVE AND WAVEGUIDE</b> Need of microwave, Advantages and application of microwave signals, Frequency Allocations and Frequency Plans, Rectangular Waveguide and its Analysis, Circular Waveguide and its Analysis, Modes of Propagation, Dominant Modes, Cut-off wavelength, Mode Excitation.	<b>07 L</b>
UNIT-02	<b>MICROWAVE TUBES</b> Limitations of Conventional Tubes at Microwave Frequency, Two cavity, Multi-cavity Klystron Amplifiers their Analysis, Reflex Klystron and their analysis, Basics of Magnetrons, Traveling Wave Tube and their Applications.	<b>07 L</b>
UNIT-03	<b>MICROWAVE DEVICES</b> Scattering Matrix of Microwave Waveguide Junction, Properties of Scattering Matrix, E-Plane Tee, H-plane Tee, Magic Tee, Directional Couplers, Ferrite Devices, Gyrator, Isolator, Circulators.	<b>06L</b>
UNIT-04	<b>MICROWAVE SOLID-STATE DEVICES</b> Gunn Diode and its Modes of Operation, Avalanche IMPATT Diode, TRAPATT Diode, Operations and V-I Characteristics of Tunnel Diode, Schottky Diode, Varactor Diodes, PIN Diode and its Applications.	<b>05 L</b>
UNIT-05	<b>MICROWAVE COMPONENT AND MEASUREMENT DEVICES</b> VSWR meter, Frequency meter, Spectrum analyser, Network analyser, Tunable detector, Slotted line carriage, Power meter, Phase Shifter, Matched Load, Waveguide Transition, Attenuators.	<b>06 L</b>
UNIT-06	<b>MICRO-STRIP LINES:</b> Introduction on Micro Strip Lines, Characteristic impedance of micro Strip Lines, Losses in Micro Strip Lines, Quality Factor of Micro Strip, Parallel Strip Lines, Coplanar Strip Lines and Shielded Strip Lines.	<b>05 L</b>
<b>Course Outcomes</b> Upon successful completion of the course, the students will be able to CO1: Demonstrate an understanding of the behaviour of electromagnetic waves in waveguides. CO2: Understand the operating principles and applications of microwave tubes. CO3: Understand the properties and behaviour of various passive microwave devices such as waveguide junction, directional couplers, power dividers, etc. CO4: Understand the performance of microwave components, measuring devices and solid-state devices. CO5: Analyse the characteristics of micro-strip lines including impedance, losses, etc.		
<b>Books and References</b> <ol style="list-style-type: none"> <li>1. Microwave Devices and Circuits by Samuel Y. Liao, Prentice-Hall, U.S.A.</li> <li>2. Microwave Engineering by David M. Pozar, Wiley Publication, New Delhi.</li> <li>3. Foundations for Microwave Engineering by R.E. Collins, Wiley Inter-science, New York.</li> <li>4. Microwave Engineering by S. Das, Oxford University Press.</li> </ol>		

Course Name: <b>Digital Communication and Systems</b>		
Course Code: <b>EC-312</b>		
Course Type: <b>Core</b>		
Contact Hours/Week: <b>3L</b>		Course Credits: <b>03</b>
<b>Course Objectives</b> <ul style="list-style-type: none"> <li>To impart knowledge about the key modules of digital communication systems with emphasis on digital modulation techniques.</li> <li>To introduce the fundamental concepts relevant to reception of digital signals</li> <li>To enable the students to understand the concept and basics of information theory and the basics of channel coding/decoding.</li> </ul>		
<b>Unit Number</b>	<b>Course Content</b>	<b>Contact Hours</b>
UNIT-01	<b>Introduction:</b> Concepts of Digital Communication, Advantages/Disadvantages of Digital Communication Systems over Analog Communication Systems. Block Diagram of Basic Digital Communication Transmitter/Receiver.	<b>02L</b>
UNIT-02	<b>Analog to Digital Conversion:</b> Noisy Communications Channels, Sampling Theorem: Low Pass Signals And Band Pass Signals, Pulse Amplitude Modulation, Channel Bandwidth For PAM Signal, Natural Sampling, Flat Top Sampling, Signal Recovery & Holding, Quantization of Signal, Quantization Error, Pulse Code Modulation (PCM), Delta Modulation, Adaptive Delta Modulation.	<b>08L</b>
UNIT-03	<b>Digital Modulation Techniques:</b> Binary Phase Shift Keying, Differential Phase Shift Keying, Differential Encoded PSK, QPSK, Quadrature Amplitude Shift Keying (QASK), Binary Frequency Shift Keying.	<b>8L</b>
UNIT-04	<b>Data Transmission:</b> Base Band Signal Receiver, Probability of Error, Optimum Filter, White Noise-Matched Filter, Probability of Error of The Matched Filter, Coherent Reception: Correlation, Application of Coherent Reception In PSK And FSK. Correlation Receiver for QPSK.	<b>7L</b>
UNIT-05	<b>Noise in Pulse Code &amp; Delta Modulation Systems:</b> PCM Transmission, Calculation of Quantization Noise, O/P Signal Power, The Effect of Thermal Noise, O/P Signal to Noise Ratio in PCM, Delta Modulation, Quantization Noise in Delta Modulation, The O/P Signal to Quantization Noise Ratio in Delta Modulation, O/P Signal to Noise Ratio in Delta Modulation.	<b>6L</b>
UNIT-06	<b>Information Coding and Decoding:</b> Coding for Error Detection and Correction, Basics of Block Coding and Decoding, Introduction to Cyclic Codes, Basic Convolution Coding /Decoding and Viterbi Algorithm.	<b>05L</b>
<b>Course Outcomes</b> Upon successful completion of the course, the students will be able to CO1: Apply the knowledge of statistical theory of communication and explain the conventional digital communication system. CO2: Apply the knowledge of signals and system and evaluate the performance of digital communication system in the presence of noise. CO3: Apply the knowledge of digital electronics and describe the error control codes like block code, cyclic code. CO4: Design as well as conduct experiments, analyze and interpret the results to provide valid conclusions for digital modulators and demodulator using hardware components and communication systems using CAD tool.		
<b>Books and References</b> <ol style="list-style-type: none"> <li>Principles of communication systems by Taub &amp; Schilling, McGraw-Hill Education (India).</li> <li>Communication Systems by Simon Haykin, John-Wiley &amp; Sons, Inc.</li> <li>Digital Communication by J.G. Proakis, McGraw – Hill.</li> <li>Digital Communications: Fundamentals &amp; Applications by B. Sklar, Pearson Education.</li> <li>Introduction to Digital Communication by R.E. Zimer &amp; R.L. Peterson, PHI.</li> </ol>		

Course Name: <b>Control Systems</b>		
Course Code: <b>EC-313</b>		
Course Type: <b>Core</b>		
Contact Hours/Week: <b>3L</b>		Course Credits: <b>03</b>
<b>Course Objectives</b> <ul style="list-style-type: none"> <li>• To discuss basic concepts of linear systems.</li> <li>• To provide a basic understanding of mathematical model of linear systems.</li> <li>• To introduce the fundamental concept of different control components.</li> <li>• To enable the students to understand the concepts of time and frequency domain analysis.</li> <li>• The students can be able to learn stability analysis.</li> <li>• To discuss the concept of state variable.</li> </ul>		
<b>Unit Number</b>	<b>Course Content</b>	<b>Contact Hours</b>
UNIT-01	<b>Basic Concepts:</b> Historical Review, Definitions, Classification, Relative Merits and Demerits of Open and Closed Loop Systems.	<b>03L</b>
UNIT-02	<b>Mathematical Models of Control System:</b> Linear and Non-Linear Systems, Transfer Function, Mathematical Modeling of Electrical, Mechanical and Thermal Systems, Analogies, Block Diagrams and Signal Flow Graphs.	<b>07L</b>
UNIT-03	<b>Control Components:</b> DC Servomotor, AC Servomotor, Potentiometers, Synchronous, Stepper Motor.	<b>05L</b>
UNIT-04	<b>Time and Frequency Domain Analysis:</b> Transient and Frequency Response of First and Second Order Systems, Correlation Between Time and Frequency Domain Specifications, Steady-State Errors and Error Constants, Concepts and Applications of P, PD, PI and PID Types of Control.	<b>07L</b>
UNIT-05	<b>Stability Analysis:</b> Definition, Routh-Hurwitz Criterion, Root Locus Techniques, Nyquist Criterion, Bode Plots, Relative Stability, Gain Margin and Phase Margins.	<b>07L</b>
UNIT-06	<b>State Variable Analysis:</b> Introduction, Concept of State, State Variables & State Models, State Space Representation of Linear Continuous Time Systems, State Models for Linear Continuous Time Systems, State Variables and Linear Discrete Time Systems, Solution of State Equations, Concept of Controllability & Observability.	<b>07L</b>
<b>Course Outcomes</b> Upon successful completion of the course, the students will be able to CO1: Demonstrate fundamentals of (feedback) control systems. CO2: Explain mathematical model for different systems. CO3: Explain different control components. CO4: Explain the relation between time and frequency domain specification and employ controllers such as P, PD, PI and PID control design. CO5: The use and significance of the different tools for control system design and analysis such as Nyquist plots, Bode plots, and Evans plots (root locus). CO6: Demonstrate concept of state variable and state model.		
<b>Books and References</b> <ol style="list-style-type: none"> <li>1. Discrete-Time Control Systems by K. Ogata, Prentice Hall India Learning Pvt. Ltd.</li> <li>2. An Introduction to Control Systems by K. Warwick, World Scientific Publishing Co. Pvt. Ltd.</li> <li>3. Control System Fundamentals by W. S. Levine, CRC Press.</li> <li>4. Modern Control Systems by R. C. Dorf and R. H. Bishop, Prentice Hall.</li> </ol>		

Course Name: <b>VLSI Design</b>		
Course Code: <b>EC-314</b>		
Course Type: <b>Core</b>		
Contact Hours/Week: <b>3L</b>		Course Credits: <b>03</b>
<b>Course Objectives</b> <ul style="list-style-type: none"> <li>• To introduce the fundamental concepts of MOSFETs and physical design of VLSI circuits.</li> <li>• To impart knowledge about various CMOS VLSI Design styles.</li> <li>• To design MOS memories and learn high performance VLSI design techniques.</li> <li>• To enable the students to understand the parameters on which the circuit performance depends and their control strategies.</li> </ul>		
Unit Number	Course Content	Contact Hours
UNIT-01	<b>MOSFETS:</b> Fundamentals of Enhancement Mode MOSFETs, Depletion Mode MOSFETs, Weak & strong Inversion Conditions, Threshold Voltage Concept in MOSFETs, Current-Voltage (IV), Characteristics of a MOSFET, Limitations in IV Model, MOSFET parasitics. Trends & Projections in VLSI Design & Technology, Flow of VLSI Circuit Design, Scaling in MOS devices.	<b>06L</b>
UNIT-02	<b>VLSI Design Styles:</b> NMOS and CMOS Process flow, Concept of Noise Margin, Resistive MOS inverter design, voltage transfer characteristics, Inverter Threshold Voltage. Active loads and their importance, NMOS Inverter design, Critical voltages for inverter design, CMOS Inverter design and properties, CMOS Delay and Power Dissipation, Parallel & Series Equivalent circuits, Static CMOS Circuit Design.	<b>11L</b>
UNIT-03	<b>VLSI Physical Design:</b> Stick Diagrams, Physical Design Rules, Layout Designing, Euler's Rule for Physical Design, Reliability issues in CMOS VLSI, Latching.	<b>05L</b>
UNIT-04	<b>High Performance Logics:</b> Precharge -Evaluate logic, Dynamic CMOS logic circuits. Transmission gate logic.	<b>06L</b>
UNIT-05	<b>MOS Memory Design:</b> Types of MOS memories, ROM memory design, SRAM cell design and DRAMs.	<b>06L</b>
UNIT-06	<b>CMOS Amplifiers:</b> Single stage MOS Amplifiers: Common Source amplifier, Common Gate amplifier, Common Drain amplifier. Differential Amplifier analysis. Merits and advantages of differential amplifiers.	<b>06L</b>
<b>Course Outcomes</b> Upon successful completion of the course, the students will be able to CO1: Comprehend and utilize digital and analog VLSI circuit design techniques and their advancements. CO2: Identify, select and design any static and dynamic CMOS VLSI logic circuits for practical applications and memory design. CO3: Analyse CMOS circuits with equivalent parameters and build upon the theoretical, mathematical and experimental models. CO4: Use EDA tools and SPICE for analysis, verification and physical design simultaneously for efficient and optimal design of VLSI circuits. CO5: Generate interest and competence in self-directed continuing professional development and for sustainable research and development in VLSI design for societal and global interest.		
<b>Books and References</b> <ol style="list-style-type: none"> <li>1. CMOS Digital Integrated Circuits-Analysis &amp; Design by S.M. Kang and Y. Leblebici, TMH.</li> <li>2. Design of Analog CMOS Integrated Circuits by B. Razavi, TMH.</li> <li>3. Solid State Electronic Devices by B.G. Streetman and S. Banerjee, PHI.</li> <li>4. Principles of CMOS VLSI Design- A Systems Perspective by Neil H E Weste and K. Eshraghian.</li> <li>5. Introduction to VLSI by K. Eshraghian and Pucknell, PHI.</li> </ol>		

Course Name: **Digital Communication Lab**  
Course Code: **EC-315**

Contact Hours/Week: **2P**

Course Credits: **01**

**Course Objectives**

- To understand practical implementation of various digital modulation schemes.
- To analyze and measure the performance of various digital modulation schemes.
- To understand practical implementation of line coding formats.

**List of Experiments**

1. Time Division Multiplexing & De-multiplexing.
2. Pulse Code Modulation & Demodulation.
3. Delta Modulation and Demodulation.
4. Adaptive Delta Modulation and Demodulation.
5. Binary Phase Shift Keying (BPSK) Modulation and Demodulation.
6. Frequency Shift Keying (FSK) Modulation and Demodulation.
7. Amplitude Shift Keying (ASK) Modulation and Demodulation.
8. Quadrature Phase Shift Keying (QPSK) Modulation and Demodulation
9. To Study Characteristics of Gaussian Noise and to Measure its Spectral Height in Frequency Band over Which Its Spectral Density is flat.
10. To Study Line Coding Techniques.
11. To Study the Characteristics of The Phase Shifter, Multiplier and The Integrate-And-Dump Filter.

**Note:** The concerned Course Coordinator will prepare the actual list of experiments/problems at the start of semester based on above generic list.

**Course Outcomes**

Upon successful completion of the course, the students will be able to

CO1: Design and implement BPSK, QPSK, ASK and BFSK based digital communication systems.

CO2: Design and implement PCM, DM and ADM based digital communication systems.

CO3: Analyze the performance measure of Digital Communication Systems.

Course Name: **VLSI Design Lab**

Course Code: **EC-316**

Contact Hours/Week: **2P**

Course Credits: **01**

### Course Objectives

- To learn Physical Design i.e. Layout making of VLSI circuits.
- Programming in SPICE and its use for design and analysis.
- To extract various design parameters from simulation results.
- Provide students with an opportunity to practice on EDA softwares & tools for VLSI Design.

### List of Experiments

1. Familiarity with Tanner L-EDIT EDA Tools: To study the main features and utilities of the tools for design and physical design of circuits. Report the pros and cons of the tool.
2. To find dc and transient response of a CMOS Inverter Circuit and its Physical Design using minimum dimension criteria. Hence extract various design parameters from simulation results.
3. To simulate transient response of CMOS NAND Gate (Fig.1). Physical Design the logic gate or design the layout, using minimum dimension criteria.

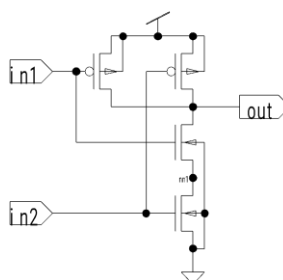


Fig.1. CMOS NAND Gate.

4. Simulate firstly minimum dimension CMOS inverter circuit using SPICE. Hence analyze and plot power and delay variations with voltage scaling, and ii) Load variations.
5. Simulate CMOS NAND, NOR and XOR circuits using SPICE. Hence analyze and plot their power and delay variations i) For dimension, load and frequency variations.
6. Design a differential amplifier circuit for a voltage gain of 10. Design its layout.
7. Physical Design of a complex circuit AOI/ OAI, making layout using Euler's method, for delay, power and area centric designs.
8. Design a four input CMOS NAND and NOR gates with the constraint propagation delay not exceeding 10ns. Compare LVS.
9. Familiarization with Cadence or Xilinx EDA tools. To study the main features and utilities of the tools for design and physical layout design. Report the same in practical file.
10. Design NAND NOR, XOR circuits using EDA Tools, for delay and power centric design criteria.
11. Physical design a full adder circuit using minimum number of CMOS NAND gates.
12. Design triangular wave generator using OP-Amps in SPICE.

**Note:** The concerned Course Coordinator will prepare the actual list of experiments/problems at the start of semester based on above generic list.

### Course Outcomes

Upon successful completion of the course, the students will be able to

CO1: Identify and abstract the programming task involved for a given VLSI problem.

CO2: Design and develop programming skills for VLSI circuit design.

CO3: Trace and debug any VLSI related program.

Course Name: <b>Digital Signal Processing</b>		
Course Code: <b>EC-321</b>		
Course Type: <b>Core</b>		
Contact Hours/Week: <b>3L</b>		Course Credits: <b>03</b>
<b>Course Objectives</b> Digital Signal processing explains the basics of discrete time signals and systems. It focuses on the operation on the signals in time and frequency domain. It covers the different design techniques for FIR and IIR filters and also their realization structures.		
Unit Number	Course Content	Contact Hours
UNIT-01	<b>DISCRETE-TIME SIGNALS AND SYSTEMS</b> Basic Elements of a Digital Signal Processing System, Advantages of Digital Signal Processing, Classification of Signals, The Concept of Frequency In Continuous-Time and Discrete-Time Domain, Discrete-Time Signals and Systems, Analysis Of Discrete-Time Linear Shift-Invariant Systems, Linearity, Causality And Stability Criterion, Discrete-Time Systems Described By Difference Equations.	07L
UNIT-02	<b>DISCRETE-TIME FOURIER TRANSFORM</b> The Fourier Transform Of Discrete-Time Signals (DTFT), Properties Of The DTFT, The Frequency Response Of An LTI Discrete-Time System, The Fourier Series Of Discrete-Time Signals (DTFS).	05L
UNIT-03	<b>DISCRETE FOURIER TRANSFORM:</b> Frequency Domain Sampling And The DFT, Properties Of The DFT, Linear Filtering Methods Based On The DFT, Efficient Computation Of The DFT: Decimation-In-Time And Decimation-In Frequency Fast Fourier Transform Algorithms.	07L
UNIT-04	<b>Z-TRANSFORM</b> Introduction To The Z-Transform & The Inverse Z-Transform, Properties Of The Z-Transform, Relationship Between The Fourier Transform And The Z-Transform, Rational Z-Transforms & The System Function, Analysis Of Linear Time-Invariant Systems In The Z-Domain.	05L
UNIT-05	<b>DIGITAL FILTER STRUCTURES</b> Digital Filter Categories, Realization Structures For FIR & IIR Digital Filters, Representation Of Numbers: Fixed-Point, Floating Point, Error Resulting From Rounding And Truncation.	04L
UNIT-06	<b>DIGITAL FILTER DESIGN</b> General considerations; design of IIR filter from Analog filters: IIR filter design using Approximation of derivative, impulse invariant method, Bilinear transformation; Design of linear phase FIR digital filters: Symmetry and Anti-symmetry FIR filters, FIR digital filter design using the windowing method and the frequency-sampling method.	08L
<b>Course Outcomes</b> Upon successful completion of the course, the students will be able to CO1: Understand the discrete time signals and systems. CO2: Understand the Fourier transform and Fourier series of discrete time signals. CO3: Analysis the discrete time signals in frequency domain using DFT and FFT. CO4: Understand the Z-Transform and its properties. CO5: Understand the realization structures for FIR and IIR digital filters. CO6: Analysis the design characteristic of FIR and IIR filters.		
<b>Books and References</b> 1. Digital Signal Processing: Principles, Algorithms and Applications by John G. Proakis & Dimitris G. Manolakis, Pearson Education. 2. Digital Signal Processing by Sanjit K. Mitra, Tata McGraw Hill Publication.		



Course Name: <b>Antenna and Wave Propagation</b>		
Course Code: <b>EC-322</b>		
Course Type: <b>Core</b>		
Contact Hours/Week: <b>3L</b>		Course Credits: <b>03</b>
<b>Course Objectives</b> <ul style="list-style-type: none"> <li>To impart knowledge about the Electromagnetic radiation, antenna basic parameters, antenna arrays and their patterns, special antennas, wave propagation over ground, through troposphere and ionosphere.</li> <li>To introduce the fundamental concepts relevant to electromagnetic theory and its application to antennas and wave propagation.</li> </ul>		
<b>Unit Number</b>	<b>Course Content</b>	<b>Contact Hours</b>
UNIT-01	<b>Electromagnetic radiation:</b> Recap of Maxwell's equations and wave equations, Retarded potentials, Vector potential A for an electric current source J, Radiation phenomenon from an oscillation dipole, Infinitesimal Dipole Antenna, Small dipole, Half wave dipole and quarter wave monopole antenna and its radiation characteristics.	<b>08L</b>
UNIT-02	<b>Antenna Fundamentals:</b> Radiation Pattern, Radiation Power Density, Radiation Intensity, Beamwidth, Directivity, Antenna Efficiency, Gain, Beam Efficiency, Bandwidth, Polarization, Input Impedance, Antenna Vector Effective Length and Equivalent Areas, Maximum Directivity and Maximum Effective Area, Friis Transmission Equation and Radar Range Equation	<b>08L</b>
UNIT-03	<b>Antenna Arrays:</b> Classification of arrays, Linear arrays of two-point sources, Linear arrays of npoint sources, Pattern multiplication, Array factor, Linear arrays of equal amplitude and spacing (Broad side and end fire arrays) of n-point sources, Directivity and beam width, Non-uniform arrays excitation using Binomial series	<b>09L</b>
UNIT-04	<b>Special Antennas:</b> Basics of Loop antennas, Folded dipole antennas, Yagi-Uda antenna, Horn antennas, Helical antennas, Microstrip Patch antennas and its feeding techniques.	<b>08L</b>
UNIT-05	<b>Wave Propagation:</b> Ground Wave Propagation: Characteristics for ground wave propagation, Reflection at the surface of a finitely conducting plane and on earth, Attenuation Calculation of field strength at a distance. Ionosphere Propagation: ionosphere structure, Effective characteristics of the various layers of ionosphere, Reflection and Refraction of waves by ionosphere, Space Wave Propagation: Space wave range, Troposphere waves-reflection, Refraction, Duct propagation, Troposphere propagation link.	<b>09L</b>
<b>Course Outcomes</b> Upon successful completion of the course, the students will be able to CO1: Understand the mechanism of radiation and will be able to Identify basic antenna parameters CO2: Design and analyse wire antennas CO3: Design and analyse antenna arrays CO4: Analyse different antennas CO5: To identify characteristics of radio wave propagation		
<b>Books and References</b> <ol style="list-style-type: none"> <li>Antennas Theory by C.A. Balanis, Willey Publication.</li> <li>Antennas by J. D. Kraus, McGraw Hill.</li> <li>Antennas and Radio Propagation by R. E. Collins, McGraw-Hill.</li> <li>Electromagnetic waves &amp; radiating System, E. C. Jorden and B. C. Balmain, P.H.I.</li> </ol>		

Course Name: <b>Electronic Measurements and Instrumentation</b> Course Code: <b>EC-323</b> Course Type: <b>Core</b>		
Contact Hours/Week: <b>2L</b>		Course Credits: <b>02</b>
<b>Course Objectives</b> <ul style="list-style-type: none"> <li>● To introduce the various methods of instrumentation and parameters</li> <li>● To impart knowledge about the various AC and DC measurement instruments</li> <li>● To enable the students to understand the various types and transducers</li> </ul>		
Unit Number	Course Content	Contact Hours
UNIT-01	<b>Instrumentation Scheme &amp; Characteristics:</b> Definition, Application and Methods of Measurements, Instrument Classification, Functional Elements of an instrument, Input Output Configuration of Measuring Instruments, Methods of Correction for Interfering and Modifying Inputs, Standards, Calibration, Accuracy, Precision, Loading Effects, Selection of Instruments, Measurement Systems–Static and Dynamic Characteristics, Zero Order, First Order and Second Order Systems and their Response	<b>07L</b>
UNIT-02	<b>Error analysis:</b> Types of Errors, Methods of Error Analysis, Uncertainty Analysis, Statistical Analysis, Gaussian Error Distribution, Rejection of Data, Method of Least Square, Curve Fitting, Graphical Analysis, General Consideration in Data Analysis	<b>07L</b>
UNIT-03	<b>DC &amp; AC Measurement:</b> Analog Ammeter, Voltmeter and Ohmmeters, PMMC, Moving Iron, Electro-dynamometer, Electrostatic, Ohmmeter, Digital type voltmeter, AC Voltmeter using Rectifier, True RMS Voltmeter, Digital VOM Meter	<b>05L</b>
UNIT-04	<b>Transducers:</b> Principles, Classification, Guidelines for Selection, Requirements, Types and Application of Transducers, Resistance, Capacitance, Inductance Transducers, Potentiometer, Strain Gauges, LVDT, Piezo Electric Transducers, Resistance Thermometers, Thermocouples, Thermistors, Photosensitive Device, Capacitive Transducer, Hall Effect Transducers, Pyroelectric Sensors, Thermo Sensors using Semiconductor Devices, Thermal Radiation Sensor, Measurement of Force, Pressure, Velocity, Humidity, Moisture, Speed, Proximity and Displacement	<b>08L</b>
<b>Course Outcomes</b> Upon successful completion of the course, the students will be able to CO1: Describe basic principles of instrumentations and measurements associated with engineering, design and the general technology applications. CO2: Use and calibrate common errors in instruments and their analysis. CO3: Selecting appropriate sensors, instruments, display devices and analyzers for the task under consideration. CO4: Understanding various transducers available, their operating principles, strengths, and weaknesses.		
<b>Books and References</b> <ol style="list-style-type: none"> <li>1. Modern Electronic Instrumentation and Measurement Techniques by A.D. Helfrick and W.D. Cooper, Prentice Hall.</li> <li>2. Instruments and Measurements by C.N. Herrick, Mc Graw Hill.</li> <li>3. Electrical and Electronic Measurements and Instrumentation by A. K Sawhney, Dhanpat Rai Publishing.</li> </ol>		

Course Name: <b>Engineering Economics and Accountancy</b> Course Code: <b>HS-321</b> Course Type: <b>Core</b>		
Contact Hours/Week: <b>2L</b>		Course Credits: <b>02</b>
<b>Course Objectives</b> <ul style="list-style-type: none"> <li>To impart knowledge about the Economics and its applicability to the Engineers</li> <li>To introduce the fundamental concepts of economics</li> <li>To enable the students to understand the factors that causes the changes in economic conditions of the entrepreneur</li> </ul>		
Unit Number	Course Content	Contact Hours
UNIT-01	<b>Introduction to Engineering Economics:</b> Definitions, Nature, Scope and application; Difference between Micro Economics and Macro Economics; Theory of Demand & Supply: Meaning, Determinants, Law of Demand, Elasticity of demand, Demand Forecasting, Law of Supply, Equilibrium between Demand & Supply.	<b>06L</b>
UNIT-02	<b>Production and Cost:</b> Production functions, Isoquant, Least Cost combination, Laws of Returns to Scale. Economics and Diseconomies of Scale of production, Cost and Cost curves, Revenue and Revenue curve, Break even analysis.	<b>06L</b>
UNIT-03	<b>Costing and Appraisal:</b> Cost elements, Economic cost, Accounting cost, Standard cost, Actual cost, Overhead cost, Cost control, Criteria of project appraisal, Social cost benefit analysis	<b>05L</b>
UNIT-04	<b>Markets:</b> Meaning, Types of Markets, Characteristics (Perfect Competition, Monopoly, Monopolistic Competition, Oligopoly) Price and Output Determination; Product Differentiation; Selling Costs; Excess Capacity.	<b>05L</b>
UNIT-05	<b>Money:</b> Meaning, Functions, Types; Monetary Policy- Meaning, Objectives, Tools; Fiscal Policy:- Meaning, Objectives, Tools. <b>Banking:</b> Meaning, Types, Functions, Central Bank: its Functions, concepts CRR, Bank Rate, Repo Rate, Reverse Repo Rate, SLR.	<b>04L</b>
UNIT-06	<b>Depreciation:</b> Meaning of depreciation, causes, object of providing depreciation, factors affecting depreciation, Methods of Depreciation: Straight line method, Diminishing balance method, Annuity method and Sinking Fund method	<b>04L</b>
UNIT-07	<b>Financial Accounting:</b> Double entry system (concept only), Rules of Double entry system, Journal(Sub-division of Journal) , Ledger, Trial Balance Preparation of final accounts-Trading Account. Profit and Loss account, Balance Sheet.	<b>06L</b>
<b>Course Outcomes</b> Upon successful completion of the course, the students will be able to CO1: Identify the challenges of the economy as entrepreneur/manufacturer as well as consumer CO2: Describe the economic system at the micro and macro level CO3: Apply principles of economics and accountancy in the professional, personal and societal life CO4: Assess the role of engineering economics and accounting in attaining economic efficiency		
<b>Books and References</b> <ol style="list-style-type: none"> <li>Principles of Micro Economics by Mceachern &amp; Kaur, Cengage Publication.</li> <li>Managerial Economics by Craig Peterson &amp; W Cris Lewis, PHI Publication.</li> <li>Modern Microeconomics by A. Koutsoyiannis, Macmillan.</li> <li>Managerial Economics Theory and Applications by D. M. Mithani, Himalaya Publication House.</li> <li>Fundamental of Managerial Economics by Mark Hirschey, South Western Educational Publishing.</li> <li>Engineering Economics by Degramo, Prentice Hall.</li> <li>Financial Accounting–A Managerial Perspective by R. Narayanaswamy, PHI.</li> <li>Introduction to Accounting by J.R. Edwards and Marriot, Sage Publication.</li> <li>Cost Accounting by Jawahar Lal, Tata McGraw Hill.</li> <li>Project Planning Analysis, Selection, Implementation and Review by Prasanna Chandra, Tata McGraw Hill</li> </ol>		

Course Name: **Digital Signal Processing Lab**  
Course Code: **EC-324**

**Contact Hours/Week: 2P**

**Course Credits: 01**

**Course Objectives**

- To understand the fundamentals of digital signal processing, including discrete-time signals and systems, sampling and quantization, and digital filter design.
- To gain hands-on experience with MATLAB and other DSP tools for signal generation, analysis, and processing.
- To undertake a project on a specific DSP application and develop a prototype system using DSP hardware and software tools.
- To develop critical thinking and problem-solving skills through laboratory assignments and project work, and to enhance technical writing and presentation skills through lab reports and project presentations.

1. Demonstrate the Nyquist-Shannon sampling theorem and perform sampling and reconstruction of analog signals using different sampling rates and reconstruction filters.
2. Generate and analyze different types of fundamental signals such as sinusoidal, square wave, triangular wave, sawtooth wave, and noise signals. Perform frequency analysis using Fourier transform and windowing techniques.
3. Perform linear convolution, correlation and circular convolution of signals using MATLAB.
4. Perform Z-transform of signals using MATLAB and analyse the important properties of Z-transform.
5. Perform discrete Fourier transform (DFT) of signals using MATLAB and analyze the frequency domain characteristics of the signals.
6. Implement FFT (fast Fourier transform) algorithm to reduce the computation complexity in MATLAB.
7. Design and implement different types of digital filters such as low-pass, high-pass, band-pass, and band-stop filters using FIR (finite impulse response) and IIR (infinite impulse response) filter structures.
8. Image Processing: Perform image processing operations such as image filtering, edge detection, and image compression using MATLAB. Implement image codecs such as JPEG and MPEG.
9. DSP Hardware Implementation: Implement DSP algorithms on hardware platforms such as DSP kits (TMS320C6713 Processor Kits).
10. Project Work: Undertake a project on a specific DSP application such as digital audio processing, speech recognition, or image processing. Develop a prototype system using DSP hardware and software tools and present a report and demonstration of the project.

*Note: The concerned Course Coordinator may prepare the actual list of experiments/problems at the start of semester based on above generic list.*

**Course Outcomes:**

Upon successful completion of the course, the students will be able to

1. have a good understanding of basic DSP concepts, including sampling, filtering, frequency analysis, and time-domain analysis.
2. be proficient in using DSP tools, such as MATLAB, Simulink, or other software platforms for designing, simulating, and analyzing DSP systems.
3. be able to implement DSP algorithms in software or hardware, depending on the focus of the lab. They should be able to design, simulate, and test DSP algorithms for various applications.
4. have hands-on experience with DSP hardware, such as digital signal processors or field-programmable gate arrays (FPGAs). They should be able to program and configure DSP hardware for specific applications.

Course Name: **Antenna and Microwave Systems Lab**

Course Code: **EC-325**

Contact Hours/Week: **2P**

Course Credits: **01**

**Course Objectives**

- To provide skills for operating microwave benches at designed X band setup
- To provide skills for usage of microwave sources
- To enable the students to practical know how the microwave measurements.

**List of Experiments**

1. To Study the Microwave Components, Sources and Different Types of Loads at X Band Setup.
2. To Study the Characteristics of Reflex Klystron Oscillator and Determine its Mechanical and Electronics Tuning Range.
3. To Study the V-I Characteristics of Gunn Diode and Determine its Negative Resistance.
4. To Determine the Insertion Loss Parameter and Isolation Parameter of a Ferrite Based Isolator and Circulators.
5. To Measure the Frequency in a Rectangular Waveguide and Demonstrate the Relationship among the Frequency, Free Space Wavelength and Guide Wavelength.
6. To Study the Characteristics of Various Tees, i.e., E-Plane Tee, H-Plane Tee and Magic Tees.
7. To Measure the Coupling and Directivity of a 3 dB, 10 dB and 20 dB Directional Couplers.
8. To Measure the Low, Medium, and High VSWR of DUT Using Slotted Lines Section.
9. To Measure the Unknown Impedances using Smith Chart.
10. To Measure of VSWR, Insertion Loss, Attenuation of Fixed and Variable Attenuators.
11. To Plot the Radiation Pattern of a Pyramidal Horn Antenna and determine its Gain and Beam width.
12. To Study the working of Vector Network Analyzer (VNA) and its applications.
13. To Study the *E*-and *H*- field of a Rectangular Waveguide using HFSS/ CST.

*Note: The concerned Course Coordinator may prepare the actual list of experiments/problems at the start of semester based on above generic list.*

**Course Outcomes**

Upon successful completion of the course, the students will be able to

CO1: Understand basic requirements of microwave components and sources in real time applications.

CO2: Identify a practical approach for testing and measurement of microwave devices in real environment.

CO3: Design any analyse microwave device and components using software.

Course Name: <b>Computer Architecture and Organization</b>		
Course Code: <b>EC-411</b>		
Course Type: <b>Core</b>		
Contact Hours/Week: <b>3L</b>		Course Credits: <b>03</b>
<b>Course Objectives</b> <ul style="list-style-type: none"> <li>• To conceptualize the basics of organizational and architectural issues of a digital computer</li> <li>• To analyse processor performance improvement using instruction level parallelism.</li> <li>• To study various data transfer techniques in digital computer.</li> </ul>		
<b>Unit Number</b>	<b>Course Content</b>	<b>Contact Hours</b>
UNIT-01	<b>Basics of Computer Architecture:</b> Codes, Number System, Logic gates, Flip flops, Registers, Counters, Multiplexer, Demultiplexer, Decoder, Encoder etc, Register transfer, Bus & memory transfer, Logic micro operations, and Shift micro operation.	<b>05L</b>
UNIT-02	<b>Basic Computer Organization:</b> Instruction codes, Computer instructions, Timing & control, Instruction Cycles, Memory reference instruction, Input/output and Interrupts, Complete computer description & design of basic computer	<b>07L</b>
UNIT-03	<b>ARM Processor Fundamentals:</b> AR M core data flow model, Architecture, ARM General Purpose Register set, Exceptions, Interrupts, Vector Table, ARM processors family	<b>08L</b>
UNIT-04	<b>Central Processing Unit:</b> General register organization, Stack organization, Instruction format, Data transfer & manipulation, Program control, RISC, CISC, Addition & subtraction, Multiplication Algorithms, Division algorithms, Peripheral devices, I/O interface Data transfer schemes, Program control, Interrupt, DMA transfer, I/O processor	<b>07L</b>
UNIT-05	<b>Memory Unit:</b> Memory hierarchy, Processor vs. memory speed, High-speed memories, Cache memory, Associative memory, Interleave, Virtual memory, Memory management.	<b>05L</b>
UNIT-06	<b>Introduction to Parallel Processing:</b> Pipelining, Characteristics of multiprocessors, Interconnection structures, Inter-processor arbitration, Inter-processor communication & synchronization.	<b>04L</b>
<b>Course Outcomes</b> Upon successful completion of the course, the students will be able to CO1: Identify and compare different methods for computer I/O mechanisms. CO2: Categorize memory organization and explain the function of each element of a memory hierarchy. CO3: Demonstrate control unit operations and conceptualize instruction level parallelism.		
<b>Books and References</b> <ol style="list-style-type: none"> <li>1. Computer System Architecture by Morris M. Mano and Yu Dong, Prentice Hall.</li> <li>2. Computer Architecture A Quantitative Approach by J. L. Hennessy, D. A. Patterson and D. Goldberg, 3 rd ARM Edition.</li> <li>3. Computer Architecture and Organization by J. P. Hayes, McGraw Hill.</li> <li>4. System Architecture: Software and Hardware Concepts by W. E. Leigh and D. L. Ali, South Wester Publishing.</li> </ol>		

Course Name: <b>Wireless Communication</b> Course Code: <b>EC-412</b> Course Type: <b>Core</b>		
Contact Hours/Week: <b>3L</b>		Course Credits: <b>03</b>
<b>Course Objectives</b> <ul style="list-style-type: none"> <li>To understand the cellular concept of wireless communications.</li> <li>To study large-scale and small-scale propagation effects in wireless channels.</li> <li>To study different techniques that improve radio link performance in wireless communications.</li> <li>To understand general concepts of various multiple access techniques used in wireless communication.</li> </ul>		
<b>Unit Number</b>	<b>Course Content</b>	<b>Contact Hours</b>
UNIT-01	<b>INTRODUCTION</b> Evolution of wireless communication systems, Examples of wireless communication systems.	<b>02L</b>
UNIT-02	<b>THE CELLULAR CONCEPT-SYSTEM DESIGN FUNDAMENTALS</b> Concept of frequency reuse, Channel assignment strategies, Handoff strategies, Interference and system capacity, Trunking and grade of service, Improving coverage and capacity in cellular systems.	<b>09L</b>
UNIT-03	<b>PROPAGATION MODELS:</b> Free space propagation model, Two-ray ground reflection model, Distance power loss, Macro-cell propagation model, Micro-cell propagation model, Shadowing model, Multipath effects in mobile communication, Models for multipath reception.	<b>08L</b>
UNIT-04	<b>EQUALIZATION, DIVERSITY AND CHANNEL CODING:</b> Fundamentals of equalization, Adaptive equalizers, Linear and nonlinear equalization, Algorithms for adaptive equalization, Diversity techniques, Fundamentals of channel coding, Overview of error detection and correction codes.	<b>09L</b>
UNIT-05	<b>MULTIPLE ACCESS TECHNIQUES:</b> Introduction to multiple access, Frequency division multiple access, Time division multiple access, Spread spectrum multiple access, Space division multiple access, Packet radio, Orthogonal frequency division multiple access; Introduction to wireless systems and standards.	<b>09L</b>
<b>Course Outcomes</b> Upon successful completion of the course, the students will be able to CO1: Understand the operation of wireless and cellular communication systems. CO2: Analyze various design related issues associated with improving coverage and capacity of cellular systems. CO3: Analyze large-scale and small-scale radio propagation effects in mobile cellular systems. CO4: Understand the concepts of equalization, diversity and channel coding in wireless communications. CO5: Understand general concepts of various multiple access techniques for wireless communication.		
<b>Books and References</b> <ol style="list-style-type: none"> <li>Wireless Communications: Principles and Practice by Theodore S. Rappaport, Pearson / PHI Publications.</li> <li>Wireless Communications and Networks: 3G and Beyond by Iti Saha Misra, Tata McGraw Hill Publications.</li> <li>Mobile Cellular Telecommunications: Analog and Digital Systems by William C. Y. Lee, Tata McGraw Hill Publication</li> <li>Wireless Digital Communications by Kamilo Feher, PHI Publication.</li> </ol>		

Course Name: <b>Optical Fiber Communication</b> Course Code: <b>EC-413</b> Course Type: <b>Core</b>		
Contact Hours/Week: <b>3L</b>		Course Credits: <b>03</b>
<b>Course Objectives</b> <ul style="list-style-type: none"> <li>To introduce the students to various optical fiber modes, configurations and various signal degradation factors associated with optical fiber.</li> <li>To study about various optical sources and optical detectors and their use in the optical communication system</li> </ul>		
Unit Number	Course Content	Contact Hours
UNIT-01	<b>OVERVIEW:</b> The Electromagnetic Spectrum, Properties of Light, Dual Nature of Light Concept of a Photon, Wave Model, Characteristics of Light Waves. Concepts of Information, General Communication Systems, Evolution of Basic Fiber Optic Communication System, Benefits and Disadvantages of Fiber Optics. Transmission Windows. Transmission Through Optical Fiber, The Laws of Reflection and Refraction, Light Rays and Light Waves, Reflection of Light from Optical Surfaces, Refraction of Light from Optical Interfaces, The Numerical Aperture (NA), The Optical Fiber, Types of Fiber.	07L
UNIT-02	<b>LOSSES IN OPTICAL FIBER:</b> Attenuation, Material Absorption Losses, Linear and Non-Linear Scattering Losses, Fiber Bend Loss, Dispersion Viz. Inter Modal Dispersion and Intra Modal Dispersion, Overall Fiber Dispersion and Polarization, Dispersion Shifted and Dispersion Flattened Fibers, Attenuation and Dispersion Limits in Fibers, Kerr Nonlinearity, Self-Phase Modulation, Combined Effect of Dispersion and Self Phase Modulation	07L
UNIT-03	<b>FIBER MATERIAL, COUPLERS AND CONNECTORS:</b> Preparation of Optical Fiber: Liquid-Phase Techniques, Vapor Phase Deposition Techniques, Connector Principles, Fiber End Preparation, Splices, Connectors.	06L
UNIT-04	<b>OPTICAL SOURCES AND DETECTORS:</b> Sources: Basic Principle of Surface Emitter LED and Edge Emitter LED- Material Used Structure, Internal Quantum Efficiency and Characteristics, LASER Diode - Material Used Structure, Internal Quantum Efficiency and Characteristics, Working Principle and Characteristics of Distributed Feedback (DFB) Laser. Detectors: PIN Photodiode - Material Used, Working Principle & Characteristics, Avalanche Photodiode: - Material Used Working Principle and Characteristics.	05L
UNIT-05	<b>ADVANCED TOPICS:</b> Optical TDM, SCM, WDM And Hybrid Multiplexing Methods, Fiber Optic Networks, Trans receivers for Fiber-Optic Networks, Semiconductor Optical Amplifiers, Erbium Doped Fiber Amplifiers (EDFAs), Introduction to Free Space Optical and Visible Light Communications.	05L
UNIT-06	<b>OPTICAL NETWORKS:</b> Elements and Architecture of Fiber-Optic Network, SONET/SDH, ATM, IP, Optical Line Terminals (OLT), Optical Add-Drop Multiplexers, Optical Cross Connects.	06L
<b>Course Outcomes</b> Upon successful completion of the course, the students will be able to CO1: Learn the basic elements of optical fiber transmission link, fiber modes configurations and structures CO2: Understand the different kind of losses, signal distortion in optical wave guides and other signal degradation factors. CO3: Learn the fiber optical receivers such as PIN APD diodes, noise performance in photodetector, receiver operation and configuration CO4: Learn the various optical source materials, LED structures, quantum efficiency, Laser diodes CO5: Understand the optical multiplexing techniques, optical network and its architecture.		
<b>Books and References</b> 1. Fiber Optic Communications (Fifth Ed.) by J.C. Palais, Pearson Prentice Hall, 2005 2. Optical Fiber Communications (Third Ed.) by Gerd Keiser, McGraw-Hill, 2000 3. Optical Networks: A Practical Perspective (Third Ed.) by R Ramaswami and K.N. Sivarajan, Morgan Kaufman Publishers		



Course Name: **Optical Fiber Communication Lab**

Course Code: **EC-414**

Course Type: **Core**

Contact Hours/Week: **2P**

Course Credits: **01**

**Course Objectives**

- To study the design and deployment of optical fiber communication links.
- To understand the characteristics of optical fiber and losses in optical fiber communication link.

**List of Experiments**

1. To Study Numerical Aperture of optical Fiber.
2. To Study bending Losses in Optical Fiber.
3. To set up an 850nm fiber optic analog link.
4. To set up 850nm and 650 nm digital link, and to measure the maximum bit rates supportable on these links.
5. To measure the losses in optical fiber communication link.
6. To analyze the V-I characteristics and P-I characteristics using LED module.
7. To analyze the V-I characteristics of Laser Diode.
8. To study the characteristics of Avalanche photodiode (APD)
  - (a). APD at zero bias
  - (b). APD at reverse bias.

*Note: The concerned Course Coordinator will prepare the actual list of experiments/problems at the start of semester based on the above generic list.*

**Course Outcomes**

Upon successful completion of the course, the students will be able to

CO1: Understand the communication using optical fiber.

CO2: Set up the analog and digital communication links.

CO3: Analyse the characteristics of LED and Avalanche photodiode.

Course Name: **Communication Systems and Network Simulation Lab**  
Course Code: **EC-415**

**Contact Hours/Week: 2P**

**Course Credits: 01**

**Course Objectives**

- To introduce students to the fundamental concepts of communication systems and networks, including signal processing, coding, modulation, and multiple access techniques.
- To provide students with hands-on experience in designing, implementing, and evaluating communication systems and networks using simulation and modeling techniques.
- To prepare students for further study or research in the field of communication systems and networks.

1. Introduction to the simulation platform or programming language.
2. Simulation of random variables and probability distributions for modelling of communication systems and networks.
3. Simulation of digital modulation techniques (PSK, QAM, FSK) and comparison of their performance in AWGN channel
4. Performance analysis of error-correcting codes (Reed-Solomon, convolutional, turbo codes) using BER and FER metrics
5. Simulation of channel coding schemes (block codes, convolutional codes) and their application in digital communication systems
6. Implementation of Equalization techniques (linear, decision feedback) and their performance evaluation in a noisy channel
7. Simulation of spread-spectrum techniques (DS-CDMA, FH-CDMA) and comparison of their performance in a multipath fading channel.
8. Implementation and analysis of Carrier Sense Multiple Access (CSMA) and Carrier Sense Multiple Access with Collision Detection (CSMA/CD) protocols in a network.
9. Simulation of TCP congestion control and analysis of its impact on network performance.
10. Design and analysis of routing protocols (Dijkstra, Bellman-Ford, OSPF) in a network.

*Note: The concerned Course Coordinator may prepare the actual list of experiments/problems at the start of semester based on above generic list.*

**Course Outcomes:**

Upon successful completion of the course, the students will be able to

CO1: Understand the fundamental concepts of communication systems and networks, including signal processing, coding, modulation, and multiple access techniques.

CO2: Analyse and evaluate the performance of digital communication systems using simulation and modelling techniques.

CO3: Proficiently use the simulation tools and software (such as MATLAB/Simulink, Python, Network simulators) to design, implement and evaluate communication systems and networks.

CO4: Develop the critical thinking skills, problem-solving skills, and apply theoretical concepts to practical scenarios.

CO5: Prepare for further study or research in the field of communication systems and networks.

## **Discipline Elective Courses**

Course Name: <b>Electronics Device Modeling</b>		
Course Code: <b>EC-241</b>		
Course Type: <b>Discipline Elective-I</b>		
Contact Hours/Week: <b>3L</b>		Course Credits: <b>03</b>
<b>Course Objectives</b> <ul style="list-style-type: none"> <li>● To bring home knowledge of fundamentals of electronics devices and modeling.</li> <li>● Build upon the theoretical, mathematical and physical models of electronic devices used in VLSI, for proper understanding of e-circuit design, simulation and working.</li> <li>● To provide students with an opportunity to understand and practice on SPICE platform.</li> </ul>		
<b>Unit Number</b>	<b>Course Content</b>	<b>Contact Hours</b>
UNIT-01	<b>Introduction to Device Modeling:</b> Introduction, physical significance of device modeling, various devices used in device modeling. Material used in device modeling. Trends & projections in device modeling. Fundamentals of SPICE.	<b>05L</b>
UNIT-02	<b>Junction Diodes:</b> PN junction depletion region formation, Depletion-capacitance model, Storage capacitance model, DC, small signal, large signal, high frequency model of diodes. Measurement and extraction of diode model parameters.	<b>10L</b>
UNIT-03	<b>MOSFETs:</b> MOSFET fundamentals, Types of MOSFETs, Concept of threshold voltage, Large signal behavior MOSFETs, Comparison of operating regions of Bipolar and MOS Transistors, Shichman Hodges and Level-1 MOS Models, Limitations of MOS level-1. MOS level-2 model.	<b>10L</b>
UNIT-04	<b>Short &amp; Narrow Channel Effects in MOSFETs:</b> Velocity saturation, Mobility degradation, Weak Inversion in MOS Transistors, Narrow & Short Channel Effects in MOSFETs, namely, Drain induced barrier lowering (DIBL), Subthreshold current, Impact ionization, Charge sharing, Threshold variation with transistor dimensions, etc.	<b>05L</b>
UNIT-05	<b>Bipolar Junction Transistors:</b> DC, small signal, high frequency models of bipolar junction transistors. Ebers–Moll BJT model. Gummel–Poon BJT model. Extraction of BJT model parameters.	<b>05L</b>
UNIT-06	<b>Modern VLSI Devices:</b> Principle of hetro-junction devices, High speed compound semiconductor devices, opto devices.	<b>05L</b>
<b>Course Outcomes</b> Upon successful completion of the course, the students will be able to CO1: Comprehend the insight of electronic devices so as to provide appropriate and economically viable solutions to electronics engineering community and society at large. CO2: Identify the new state of art electronic device models to solve the real world research problems. CO3: Apply principles of usage of EDA tools & techniques for effective & efficient modeling of e-devices for e-circuits. CO4: Assess performance of electronic devices without actual fabrication so as to deal with e-designing for practical aspects and generate interest and competence in self-directed continuing professional development.		
<b>Books and References</b> <ol style="list-style-type: none"> <li>1. CMOS Digital Integrated Circuits-Analysis &amp; Design by S.M. Kang &amp; Y. Leblibici, TMH.</li> <li>2. Physics of Semiconductor Devices by S.M. Sze, Wiley Pub.</li> <li>3. Introduction to PSPICE by H.M. Rashid, PHI.</li> <li>4. Solid State Electronic Devices by B.G. Streetman &amp; S. Banerjee, PHI.</li> </ol>		

Course Name: <b>Nano Technology</b> Course Code: <b>EC-242</b> Course Type: <b>Discipline Elective-I</b> Contact Hours/Week: <b>3L</b> <span style="float: right;">Course Credits: <b>03</b></span>		
<b>Course Objectives</b> <ul style="list-style-type: none"> <li>To acquire the knowledge of the fundamentals of Nanomaterials.</li> <li>To enable the students to get familiarize with the basic concepts of Statistical and Quantum Mechanics</li> </ul>		
<b>Unit Number</b>	<b>Course Content</b>	<b>Contact Hours</b>
UNIT-01	<b>INTRODUCTION TO NANOTECHNOLOGY:</b> Importance of Nanotechnology-History of Nanotechnology-Opportunity at the nanoscale-length and time scale in structures-energy landscapes-Interdynamic aspects of inter molecular forces -classification based on the dimensionality- nanoparticles- nanoclusters-nanotubes-nanowires and nanodots- Semiconductor nanocrystals- carbon nanotubes- Influence of Nano structuring on Mechanical, optical, electronic, magnetic and chemical properties	09L
UNIT-02	<b>BASICS OF QUANTUM MECHANICS:</b> Introduction to Quantum Mechanics – Schrodinger equation – time dependent and time independent equations – Operators and observables – Commutation relations – Hermitian operators – Expectation values of observables – Solutions of the Schrodinger equation – free particle – particle in a box – one and three dimensions – particle in a finite well – Penetration through a barrier – Tunnel effect – Single step barrier.	09L
UNIT-03	<b>TYPES OF NANOMATERIALS:</b> Nanoclusters, Solid solutions, Thin film, Nanocomposites (Metal Oxide and Polymer based), Core Shell Nanostructure, Buckyballs, Carbon nano tubes and, Zeolites minerals, Dendrimers, Micelles, Liposomes, Block Copolymers, Porous Materials, Metal Nanocrystals, Semiconductor nanomaterials.	06L
UNIT-04	<b>STATISTICAL DESCRIPTION OF SMALL SYSTEMS:</b> Quantum states and phase space, the density matrix, few examples; An ideal gas in quantum mechanical ensembles; statistics of occupation numbers. Basic concepts and thermodynamic behavior of an ideal Bose gas, Bose-Einstein condensation; Discussion of a gas of phonons(The Debye field); Thermodynamics of an ideal Fermi gas, heat capacity of a free-electron gas at low temperatures.	09L
UNIT-05	<b>ELECTRON DYNAMICS IN SOLIDS:</b> Free electron theory of metals, band theory of solids, Bloch theorem – tight binding approximation – Kroning – Penne model, Evolution of band structure and Fermi surface- Metals and Insulators, semiconductors - Transport properties – Mobility, Resistivity, Relaxation time, Recombination centers- Hall effects – Confinement and transport in nanostructure – Electrons in quantum wires and quantum dots– Current, reservoirs, and electron channels – conductance formula for nanostructures – quantized conductance – Local density of states- Ballistic transport – Coulomb blockade – Diffusive transport – Fock space- Nanostructure Devices: Introduction, MODFETS, heterojunction bipolar transistors, Resonant-tunneling diodes, Field-effect transistors, Single electron-transfer devices, Potential-effect transistors, Carbon Nanotube transistors, Semiconductor Nanowire FETs	12L
<b>Course Outcomes</b> Upon successful completion of the course, the students will be able to C01: Understanding the insight of Nanoelectronics device physics so as to provide appropriate and economical viable solution to electronic engineering community and society at large. C02: Identifying different techniques to improve the state of art electronic device so as to solve the real world research problems. C03: Understanding the applications of quantum physics in semiconductor devices. C04: Identifying different devices so as to meet out the present design, health, safety and environmental challenges		
<b>Books and References</b> <ol style="list-style-type: none"> <li>A textbook of Nanoscience and Nanotechnology by Pradeep.T , McGraw Hill, 2012.</li> <li>Introduction to Nanoelectronics: Science, Nanotechnology, Engineering &amp; Applications by Vladimir. V. Mitin, Cambridge University Press.</li> <li>Introduction to Quantum Mechanics by David. J. Griffiths, Pearson, 2009.</li> <li>Introductory Quantum Mechanics by Richard. L. Liboff, R, Pearson, 2003.</li> <li>Statistical Physics by Claudine Herman, Springer, New York,2005.</li> <li>Introduction to Solid State Physics by Kittel. C, Wiley India Pvt Ltd., 2007.</li> <li>Nanomaterials Chemistry by Rao. C. N, Muller. A, Cheetham. A. K, Wiley-VCH, 2007.</li> <li>Nanotechnology for Microelectronics and Optoelectronics by J.M. Martinez-Duart, R.J.Martin Palma, F. Agulle Rueda, Elsevier.</li> </ol>		

Course Name: <b>Digital Arithmetic</b> Course Code: <b>EC-243</b> Course Type: <b>Discipline Elective-I</b>	
Contact Hours/Week: <b>3L</b>	Course Credits: <b>03</b>
<b>Course Objectives:</b> <ul style="list-style-type: none"> <li>To impart knowledge about the computer arithmetic algorithms, including different techniques enabling enhanced throughput and low power.</li> <li>To understand algorithms techniques to hardware implementation of various arithmetic operations.</li> </ul>	
<b>Course Content</b>	
<p>Numbers and Arithmetic: Review of Number systems, their encoding and basic arithmetic operations, Class of Fixed-Radix Number Systems, Unconventional fixed-point number systems, Representing Signed Numbers, Negative-radix number Systems, Redundant Number Systems, Residue Number Systems. Algorithms for Fast Addition: Basic Addition and Counting, Bit-serial and ripple-carry adders, Addition of a constant: counters, Manchester carry chains and adders, Carry-Look-ahead Adders, Carry determination as prefix computation, Alternative parallel prefix networks, VLSI implementation aspects, Variations in Fast Adders, Simple carry-skip and Carry-select adders, Hybrid adder designs, Optimizations in fast adders, Multi-Operand Addition, Wallace and Dadda trees, Parallel counters, Generalized parallel counters, Adding multiple signed numbers. High-Speed Multiplication: Basic Multiplication Schemes, Shift/add multiplication algorithms, Programmed multiplication, Basic hardware multipliers, Multiplication of signed numbers, Multiplication by constants, Preview of fast multipliers, High-Radix Multipliers, Modified Booth's recoding, Tree and Array Multipliers, Variations in Multipliers, VLSI layout considerations. Fast Division and Division Through Multiplication: Basic Division Schemes, Shift/subtract division algorithms, Programmed division, Restoring hardware dividers, Non-restoring and signed division, Division by constants, Preview of fast dividers, High-Radix Dividers, Variations in Dividers, Combined multiply/divide units, Division by Convergence, Hardware implementation. Real Arithmetic: Representing the Real Numbers, Floating-point arithmetic, The ANSI/IEEE floating point standard, Exceptions and other features, Floating-point arithmetic operations, Rounding schemes, Logarithmic number systems, Floating-point adders, Barrel-shifter design, Leading-zeros/ones counting, Floating-point multipliers, Floating-point dividers, Arithmetic Errors and Error Control. Implementation Topics: Computing algorithms, Exponentiation, Approximating functions, Merged arithmetic, Arithmetic by Table Lookup, Tradeoffs in cost, speed, and accuracy. High-Throughput Arithmetic, Low-Power Arithmetic, Fault-Tolerant Arithmetic, Emerging Trends, Impact of Hardware Technology.</p>	
<b>Course Outcomes:</b> Upon successful completion of the course, the students will be able to CO1: Understand hardware implementation of various algorithms. CO2: Learn to apply tradeoffs and multiple implementations and architectures. CO3: Know the use cases of various algorithms and their considerations	
<b>Books and References:</b> <ol style="list-style-type: none"> <li>Computer Arithmetic: Algorithms and Hardware Design by Parhami, B., Oxford University Press.</li> <li>Computer Arithmetic Algorithms by Koren, I., CRC Press.</li> <li>Digital Arithmetic by Ercegovic, M. and Lang, T., Elsevier.</li> <li>Verilog Digital Computer Design Algorithms into Hardware by Mark Gordon Arnold, Prentice Hall PTR.</li> </ol>	

Course Name: <b>Switching and Finite Automata Theory</b> Course Code: <b>EC-351</b> Course Type: <b>Discipline Elective-II</b>		
Contact Hours/Week: <b>3L</b>		Course Credits: <b>03</b>
<b>Course Objectives</b> <ul style="list-style-type: none"> <li>Understand the basics of threshold logic, effect of hazards on digital circuits and techniques of fault detection</li> <li>Explain finite state model and minimization techniques</li> <li>Know structure of sequential machines, and state identification</li> <li>Understand the concept of fault detection experiments</li> </ul>		
<b>Unit Number</b>	<b>Course Content</b>	<b>Contact Hours</b>
UNIT-01	<b>Threshold Logic:</b> Introductory Concepts: Threshold element, capabilities and limitations of threshold logic, Elementary Properties, Synthesis of Threshold networks: Unate functions, Identification and realization of threshold functions, The map as a tool in synthesizing threshold networks.	<b>08L</b>
UNIT-02	<b>Reliable Design and Fault Diagnosis:</b> Hazards, static hazards, Design of Hazard-free Switching Circuits, Fault detection in combinational circuits, Fault detection in combinational circuits: The faults, The Fault Table, Covering the fault table, Fault location experiments: Preset experiments, Adaptive experiments, Boolean differences, Fault detection by path sensitizing.	<b>08L</b>
UNIT-03	Sequential Machines: Capabilities, Minimization and Transformation the Finite state model and definitions, capabilities and limitations of finite state machines, State equivalence and machine minimization: k-equivalence, The minimization Procedure, Machine equivalence, Simplification of incompletely specified machines.	<b>08L</b>
UNIT-04	Structure of Sequential Machines: Introductory example, State assignment using partitions: closed partitions, The lattice of closed partitions, Reduction of output dependency, Input dependence and autonomous clocks, Covers and generation of closed partitions by state splitting: Covers, The implication graph, An application of state splitting to parallel decomposition.	<b>08L</b>
UNIT-05	State-Identification and Fault Detection Experiments: Experiments, Homing experiments, Distinguishing experiments, Machine identification, Fault detection experiments, Design of diagnosable machines, Second algorithm for the design of fault detection experiments.	<b>08 L</b>
<b>Course Outcomes</b> Upon successful completion of the course, the students will be able to CO1: Explain the concept of threshold logic. CO2: Understand the effect of hazards on digital circuits and fault detection and analysis. CO3: Define the concepts of finite state model. CO4: Analyse the structure of sequential machine. CO5: Explain methods of state identification and fault detection experiments.		
<b>Books and References</b> <ol style="list-style-type: none"> <li>Switching and Finite Automata Theory – Zvi Kohavi, McGraw Hill, 2nd edition. (Text Book)</li> <li>Fault Tolerant and Fault Testable Hardware Design - Parag K Lala, Prentice Hall Inc.</li> <li>Digital Circuits and Logic Design .- Charles Roth Jr, Larry L. Kinney, Cengage Learning.</li> </ol>		

Course Name: <b>Data Structures and Algorithms</b>		
Course Code: <b>EC-352</b>		
Course Type: <b>Discipline Elective-II</b>		
Contact Hours/Week: <b>3L</b>		Course Credits: <b>03</b>
<b>Course Objectives</b> <ul style="list-style-type: none"> <li>To impart knowledge about linear and non-linear data structures as the foundational base for computer solutions to problems.</li> <li>To introduce the fundamental concepts relevant to binary trees, binary tree traversals, binary search trees and perform related analysis to solve problems.</li> <li>To enable the students to understand various types of sorting algorithms.</li> </ul>		
<b>Unit Number</b>	<b>Course Content</b>	<b>Contact Hours</b>
UNIT-01	<b>Introduction:</b> Data types, data structures, abstract data types, the running time of a program, the running time and storage cost of algorithms, complexity, asymptotic complexity, big O notation, obtaining the complexity of an algorithm.	<b>07L</b>
UNIT-02	<b>Development of Algorithms:</b> Notations and Analysis, Storage structures for arrays – sparse matrices - structures and arrays of structures, Stacks and Queues: Representations, implementations and applications. Linked Lists: Singly linked lists, Linked stacks and queues, operations on Polynomials, Doubly Linked Lists, Circularly Linked Lists, Operations on linked lists- Insertion, deletion and traversal, dynamic storage management – Garbage collection and compaction.	<b>10L</b>
UNIT-03	<b>Trees:</b> Basic terminology, General Trees, Binary Trees, Tree Traversing: in-order, pre-order and post-order traversal, building a binary search tree, Operations on Binary Trees - Expression Manipulations - Symbol Table construction, Height Balanced Trees (AVL), B-trees, B+-trees	<b>07L</b>
UNIT-04	<b>Graphs:</b> Basic definitions, representations of directed and undirected graphs, the single-source shortest path problem, the all-pair shortest path problem, traversals of directed and undirected graphs, directed acyclic graphs, strong components, minimum cost spanning tress, articulation points and biconnected components, graph matching.	<b>06L</b>
UNIT-05	<b>Sorting and Searching Techniques:</b> Bubble sorting, Insertion sort, Selection sort, Shell sort, Merge sort, Heap and Heap sort, Quick sort, Radix sort and Bucket sort, Address calculation, Sequential searching, Binary Searching, Index searching, Hash table methods.	<b>06L</b>
<b>Course Outcomes</b> Upon successful completion of the course, the students will be able to CO1: Interpret and compute asymptotic notations of an algorithm to analyze the time complexity. CO2: Use of linear and non-linear data structures as the foundational base for computer solutions to problems. CO3: Demonstrate the ability to implement various types of static and dynamic lists. CO4: Implement binary trees, binary tree traversals, and binary search trees. CO5: Implement various types of sorting algorithms.		
<b>Books and References</b> <ol style="list-style-type: none"> <li>1. An Introduction to Data Structures with applications by J.P. Tremblay and P.G. Sorenson, Tata McGraw Hill.</li> <li>2. Data structures, Algorithms ad Applications in C++ by Sartaj Sahni, WCB/McGraw Hill.</li> <li>3. Data Structures and Algorithms by Alfred V. Aho, Jeffrey D. Ullman and John E. Hopcroft, Addison Wesley.</li> <li>4. Data Structures using C by Y. Langsam, M. J. Augenstein and A. M. Tenenbaum, Pearson Education.</li> <li>5. Data Structures – A Pseudocode Approach with C by Richard F. Gilberg and Behrouz A. Forouzan, Thomson Brooks /Cole</li> </ol>		



Course Name: <b>Object Oriented Programming</b>		
Course Code: <b>EC-353</b>		
Course Type: <b>Discipline Elective-II</b>		
Contact Hours/Week: <b>3L</b>		Course Credits: <b>03</b>
<b>Course Objectives</b> <ul style="list-style-type: none"> <li>To impart knowledge about the concept of Object Oriented programming.</li> <li>To introduce the fundamental concepts relevant to Arrays, Pointers and Functions, Classes, Objects, etc.</li> <li>To enable the students to understand the standard library, exception handling, streams and files</li> </ul>		
<b>Unit Number</b>	<b>Course Content</b>	<b>Contact Hours</b>
UNIT-01	<b>Concepts of Object-Oriented Programming:</b> Object Oriented Programming Paradigm, Basic concepts of OOPs, Benefits of OOPs, and Introduction to object oriented design and development, Design steps, Design example, Object oriented languages, Comparison of structured and object-oriented programming languages.	<b>06L</b>
UNIT-02	<b>Arrays, Pointers and Functions:</b> Arrays, Storage of arrays in memory, Initializing Arrays, MultiDimensional Arrays, Pointers, accessing array elements through pointers, passing pointers as function arguments, Arrays of pointers, Pointers to pointers, Functions, Arguments, Inline functions, Function Overloading Polymorphism.	<b>06L</b>
UNIT-03	<b>Classes and Objects:</b> Data types, operators, expressions, control structures, arrays, strings, Classes and objects, access specifiers, constructors, destructors, operator overloading, type conversion. Storage classes: Fixed vs. Automatic declaration, Scope, Global variables, register specifier, Dynamic memory allocation.	<b>07L</b>
UNIT-04	<b>Inheritance:</b> Inheritance, single Inheritance, Multiple Inheritance, Multi-level inheritance, hierarchical inheritance, hybrid inheritance, Virtual functions. Streams and Files: Opening and closing a file, File pointers and their manipulations, Sequential Input and output operations, multi-file programs, Random Access, command line argument, string class, Date class, Array class, List class, Queue class, User defined class, Generic Class.	<b>07L</b>
UNIT-05	<b>Exception Handling and Graphics:</b> List of exceptions, catching exception, handling exception. Text Mode, Graphics mode functions, Rectangles, and Lines, Polygons and Inheritance, Sound and Motion, Text in Graphics Mode. Standard Template Library: Standard Template Library, Overview of Standard Template Library, Containers, Algorithms, Iterators, Other STL Elements, Container Classes, General Theory of Operation, Vectors.	<b>10L</b>
<b>Course Outcomes</b> Upon successful completion of the course, the students will be able to CO1: Understand the concept of object oriented paradigm and programming. CO2: Apply the concept of polymorphism and inheritance. CO3: Implement exception handling and templates. CO4: Handling of files and streams during programming.		
<b>Books and References</b> <ol style="list-style-type: none"> <li>Object Oriented programming with C++ by E. Balagurusamy, Tata McGraw Hill.</li> <li>The C++ programming Language by Bjarne Stroustrup, Addison Wesley.</li> <li>Object Oriented Analysis and Design with Applications by Grady Booch, Addison Wesley.</li> <li>The Complete Reference Visual C++ by Chris H. Pappas and William H. Murray, Tata McGraw Hill.</li> <li>C++ Primer by S. B. Lippman, Josee Lajoie and Barbara E. Moo, Pearson Education.</li> </ol>		

Course Name: <b>Data Communication and Computer Networks</b>		
Course Code: <b>EC-341</b>		
Course Type: <b>Discipline Elective-III</b>		
Contact Hours/Week: <b>3L</b>		Course Credits: <b>03</b>
<b>Course Objectives</b> <ul style="list-style-type: none"> <li>To introduce basic concepts of Data communication with different models. Enumerate the physical layer, Data Link Layer, Network Layer, Transport Layer and Application Layer, explanation of the function(s) of each layer.</li> <li>Understanding of switching concept and different types of switching techniques</li> </ul>		
<b>Unit Number</b>	<b>Course Content</b>	<b>Contact Hours</b>
UNIT-01	<b>Introduction to Data Communication:</b> Goals and Applications of Networks, Wireless Network, Interfaces and services. Reference Models: The OSI reference model, TCP/IP reference model.	<b>07L</b>
UNIT-02	<b>Physical Layer:</b> Analog and Digital, Analog Signals, Digital Signals, Analog versus Digital, Data Rate Limit, Transmission Impairments, Line Coding, Block Coding, Sampling, Transmission Mode, Modulation of Digital Data, Telephone Modems, Modulation of Analog Signal, FDM, WDM, TDM, Guided Media, Unguided Media, Switching.	<b>05L</b>
UNIT-03	<b>Data Link Layer:</b> Data Link Layer Design Issues, Services Provided to Network Layers, Framing, Error Control, Flow Control, Error Detection and Correction, Elementary Data Link Protocols, An Unrestricted Simplex Protocol, A Simplex Stop-and-Wait Protocol, Simplex Protocol for a Noisy Channel, Sliding Window Protocols, A protocol using go-back-N, A Protocol using Selective Repeat, Example Data Link Protocol-HDLC, PPP.	<b>07L</b>
UNIT-04	<b>Medium Access Sublayer:</b> Channel Allocations, Random Access, ALOHA, Carrier Sense Multiple Access Protocols, Collision Free Protocols, Limited Contention Protocols, Controlled Access, Channelization, Wired LANs: Ethernet, Wireless LANs	<b>05L</b>
UNIT-05	<b>Network Layer:</b> Internetworks, Addressing, Routing, ARP, IP, ICMP, IPV6, Unicast Routing, Unicast Routing Protocol, Multicast Routing, Multicast Routing Protocols	<b>04L</b>
UNIT-06	<b>Transport Layer:</b> Process to Process Delivery, User Datagram Protocol (UDP), Transmission Control Protocol (TCP), Data Traffic, Congestion, Congestion Control, Quality of Service, Techniques to Improve QOS, Integrated Services, QOS in Switched Networks	<b>08L</b>
UNIT-07	<b>Application Layer:</b> Design Issues of the Layer, Domain Name Systems, File Transfer, http, Web Documents, Virtual Terminals.	<b>--</b>
<b>Course Outcomes</b> Upon successful completion of the course, the students will be able to CO1: Give the basic information of how a network can be designed, possible choice of various models for designing a network. CO2: Understand the protocol layer specific communication between two trusted entities. CO3: Analyse the possible attacks on a network to interrupt the transmission and mislead the communication between different entities. CO4: Analyse the shortest path over which data can be transmitted, able to design a routing protocol implementing security mechanisms for secure transmission of data from sender to the receiver. CO5: Understand the subject based on course work, assignments and through implementation on a specific platform CO6: Design a network topology with the available networking elements and can implement a routing protocol along with secure mechanisms ensuring the error free transmission of data.		
<b>Books and References</b> <ol style="list-style-type: none"> <li>Data Communications and Networking by Behrouz A Ferouzan, McGraw Hill Education.</li> <li>Data and Computer Communication by Stallings William, Pearson Education.</li> <li>Computer Networks by A.S. Tanenbaum, Pearson Education.</li> <li>An Engineering Approach on Computer Networking by S. Keshav, Addison Welsey.</li> <li>Introduction to Data Communications and Networking by Wayne Tomasi, Pearson.</li> </ol>		

Course Name: <b>Spread Spectrum and Wideband Communication</b>		
Course Code: <b>EC-342</b>		
Course Type: <b>Discipline Elective-III</b>		
Contact Hours/Week: <b>3L</b>		Course Credits: <b>03</b>
<b>Course Objectives</b> <ul style="list-style-type: none"> <li>To impart knowledge about the basic spread spectrum techniques that are used in CDMA based cellular communication systems, including direct sequence spread spectrum and frequency-hopped spread spectrum.</li> <li>To introduce the fundamental mathematical concepts relevant to design aspects of the PN sequence generators.</li> <li>To enable the students to understand the factors that affect the practical implementation of IS-95, 3G,4G and 5G systems.</li> </ul>		
<b>Unit Number</b>	<b>Course Content</b>	<b>Contact Hours</b>
UNIT-01	<b>Introduction:</b> Concept of Multiple Access Systems, Narrowband and Broadband Systems, Advantages of Spread Spectrum Systems.	<b>02L</b>
UNIT-02	<b>Principles of Direct Spread Spectrum:</b> Direct Spectrum System: Definition and Concepts, Spreading Sequences and Waveforms, Random Binary Sequence, Shift-Register Sequences, Periodic Auto Correlations, Polynomials over The Binary Field, Systems with PSK Modulation, Power Spectral Density of DSS-CDMA, Pulsed Interference, De-Spreading with Matched Filter.	<b>10L</b>
UNIT-03	<b>Spreading Code Acquisition and Tracking:</b> Initial Code Acquisition, Acquisition Strategy: Serial Search, Parallel Search, Multi-Dwell Detection, False Alarm and Miss Probability for Matched Filter Receiver, False Alarm and Miss Probability for Radiometer, Mean Overall Acquisition Time for Serial Search.	<b>06L</b>
UNIT-04	<b>Performance of Spread Spectrum System:</b> Link Performance of Direct Sequence Spread Spectrum CDMA In (I) Additive White Noise Channel (Ii) Multipath Fading Channel. Concept of Rake Receiver, Performance of RAKE Receiver in Multipath Fading.	<b>06L</b>
UNIT-05	<b>Frequency Hoped Systems:</b> Concepts and Characteristics, Modulations, MFSK, Hybrid Systems, Frequency Synthesizers, Direct Frequency Synthesizer, Digital Frequency Synthesizer, Indirect Frequency Synthesizers.	<b>06L</b>
UNIT-06	<b>Spread Spectrum and Wide-band systems:</b> CDMA-IS-95: Forward link Channels, Reverse link Channels, Overview of 3G, 4G and 5G Systems. Ultra-wideband communication: definition, features and applications.	<b>06L</b>
<b>Course Outcomes</b> Upon successful completion of the course, the students will be able to CO1: Identify spread spectrum techniques that are used in CDMA based cellular communication systems, including direct sequence spread spectrum and frequency-hopped spread spectrum. CO2: Apply the principles of linear algebra to design PN sequence generators. CO3: Analyze the performance of CDMA systems in various wireless Channels CO4: Assess the practical implementation of IS-95, 3G,4G and 5G systems.		
<b>Books and References</b> <ol style="list-style-type: none"> <li>Principles of Spread Spectrum Communication Systems by Don Torrieri, Springer Science &amp; Business Media, Inc.</li> <li>CDMA: Principles of Spread Spectrum Communication by Andrew J. Viterbi, Addison- Wesley Publishing Company.</li> <li>Introduction to CDMA Wireless Communications by Mosa Ali Abu-Rgheff, Elsevier Academic Press.</li> <li>Code Division Multiple Access-CDMA by R. Michael Buehrer, Morgan &amp; Claypool Publishers Series.</li> <li>CDMA Systems Engineering Handbook by Jhong S. Lee and Leonard E. Miller, Artech House Publishers.</li> <li>IS-95 CDMA and CDMA-2000 by Vijay K Garg, Pearson Education.</li> <li>OFDM for Wireless Communications Systems by Ramjee Prasad, Artech House, Inc.</li> </ol>		

Course Name: <b>Satellite Communication and Radar</b> Course Code: <b>EC-343</b> Course Type: <b>Discipline Elective-III</b>		
Contact Hours/Week: <b>3L</b>		Course Credits: <b>03</b>
<b>Course Objectives</b> <ul style="list-style-type: none"> <li>To impart knowledge about the Orbital Mechanism, satellites and satellite system, satellite link design.</li> <li>To introduce the basic functioning of a radar system and to make the students understand this by taking a specific example of MTI and PULSE Doppler radar.</li> </ul>		
Unit Number	Course Content	Contact Hours
UNIT-01	<b>Orbital Mechanics:</b> Overview of Satellite Communications, Satellite orbit and orbital equations, Kepler's laws of planetary motion, Locating satellite in the orbit, Locating satellite with respect to earth, Orbital Elements, Look Angle Determination, Orbital Perturbations, Orbit Determination, Space Launch Vehicles and Rockets, Placing Satellites Into Geostationary Orbit, Orbital Effects in Communications Systems Performance	<b>08L</b>
UNIT-02	<b>Satellites:</b> Satellite subsystems, Attitude and orbit control system, Telemetry tracking command and monitoring, Power system, communication subsystem, Satellite antennas	<b>07L</b>
UNIT-03	<b>Satellite Link Design:</b> Transmission Theory, System Noise Temperature and G/T Ratio, Design of Downlinks, Ku-Band GEO Satellite Systems, Uplink Design, Design for Specified CNR: Combining CNR and C/I Values in Satellite Links, System Design for Specific Performance	<b>08L</b>
UNIT-04	<b>Introduction to Radar System:</b> Introduction to Radar, Applications of Radar, Radar Frequencies, Working Principle of Radar, Radar block diagram, Simple form of Radar Equation, Minimum detectable signal, Range to a target, Pulse repetition frequency and range ambiguities. Radar System: Maximum radar range, Detection of Signals in Noise, Receiver noise and SNR, Integration of radar pulse, Radar cross section of targets, RADAR detection, Range & Doppler measurements, tracking.	<b>10L</b>
UNIT-05	<b>MTI and Pulse Doppler Radar:</b> Concept of Doppler Effect, Introduction to Doppler and MTI Radar, Doppler frequency shift, CW Doppler radar, MTI radar, Delay line cancellers, Staggered PRF, pulse Doppler radar, FM-CW radar, Tacking Radar, Sequential lobing, Conical Scan, Monopulse, Acquisition, Comparison of Track, Radar Transmitter/Receiver.	<b>09L</b>
<b>Course Outcomes</b> Upon successful completion of the course, the students will be able to CO1: Understand the orbital and functional principles of satellite communication systems CO2: Architect, interpret, and select appropriate technologies for implementation of specified satellite communication systems CO3: Analyze and evaluate a satellite link and suggest enhancements to improve the link performance. CO4: Understand the basic working of RADAR, Doppler shift. CO5: Understand the working of a MTI, Pulse Doppler Radar and Tracking radar		
<b>Books and References</b> <ol style="list-style-type: none"> <li>Satellite Communications by Timothy Pratt and Jeremy E., Wiley.</li> <li>Satellite Communications by Dennis Roddy, Tata McGraw Hill.</li> <li>Digital Satellite Communications by Tri. T. Ha, Tata McGraw Hill.</li> <li>Introduction to Radar Systems by Skolnik, Tata McGraw Hill.</li> <li>Radar Handbook by Skolnik, McGraw-Hill</li> <li>Radar Principles by Peyton Z. Peebles, John Wiley and Sons (2004).</li> <li>Radar Foundation for Imaging &amp; Advanced Concepts by R.J Sullivan, PHI, 2004</li> </ol>		

Course Name: <b>Digital Image Processing</b>		
Course Code: <b>EC-361</b>		
Course Type: <b>Discipline Elective-IV</b>		
Contact Hours/Week: <b>3L</b>		Course Credits: <b>03</b>
<b>Course Objectives</b> <ul style="list-style-type: none"> <li>● To understand the basics of image formation</li> <li>● To understand image enhancement in spatial and frequency domain.</li> <li>● To understand the concepts of image compression, image segmentation and morphological operation on image.</li> </ul>		
Unit Number	Course Content	Contact Hours
UNIT-01	<b>Introduction:</b> Steps in Digital Image Processing, Components of an Image Processing system, Applications. Human Eye and Image Formation; Sampling and Quantization, Basic Relationship among pixels- neighbour, connectivity, regions, boundaries, distance measures.	06
UNIT -02	<b>Image Enhancement:</b> Spatial Domain, Gray Level transformations, Histogram, Arithmetic/Logical Operations, Spatial filtering, Smoothing & Sharpening Spatial Filters; Frequency Domain- 2-D Fourier transform, Smoothing and Sharpening Frequency Domain Filtering; Convolution and Correlation Theorems.	08
UNIT -03	<b>Image Restoration:</b> Image degradation/restoration process, noise models, restoration in the presence of noise only-spatial filtering, periodic noise reduction by frequency domain filtering, Linear, Position-Invariant Degradation, Estimating the Degradation Function, Inverse filtering, Wiener Filtering, Constrained Least Square Filtering, Geometric filter, Geometric Transformation.	08
UNIT -04	<b>Image Compression:</b> Redundancies- Coding, Interpixel, Psycho visual; Fidelity, Source and Channel Encoding, Elements of Information Theory; Loss Less and Lossy Compression; Run length coding, Differential encoding, DCT, Vector quantization, entropy coding, LZW coding; Image Compression Standards-JPEG, JPEG 2000, MPEG; Video compression.	08
UNIT -05	<b>Image Segmentation:</b> Discontinuities, Edge Linking and boundary detection, Thresholding, Region Based Segmentation, Segmentation by Morphological Watersheds; binary morphology- erosion, dilation, opening and closing operations, Hit-or-Miss Transform, some basic morphological algorithms.	08
<b>Course Outcomes</b> Upon successful completion of the course, the students will be able to: CO1: Understand the basic image formation model and application of image processing. CO2: Enhancement of image in spatial and frequency domain. CO3: Understand the Image Restoration CO4: Analyse the different image compression techniques. CO5: Understand the image segmentation and morphological operations		
<b>Books and References</b> <ol style="list-style-type: none"> <li>1. Digital Image Processing- R. C. Gonzalez and R. E. Woods, Pearson Education</li> <li>2. Fundamentals of Digital Image processing- A. K. Jain, Pearson Education</li> <li>3. Digital Image Processing using MATLAB- R. C. Gonzalez , R. E. Woods and S. L Eddins, Pearson Education.</li> <li>4. Digital Image Processing and Analysis- Chanda and Mazumdar, PHI</li> <li>5. N.J. Fliege, "Multirate Digital Signal Processing", John Wiley and Sons.</li> </ol>		

Course Name: <b>Speech Processing</b> Course Code: <b>EC-362</b> Course Type: <b>Discipline Elective-IV</b>		
Contact Hours/Week: <b>3L</b>		Course Credits: <b>03</b>
<b>Course Objectives:</b> <ul style="list-style-type: none"> <li>To introduce speech production and related parameters of speech.</li> <li>To show the computation and use of techniques such as short time Fourier transform, linear predictive coefficients and other coefficients in the analysis of speech.</li> <li>To understand different speech modeling procedures such as Markov and their implementation issues.</li> </ul>		
Unit Number	Course Content	Contact Hours
UNIT-01	<b>BASIC CONCEPTS:</b> Speech Fundamentals: Articulatory Phonetics Production and Classification of Speech Sounds; Acoustic Phonetics Acoustics of speech production; Review of Digital Signal Processing concepts; Short-Time Fourier Transform, Filter-Bank and LPC Methods.	07L
UNIT-02	<b>SPEECH ANALYSIS:</b> Features, Feature Extraction and Pattern Comparison Techniques: Speech distortion measures—mathematical and perceptual – Log–Spectral Distance, Cepstral Distances, Weighted Cepstral Distances and Filtering, Likelihood Distortions, Spectral Distortion using a Warped Frequency Scale, LPC, PLP and MFCC Coefficients, Time Alignment and Normalization – Dynamic Time Warping, Multiple Time – Alignment Paths.	07L
UNIT-03	<b>SPEECH MODELING:</b> Hidden Markov Models: Markov Processes, HMMs – Evaluation, Optimal State Sequence – Viterbi Search, Baum-Welch Parameter Re-estimation, Implementation issues.	09L
UNIT-04	<b>SPEECH RECOGNITION:</b> Large Vocabulary Continuous Speech Recognition: Architecture of a large vocabulary continuous speech recognition system – acoustics and language models – n-grams, context dependent sub-word units; Applications and present status.	09L
UNIT-05	<b>SPEECH SYNTHESIS:</b> Text-to-Speech Synthesis: Concatenative and waveform synthesis methods, sub-word units for TTS, intelligibility and naturalness – role of prosody, Applications and present status.	08L
<b>Course Outcomes</b> Upon completion of the course, students will be able to: CO1: Model speech production system and describe the fundamentals of speech. CO2: Extract and compare different speech parameters. CO3: Choose an appropriate statistical speech model for a given application. CO4: Design a speech recognition system. CO5: Use different speech synthesis techniques.		
<b>Books and References</b> <ol style="list-style-type: none"> <li>Lawrence Rabiner and Biing-Hwang Juang, "Fundamentals of Speech Recognition", Pearson Education, 2003.</li> <li>Daniel Jurafsky and James H Martin, "Speech and Language Processing – An Introduction to Natural Language Processing, Computational Linguistics, and Speech Recognition", Pearson Education, 2002.</li> <li>Frederick Jelinek, "Statistical Methods of Speech Recognition", MIT Press, 1997.</li> </ol>		

Course Name: <b>Estimation and Detection Theory</b> Course Code: <b>EC-363</b> Course Type: <b>Discipline Elective-IV</b>		
Contact Hours/Week: <b>3L</b>		Course Credits: <b>03</b>
<b>Course Objectives</b> <ul style="list-style-type: none"> <li>● To enable the students to acquire the fundamental concepts of Signal Detection and Estimation.</li> <li>● To get familiarize with different hypotheses in detection and estimation problems.</li> <li>● To introduce the methods of detection and estimation of signals in white and non-white Gaussian noise.</li> <li>● To familiarize with the detection of random signals.</li> </ul>		
Unit Number	Course Content	Contact Hours
UNIT-01	Review of Probability, Random variables, random processes, filtered random processes, Ensemble averages, correlation, covariance, power spectrum, cross power spectrum, Ergodicity, time averages, biased & unbiased estimators, consistent estimators. Problem Formulation and Objective of Signal Detection and Signal Parameter Estimation in Discrete-Time Domain.	07L
UNIT-02	Statistical Decision Theory: Bayesian, Min- Max, and Neyman-Pearson Decision Rules, Likelihood Ratio, Receiver Operating Characteristics, Composite Hypothesis Testing, Locally Optimum Tests, Detector Comparison Techniques, Asymptotic Relative Efficiency.	07L
UNIT-03	Detection of Deterministic Signals: Matched Filter Detector and its Performance; Generalized Matched Filter; Detection of Sinusoid with Unknown Amplitude, Phase, Frequency and Arrival Time, Linear Model.	07L
UNIT-04	Random Signals: Estimator-Correlator, Linear Model, General Gaussian Detection, Detection of Gaussian Random Signal with Unknown Parameters, Weak Signal Detection. Estimation of Signal Parameters: Minimum Variance Unbiased Estimation.	07L
UNIT-05	Fisher Information Matrix, Cramer-Rao Bound, Sufficient Statistics, Minimum Statistics, Complete Statistics; Linear Models; Best Linear Unbiased Estimation; Maximum Likelihood Estimation, Invariance Principle; Estimation Efficiency.	07L
UNIT-06	Bayesian Estimation: Philosophy, Nuisance Parameters, Risk Functions, Minimum Mean Square Error Estimation, Maximum a posteriori estimation	07L
<b>Course Outcomes</b> Upon successful completion of the course, the students will be able to CO1: Understand the basic concepts of signal detection and estimation. CO2: Understand the different hypotheses in detection and estimation problems. CO3: Understand the conceptual basics of detection and estimation of signals in white Gaussian noise. CO4: Understand the detection of random signals. CO5: Understand the time varying waveform detection and its estimation		
<b>Books and References</b> <ol style="list-style-type: none"> <li>1. Fundamentals of Statistical Signal Processing: Estimation Theory, Vol. I, by S. M. Kay, Prentice Hall PTR.</li> <li>2. Fundamentals of Statistical Signal Processing: Detection Theory, Vol. II by S. M. Kay, Prentice Hall PTR.</li> <li>3. Detection, Estimation and Modulation Theory: Part I, II, and III by H. L. Van Trees, John Wiley, NY.</li> <li>4. An Introduction to Signal Detection and Estimation by H. V. Poor, Springer.</li> </ol>		

Course Name: <b>FPGA SoC Design</b> Course Code: <b>EC-431</b> Course Type: <b>Discipline Elective-V</b>		
Contact Hours/Week: <b>3L</b>		Course Credits: <b>03</b>
<b>Course Objectives</b> <ul style="list-style-type: none"> <li>To give the student an understanding of basics of system on chip and platform-based design</li> <li>To enable the student to understand issues related to FPGA and tools related to design and implementation.</li> <li>Analyse the functional and nonfunctionally performance of the system early in the design process to support design decisions.</li> <li>To impart knowledge about the system-level and SoC design methodologies and tools.</li> <li>Describe examples of applications and systems developed using a co-design approach.</li> </ul>		
<b>Unit Number</b>	<b>Course Content</b>	<b>Contact Hours</b>
UNIT-01	<b>INTRODUCTION TO DESIGN FLOW</b> ASICs: Types of ASICs, VLSI Design flow, Programmable ASICs, Antifuse, SRAM, EPROM, EEPROM based ASICs. Programmable ASIC logic cells and I/O cells. Programmable interconnects. Latest Version, FPGAs and CPLDs and Soft-core processors. Design Specification and Resource Scoping: Trade off issues at System Level: Optimization with regard to speed, area and power, asynchronous and low power system design. ASIC physical design issues, System Partitioning, Power Dissipation, Partitioning Methods.	08L
UNIT-02	<b>SYSTEM-ON-CHIP DESIGN:</b> SoC Block-Based Design: System-On-Chip Design, SoC Design Flow, Platform-based and IP based SoC Designs, Basic Concepts of Bus-Based Communication Architectures, On-Chip Communication Architecture Standards, Low-Power SoC Design, Performance Evaluation Methods for Multiprocessor System-on-Chip Design.	07L
UNIT-03	<b>FPGA DESIGN:</b> FPGA Design Environment: Introduction, Scripting Environment, Interaction with Version Control Software, A Regression Test System, Common Tools in the FPGA Design Environment, Challenges that FPGAs Create for Board Design, Engineering Roles and Responsibilities, FPGA Engineers, Design Flows for Creating the FPGA Pinout, Board Design Check List for a Successful FPGA Pin-Out.	08L
UNIT-04	<b>RTL DESIGN:</b> Power Analysis and RTL Design: Introduction, Power Basic, Key Factors in Accurate Power Estimation, Power Estimation Early in the Design Cycle, Simulation Based Power Estimation, Best Practices for Power Estimation, Recommendations for Engineers with an ASIC Design Background, Writing Effective HDL, Analyzing the RTL Design.	07L
UNIT-05	<b>THE HARDWARE/SOFTWARE CONSIDERATIONS:</b> Introduction, Software Interface, Definition of Register Address Map, Use of the Register Address Map, Hardware/Software Co-Design and Verification, High performance algorithms for ASICs/ SoCs as case studies – Canonic Signed Digit Arithmetic, Distributed Arithmetic.	07L
<b>Course Outcomes</b> Upon successful completion of the course, the students will be able to CO1: Demonstrate VLSI tool-flow and appreciate FPGA architecture. CO2: Understand the basics of system on chip and on chip communication architectures. CO3: Understand the issues involved in ASIC design, including technology choice, design management, tool flow.		
<b>Books and References</b> <ol style="list-style-type: none"> <li>Embedded System Design: Modeling, Synthesis and Verification by D. Gajski, S. Abdi, A. Gerstlauer, G. Schirner, Springer.</li> <li>Synthesis and Optimization of Digital Circuits by G. De Micheli, McGraw-Hill.</li> <li>EDA for IC System Design, Verification, and Testing by Louis Scheffer, Luciano Lavagno, and Grant Martin.</li> <li>Verilog Digital Computer Design by Mark Gordon Arnold.</li> </ol>		



Course Name: <b>Neural Network and Deep Learning</b> Course Code: <b>EC-432</b> Course Type: <b>Discipline Elective-V</b>		
<b>Contact Hours/Week: 3L</b>		<b>Course Credits: 03</b>
<b>Course Objectives</b> The objective of this course is to develop artificial intelligence and machine learning techniques for solving various real-life problems.		
<b>Unit Number</b>	<b>Course Content</b>	<b>Lectures</b>
UNIT-01	<b>NEURAL NETWORK LEARNING</b> Models of a Neuron, Multilayer Perceptron (MLP), Back Propagation Learning, Role of Loss Functions and Optimization, Gradient Descent, MLP for Classification and Regression	<b>06L</b>
UNIT-02	<b>DEEP LEARNING</b> Unsupervised Learning with Deep Network, Autoencoders, Convolutional Neural Network (CNN), Building blocks of CNN, Transfer Learning, Recurrent Neural Networks (RNN), Effective training in Deep Network-early stopping, Dropout, Batch Normalization, Instance Normalization, Group Normalization	<b>07L</b>
UNIT-03	Effective training in Deep Net- early stopping, Dropout, Batch Normalization, Instance Normalization, Group Normalization	<b>07L</b>
UNIT-04	<b>RECENT TRENDS IN DEEP NETWORK ARCHITECTURES</b> Generative Adversarial Networks (GAN) - Fundamentals and Applications, Classical Supervised Tasks with Deep Networks, CNN and RNN for Signal Classification, Convolutional Networks for Image Segmentation, Networks for Image Denoising, Object Detection	<b>08L</b>
UNIT-05	Classical Supervised Tasks with Deep Learning, Image Denoising, Semantic Segmentation, Object Detection etc., LSTM Networks, Generative Modeling with DL, Variational Autoencoder, Generative Adversarial Network	<b>08L</b>
<b>Course Outcomes:</b> Upon successful completion of the course, the students will be able to CO1: Understand the fundamentals of artificial intelligence and machine learning. CO2: Understand the concepts of selected machine learning algorithms. CO3: Understand the concepts of neural network learning. CO4: Understand the concepts of deep neural networks.		
<b>Books and References:</b> 1. Marc Peter Deisenroth, A. Aldo Faisal, Cheng Soon Ong, "Mathematics for Machine Learning", Cambridge University Press. 2. Ian Goodfellow, Yoshua Bengio, and Aaron Courville, "Deep Learning", MIT Press Ltd, Illustrated edition. 3. Richard O. Duda, Peter E. Hart, David G. Stork, "Pattern Classification", John Wiley & Sons Inc.		

Course Name: <b>VLSI Testing</b>		
Course Code: <b>EC-433</b>		
Course Type: <b>Discipline Elective-V</b>		
Contact Hours/Week: <b>3L</b>		Course Credits: <b>03</b>
<b>Course Objectives</b> <ul style="list-style-type: none"> <li>• To impart knowledge about the basics of testing techniques for VLSI circuits and Test Economics</li> <li>• To introduce the fundamental concepts of Design for Testability.</li> <li>• To enable the students to generate the test patterns.</li> </ul>		
<b>Unit Number</b>	<b>Course Content</b>	<b>Contact Hours</b>
UNIT-01	<b>Basics of Testing And Fault Modeling:</b> Introduction to Testing, Test process and ATE, Faults in digital circuits, Modeling of faults, Logical Fault Models, Fault detection, Fault location, Fault dominance, Logic Simulation, Types of simulation, Delay models, Gate level Event-driven simulation.	<b>02L</b>
UNIT-02	<b>Test Generation for Combinational and Sequential Circuits:</b> Logic simulation and fault simulation, Testability measures, Test generation for combinational logic circuits, Testable combinational logic circuit design, Test generation for sequential circuits, Design of testable sequential circuits.	<b>8L</b>
UNIT-03	<b>Design For Testability:</b> Ad-hoc design, Generic scan based design, Classical scan based design – System level DFT approaches, Memory test.	<b>9L</b>
UNIT-04	<b>Self-Test and Test Algorithms Built-In Self-Test:</b> Test pattern generation for BIST, Circular BIST, BIST Architectures, Testable Memory Design, Test algorithms, Test generation for Embedded RAMs, Logic BIST and EDT, Boundary Scan, System test and core test.	<b>12L</b>
UNIT-05	<b>Fault Diagnosis Logic Level Diagnosis:</b> Diagnosis by UUT reduction, Fault Diagnosis for Combinational Circuits, Self-checking design, System Level Diagnosis.	<b>03L</b>
<b>Course Outcomes</b> Upon successful completion of the course, the students will be able to CO1: Apply the concepts in testing which can help them design a better yield in IC design. CO2: Tackle the problems associated with testing of semiconductor circuits at earlier design levels so as to significantly reduce the testing costs. CO3: Identify the design for testability methods for combinational & sequential CMOS circuits		
<b>Books and References</b> <ol style="list-style-type: none"> <li>1. M. Abramovici, M.A. Breuer and A.D. Friedman, "Digital Systems and Testable Design", Jaico Publishing House.</li> <li>2. M.L. Bushnell and V.D. Agrawal, "Essentials of Electronic Testing for Digital, Memory and Mixed-Signal VLSI Circuits", Kluwer Academic Publishers.</li> <li>3. P.K. Lala, "Digital Circuit Testing and Testability", Academic Press, 2002.</li> <li>4. A.L. Crouch, "Design Test for Digital IC's and Embedded Core Systems", Prentice Hall International.</li> </ol>		

# **Open Elective Courses**

Course Name: <b>Microcontroller and Embedded System</b>		
Course Code: <b>EC-301</b>		
Course Type: <b>Open Elective</b>		
Contact Hours/Week: <b>3L</b>		Course Credits: <b>03</b>
<b>Course Objectives</b> <ul style="list-style-type: none"> <li>• To discuss the microcontroller architecture, programming techniques, interrupts timers and assembly language.</li> <li>• The concepts of ARM architecture and real-time operating system.</li> <li>• To provide theoretical knowledge enabling integration of hardware and software in microcontroller-based applications.</li> <li>• To impart ability to put together processor, peripherals and memory and build a real time system</li> </ul>		
Unit Number	Course Content	Contact Hours
UNIT-01	<b>MICROCONTROLLER</b> Introduction and Characteristics of Embedded Systems, Hardware and Software Co-Design, RISC Vs CISC, MCS-51 Family Overview, Architecture, Pin Diagram, Addressing Modes, Instruction Set, Instruction Types and applications of 8051 in ASIC and FPGA designs.	<b>05L</b>
UNIT-02	<b>PROGRAMMING</b> Assembly Programming, Timer Registers, Timer Modes, Overflow Flags, System clock, Timer, Counter, Interrupts, Baud Rate Generation, Serial Port Register, Modes of Operation, Processing Interrupts, Interrupt Service Routines, Look-up Tables.	<b>10L</b>
UNIT-03	<b>EMBEDDED SOFTWARE DEVELOPMENT</b> Software development flow, Polling, Multitasking systems, Architecture of an RTOS, Important features of RTOS, Embedded Systems Programming, Locks and Semaphores, Operating System Timers and Interrupts, Exceptions, Tasks, Task states and scheduling, Task structures, Synchronization, Communication and concurrency, Semaphores, Real-time clock	<b>08L</b>
UNIT-04	<b>32-BIT CORTEX-M ARCHITECTURE</b> Introduction to CPU Architecture, Memory model, Registers, Modes, Exceptions, Interrupts, Exception handlers, interrupt controllers, Power modes, Hardware features and optimizations, Advanced bus standards like AMBA, The NVIC on ARM Cortex-M.	<b>07L</b>
UNIT-05	<b>INTRODUCTION TO ARM ARCHITECTURE</b> Introduction to ARM architecture family, Syntax, Addressing modes and operands, Memory access instructions. Logical, Shift and Arithmetic operations, Stack, Functions and control flow, Macros and Directives, Thumb and arm instruction differences, Development with Keil and Mbed, Applications like IoT and machine learning with cortex-M.	<b>04L</b>
<b>Course Outcomes</b> Upon successful completion of the course, the students will be able to CO1: Develop the assembly codes for the microcontrollers. CO2: Understand the functional role of embedded systems in real time. CO3: Understand the design concept of embedded systems.		
<b>Books and References</b> <ol style="list-style-type: none"> <li>1. The 8051 Microcontroller and Embedded Systems (2nd Ed.) by Mazidi Muhammad Ali, Pearson publications.</li> <li>2. The Definitive Guide to ARM Cortex-M3 processors (3rd Ed.) by Joseph Yiu, Newnes publication.</li> <li>3. Introduction to ARM Cortex-M Microcontrollers, Vol. 1 (5th Ed.) by Jonathan W. Valvano, Create Space.</li> <li>4. Real-Time Interfacing to ARM Cortex-M Microcontrollers, Vol. 2 (4th Ed.) by Jonathan W. Valvano, Create Space.</li> <li>5. Real-Time Operating Systems for ARM Cortex-M Microcontrollers, Vol. 3 (2nd Ed.) by Jonathan W. Valvano, Create Space.</li> </ol>		

Course Name: <b>MEMS and Sensor Design</b>		
Course Code: <b>EC-302</b>		
Course Type: <b>Open Elective</b>		
Contact Hours/Week: <b>3L</b>		Course Credits: <b>03</b>
<b>Course Objectives</b> <ul style="list-style-type: none"> <li>● To impart knowledge about the need and applications of microsystems in engineering.</li> <li>● To introduce the fundamental concepts relevant to fabrication and machining process of MEMS sensors and actuators.</li> <li>● To enable the students to understand the various sensing and actuation mechanisms.</li> <li>● To enable the students to design and analysis MEMS technology-based devices</li> </ul>		
<b>Unit Number</b>	<b>Course Content</b>	<b>Contact Hours</b>
UNIT-01	<b>Introduction:</b> Introduction to MEMS and Microsystems, MEMS Classification, MEMS versus Microelectronics, Applications of MEMS in Various Industries, Some Examples of Microsensors, Microactuators, and Microsystems, Classification of Sensors, Scaling Laws in miniaturization, Materials for MEMS	<b>05L</b>
UNIT-02	<b>MEMS Fabrication:</b> Structure of Silicon, Single Crystal Growth Techniques, Photolithography, Oxidation, Diffusion, Ion Implantation, Physical Vapor Deposition, Chemical Vapor Deposition, Surface Micromachining, Bulk Micromachining, Overview of Etching, Isotropic and Anisotropic Etching, Wet Etchants, Etch Stop Techniques, Dry Etching, Micro- stereolithography, LIGA, SLIGA, Wafer Bonding	<b>12L</b>
UNIT-03	<b>Mechanical microsensors and microactuators:</b> Basic Modeling Elements in Mechanical, Electrical and Thermal Systems, Types of Beams: Fixed-Free (Cantilevers), Fixed-Fixed (Bridges), Fixed-Guided beams, Electrostatic sensing and Actuation: Parallel plate capacitor, Applications of parallel plate capacitors: Inertial sensor, Pressure sensor, Flow sensor, Parallel plate Actuators, Piezoresistive Sensors: Origin and Expressions of Piezoresistivity, Piezoresistive Materials and design of Piezoresistive Sensors, Piezoelectric effect, Piezoelectric materials, Design of Piezoelectric Principle based Sensors and Actuators	<b>12L</b>
UNIT-04	Thermal and Magnetic microsensors: Thermal Sensing and Actuation: Sensors and Actuators based on Thermal Expansion, Thermocouples, Thermoresistors, Shape Memory Alloy, Applications: Interital sensors, Flow sensors, Infrared sensors, Magnetic material for MEMS, magnetic sensing, and detection, mannetoresistive sensors, hall effect, magnetodiode, megnetotransitors, MEMS magnetic sensors	<b>05L</b>
UNIT-05	<b>RF-MEMS:</b> MEMS devices for RF Applications: High-Q Capacitors, Inductors, Switching devices and their Applications in RF Circuits, Mechanical, Electromechanical and Electromagnetic modelling	<b>06L</b>
<b>Course Outcomes</b> Upon successful completion of the course, the students will be able to CO1: Identify structural and sacrificial materials for MEMS. CO2: Describe the fabrication steps in designing various MEMS devices. CO3: Apply principles for the design of Sensor and actuators. CO4: Apply MEMS for different applications in various fields of engineering.		
<b>Books and References</b> <ol style="list-style-type: none"> <li>1. MEMS and Microsystems design and manufacture by Tai-Ran Hsu, Tata McGraw Hill.</li> <li>2. MEMS by N. P. Mahalik, Tata McGraw Hill.</li> <li>3. Foundations of MEMS by Chang Liu, Pearson Prentice Hall.</li> <li>4. Sensors and Transducers by M. J. Usher,McMillian Hampshire.</li> <li>5. Analysis and Design Principles of MEMS Devices by MinhangBao, Elesvier.</li> <li>6. Fundamentals of Microfabrication by M. Madou, CRC Press</li> <li>7. Microsensors by R.S. Muller, Howe, Senturia and Smith, IEEE Press.</li> <li>8. Semiconductor Sensors by S. M. Sze, Willy –Interscience Publications.</li> </ol>		

Course Name: <b>Communication Systems</b> Course Code: <b>EC-303</b> Course Type: <b>Open Elective</b>		
Contact Hours/Week: <b>3L</b>		Course Credits: <b>03</b>
<b>Course Objectives</b> <ul style="list-style-type: none"> <li>To introduce students to the principles and applications of communication systems</li> <li>To provide students with an understanding of analog, digital and pulse communication techniques</li> <li>To familiarize students with modulation and demodulation techniques</li> <li>To introduce students to multiplexing and error correction codes</li> <li>To provide students with an understanding of the basic concepts of information theory, noise, and channel capacity</li> </ul>		
<b>Unit Number</b>	<b>Course Content</b>	<b>Contact Hours</b>
UNIT-01	<b>Introduction to Communication Systems</b> Definition and scope of communication systems, Communication channels and media, Basic block diagram of communication systems, Signal Representation and Analysis, Time and frequency domain representation of signals, Analog and digital signals, Fourier series and Fourier transform.	05L
UNIT-02	<b>Modulation and multiplexing techniques</b> Amplitude modulation (AM), Frequency modulation (FM), Phase modulation (PM) Digital modulation techniques (ASK, FSK, PSK, QAM), Pulse modulation schemes: pulse amplitude modulation (PAM), pulse position modulation (PPM), pulse width modulation (PWM), pulse code modulation (PCM), delta modulation (DM), and adaptive delta modulation (ADM). Frequency division multiplexing (FDM), Time division multiplexing (TDM), Code division multiplexing (CDM).	08L
UNIT-03	<b>Analog and Digital radio receivers</b> Types of analog radio receivers: Superheterodyne, TRF (Tuned Radio Frequency), and Regenerative, Demodulation Techniques: Amplitude Modulation (AM) and Frequency Modulation (FM), Sensitivity and Selectivity of Radio Receivers, Noise in Radio Receivers Automatic Gain Control (AGC) in Radio Receivers, Applications of analog radio receivers, Types of digital radio receivers: Direct Conversion, Superheterodyne, and Software Defined Radio (SDR), Demodulation Techniques: Amplitude Shift Keying (ASK), Frequency Shift Keying (FSK), Phase Shift Keying (PSK), and Quadrature Amplitude Modulation (QAM).	08L
UNIT-04	<b>Channel Capacity and Coding</b> Introduction to information theory, Entropy and information content, Source coding, Channel capacity, introduction to error correction codes, Linear block codes, Convolutional codes, Shannon's channel capacity theorem.	05L
UNIT-05	<b>Recent Communication Systems</b> 5G Communication Systems, Satellite Communication Systems, Cognitive Radio Systems, Internet of Things (IoT) Communication Systems.	05L
<b>Course Outcomes</b> Upon successful completion of the course, the students will be able to CO1: understand the basic building blocks of communication systems, including modulation techniques and transmission media. CO2: understand the principles of digital signal processing and its application to communication systems, including techniques for signal filtering, modulation, and demodulation. CO3: Understand the source and channel encoding principles used in the communication systems. CO4: Understand the emerging communication technologies, including 5G, Internet of Things (IoT), and Cognitive radio systems.		
<b>Books and References</b> <ol style="list-style-type: none"> <li>"Communication Systems Engineering" by John G. Proakis and Masoud Salehi.</li> <li>"Communication Systems, 3rd edition" by Simon Haykin.</li> </ol>		

Course Name: <b>VLSI Design and Techniques</b>		
Course Code: <b>EC-304</b>		
Course Type: <b>Open Elective</b>		
Contact Hours/Week: <b>3L</b>		Course Credits: <b>03</b>
<b>Course Objectives</b> <ul style="list-style-type: none"> <li>● To impart knowledge about the Hardware Description Language based design approach.</li> <li>● To Understand different modeling techniques used in HDL.</li> <li>● The ability to code and simulate any digital function in HDL.</li> </ul>		
<b>Unit Number</b>	<b>Course Content</b>	<b>Contact Hours</b>
UNIT-01	<b>Introduction:</b> Introduction and levels of abstraction, Modeling and Hierarchical design concepts, Languages, Compilation & Simulation, concurrency, Logic value system, Role of CAD Tools in the VLSI design process.	<b>05L</b>
UNIT-02	<b>Language concepts:</b> Lexical conventions, Data types, modules and ports, Behavioral modeling, Dataflow modeling, Structural modelling.	<b>08L</b>
UNIT-03	<b>RTL Design:</b> Control & Data partitioning, Synthesis concepts, Non-synthesizable constructs, Operators, Expressions, Conditional statements, Post synthesis simulation.	<b>07L</b>
UNIT-04	<b>Advanced language concepts:</b> Procedures and timing control, Procedural blocks, Loops, Tasks and functions, Test bench modeling techniques, Path delay modeling, Timing analysis, User defined primitives, Compiler directives, and System tasks.	<b>08L</b>
UNIT-05	<b>Hardware modules:</b> Boolean equations, Encoders, Decoders, Multiplexers, Cascaded multiplexers, Adders, Serial adders, Comparators, Multipliers, Divider, Sorters, Shifters, Static and dynamic memories, Mealy & Moore finite state machine, Vending machines.	<b>05L</b>
UNIT-06	<b>Advanced design concepts:</b> FPGA architecture, Static timing analysis, Synchronization, Metastability, Verification methods, Implementation on FPGA, PLA based design.	<b>06L</b>
<b>Course Outcomes</b> Upon successful completion of the course, the students will be able to CO1: Design digital circuits in HDL. CO2: Verify behavioral and RTL models. CO3: Synthesize and simulate RTL design to standard cell libraries and FPGAs.		
<b>Books and References</b> <ol style="list-style-type: none"> <li>1. Peter J. Ashenden, "The Designer's Guide to VHDL", 2nd Edition, Morgan Kaufmann Publishers, 2001</li> <li>2. J. Bhasker, "A Verilog HDL Primer", Star Galaxy Press, 1996.</li> <li>3. Samir Palnitkar, "Verilog HDL: A Guide to Digital Design and Synthesis", Prentice Hall, 1996.</li> <li>4. Vivek Sagdeo, "The Complete Verilog Book", Kluwer Academic Publishers.</li> <li>5. Douglas J. Smith, "HDL Chip Design : A Practical guide for Designing, Synthesizing and Simulating ASICs and FPGAs using VHDL or Verilog", Doone Pubns, 1996.</li> <li>6. Ben Cohen, "VHDL Coding Styles and Methodologies", Kluwer Academic Publishers, 1999.</li> <li>7. J. Bhasker, "A VHDL Primer", Third Edition, Prentice Hall, 1998.</li> </ol>		

## **Stream Core Courses**



Course Name: <b>Hardware Description Language</b>		
Course Code: <b>EC-381</b>		
Course Type: <b>Stream Core-I</b>		
Contact Hours/Week: <b>2L</b>		Course Credits: <b>02</b>
<b>Course Objectives</b> <ul style="list-style-type: none"> <li>• To impart knowledge about the Hardware Description Language based design approach.</li> <li>• To Understand different modeling techniques used in HDL</li> <li>• The ability to code and simulate any digital function in HDL.</li> </ul>		
<b>Unit Number</b>	<b>Course Content</b>	<b>Contact Hours</b>
UNIT-01	<b>Language concepts:</b> Lexical conventions, Data types, modules and ports, Behavioral modeling, Dataflow modeling, Structural modelling.	<b>08L</b>
UNIT-02	<b>RTL Design:</b> Control & Data partitioning, Synthesis concepts, Non-synthesizable constructs, Operators, Expressions, Conditional statements, Post synthesis simulation.	<b>07L</b>
UNIT-03	<b>Advanced language concepts:</b> Procedures and timing control, Procedural blocks, Loops, Tasks and functions, Test bench modeling techniques, Path delay modeling, Timing analysis, User defined primitives, Compiler directives, and System tasks.	<b>08L</b>
UNIT-04	<b>Hardware modules:</b> Boolean equations, Encoders, Decoders, Multiplexers, Cascaded multiplexers, Adders, Serial adders, Comparators, Multipliers, Divider, Sorters, Shifters, Static and dynamic memories, Mealy & Moore finite state machine, Vending machines.	<b>05L</b>
<b>Course Outcomes</b> Upon successful completion of the course, the students will be able to CO1: Design digital circuits in HDL. CO2: Verify behavioral and RTL models. CO3: Synthesize and simulate RTL design to standard cell libraries.		
<b>Books and References</b> <ol style="list-style-type: none"> <li>1. Peter J. Ashenden, "The Designer's Guide to VHDL", 2nd Edition, Morgan Kaufmann Publishers, 2001</li> <li>2. J. Bhasker, "A Verilog HDL Primer", Star Galaxy Press, 1996.</li> <li>3. Samir Palnitkar, "Verilog HDL: A Guide to Digital Design and Synthesis", Prentice Hall, 1996.</li> <li>4. Vivek Sagdeo, "The Complete Verilog Book", Kluwer Academic Publishers.</li> <li>5. Douglas J. Smith, "HDL Chip Design : A Practical guide for Designing, Synthesizing and Simulating ASICs and FPGAs using VHDL or Verilog", Doone Pubns, 1996.</li> <li>6. Ben Cohen, "VHDL Coding Styles and Methodologies", Kluwer Academic Publishers, 1999.</li> <li>7. J. Bhasker, "A VHDL Primer", Third Edition, Prentice Hall, 1998.</li> </ol>		

Course Name: <b>Simulation of Communication Systems</b> Course Code: <b>EC-382</b> Course Type: <b>Stream Core-I</b>		
Contact Hours/Week: <b>2L</b>		Course Credits: <b>02</b>
<b>Course Objectives</b> <ul style="list-style-type: none"> <li>• This course provides an introduction to simulating communication systems using MATLAB and Python.</li> <li>• Understand the basics of communication systems, including signal modulation, demodulation, channel impairments, and error correction techniques.</li> <li>• Develop skills in MATLAB and Python programming for numerical computation and data analysis.</li> <li>• Learn how to model and simulate various communication systems, including analog and digital modulation, synchronization, channel coding, and demodulation.</li> <li>• Analyze the performance of simulated systems under different channel conditions and noise levels.</li> <li>• Gain experience in presenting and interpreting simulation results.</li> </ul>		
Unit Number	Course Content	Contact Hours
UNIT-01	Introduction to Communication Systems and Programming Essentials: Overview of communication systems, Signal representation and analysis, Analog modulation and demodulation techniques, Digital modulation and demodulation techniques, Channel impairments and noise Introduction to MATLAB and Python Data types, variables, operators, and control flow Functions, modules, and packages Numerical computations and data analysis, Plotting and visualization.	<b>08L</b>
UNIT-02	Communication System Modeling and simulation in MATLAB: Modeling of signal sources and modulators Channel modeling and noise generation, simulating analog modulation schemes (AM, FM, PM), Simulating digital modulation schemes (PSK, QAM), Performance analysis of simulated systems.	<b>08L</b>
UNIT-03	Communication System Modeling in Python: Implementing similar simulations using Python libraries (NumPy, SciPy, Matplotlib), Object-oriented programming for complex system modeling, Advanced signal processing techniques in Python.	<b>08L</b>
UNIT-04	Advanced Topics: Channel coding and decoding techniques, Synchronization methods, MIMO systems, Introduction to software-defined radio (SDR)	<b>06L</b>
<b>Course Outcomes</b> Upon successful completion of the course, the students will be able to CO1: Explain the basic concepts of modeling and simulation and their role in communication system engineering. CO2: Simulate communication systems including modulators, filters, demodulators, channels, equalizers, and encoders and decoders. CO3: Apply simulation and modeling methodologies to solve real-world communication system problems. CO4: Interpret simulation results and use them to make informed decisions about communication system design and implementation		
<b>Books and References</b> <ol style="list-style-type: none"> <li>1. "Simulation of Communication Systems" by Michel C. Jeruchim, Philip Balaban, Sam Shanmugan.</li> <li>2. "Communication Systems" by Simon Haykin, Wiley.</li> </ol>		

Course Name: <b>Information Theory and Coding</b>		
Course Code: <b>EC-451</b>		
Course Type: <b>Stream Core-II</b>		
Contact Hours/Week: <b>02L</b>		Course Credits: <b>02</b>
<b>Course Objectives</b> <ul style="list-style-type: none"> <li>● To impart knowledge about measuring the amount of information, capacities calculation of different channels in communication systems</li> <li>● To understand the theorems and inequalities used in information and coding theory field.</li> <li>● To enable the students to design the source coding algorithms for improving transmission efficiency.</li> <li>● To enable the students to design the block-based error control coding algorithms for improving error performance of communication systems.</li> </ul>		
<b>Unit Number</b>	<b>Course Content</b>	<b>Lectures</b>
UNIT-01	<b>Measures of Information and Channel Capacity:</b> Entropy, Relative Entropy and Mutual Information, Chain rule for entropy functions, Basic Inequalities: Jensen Inequality and its Physical Application), Log–Sum Inequality and its Physical Application. Definition of Capacity and its Computation of Discrete Memory Less Channels (BNC, BSC, BEC, Cascaded Channels, Noiseless Channels, Noisy Typewriter),	<b>06L</b>
UNIT-02	<b>Data Compression:</b> Unique Decodability and the Prefix Condition, Kraft's Inequality, Relationship of Average Codeword Length to Source Entropy, Examples of Coding Techniques: Huffman, Shannon–Fano–Elias, Lempel–Ziv and Universal.	<b>06L</b>
UNIT-03	<b>Design of Linear Block Codes:</b> Introduction of Linear Block Codes, Syndrome and Error Detection, Minimum Distance of a Block Code, Error Detecting and Error Correcting Capability of a Block Code, Design of Encoder and Syndrome Decoder for Linear Block Codes.	<b>06L</b>
UNIT-04	<b>Convolutional Codes:</b> Encoding of Convolutional Codes, Structural Properties of Convolutional Codes, Distance Properties of Convolutional Codes, Design of Encoder and Decoder for Convolutional Codes.	<b>06L</b>
<b>Course Outcomes:</b> Upon successful completion of the course, the students will be able to CO1: Understand the various terminologies to estimate information content in the communication system. CO2: Apply various inequalities and quantities to evaluate the information content of a discrete memory-less source. CO3: Design lossless source codes for discrete memory-less source to improve the efficiency of information transmission. CO4: Design block-based error control codes for improving the error performance of information transmission systems.		
<b>Books and References:</b> <ol style="list-style-type: none"> <li>1. Elements of Information Theory by T. M. Cover and J. A. Thomas, John Wiley, 1991.</li> <li>2. Error Control Coding by S. Lin and D. J. Costello, Pearson Education, 2010.</li> <li>3. Information Theory and Reliable Communication by R. G. Gallager, John Wiley &amp; Sons, 1969.</li> <li>4. Information Theory, Coding &amp; Cryptography by Ranjan Bose, Mc Graw Hill, 2016.</li> </ol>		

Course Name: <b>Analog IC Design</b>		
Course Code: <b>EC-452</b>		
Course Type: <b>Stream Core-II</b>		
Contact Hours/Week: <b>2L</b>		Course Credits: <b>02</b>
<b>Course Objectives</b> <ul style="list-style-type: none"> <li>● To give the student an understanding of basics of Analog IC Design.</li> <li>● To enable the student to understand issues related to MOSFET and its small signal model design.</li> <li>● The course aims to teach basic concepts along with advanced design techniques for CMOS amplifiers.</li> <li>● The objective of the course is to design and implement the product level op amps and buffers for VLSI applications.</li> </ul>		
Unit Number	Course Content	Contact Hours
UNIT-01	<b>MOS DEVICE MODELS AND SHORT CHANNEL EFFECTS:</b> MOSFET level 1 and level 2 models, threshold voltage model, capacitance model, mobility model, MOSFET basics, small-signal model derivation for a single transistor amplifier Process	<b>06L</b>
UNIT-02	<b>CMOS CURRENT MIRRORS:</b> Scheme and implementation: basic current mirrors, cascode current mirrors and active current mirrors with large and small signal analysis Understanding of common-mode properties Analog layout making techniques for current mirrors	<b>06L</b>
UNIT-03	<b>CMOS AMPLIFIERS:</b> Basic concept, Common source stage: with resistive load, with diode connected load, with current-source load, with triode load, Single ended differential operation, basic differential pair (qualitative and quantitative analysis, Miller effect, common source (CS), common gate (CG), common drain (CD) stages and cascode stage Analog layout techniques for MIM, MOM and fringe capacitor Noise analysis of the CMOS amplifier circuits	<b>08L</b>
UNIT-04	<b>DESIGN OF THE CMOS OPERATIONAL AMPLIFIERS:</b> One-stage op amps and two stage op-amps, Gain boosting techniques, folded cascode, telescopic amplifier and common mode feedback (CMFB) amplifier Three stage op amp architectures, op amp specifications analysis, Design of high speed and high gain amplifiers, Feedback topologies	<b>08L</b>
<b>Course Outcomes:</b> Upon successful completion of the course, the students will be able to CO1: Understand the operation of MOSFET and its small signal model. CO2: Analyze and design amplifiers, current mirrors and differential amplifiers. CO3: Understand the significance of different biasing techniques and apply them aptly to different circuits. CO4: Comparatively evaluate the frequency response different single stage amplifiers. CO5: Analyze & design the compensation different single stage amplifiers.		
<b>Books and References:</b> <ol style="list-style-type: none"> <li>1. Design of Analog CMOS Integrated Circuits by Behzad Razavi, McGraw Hill Education (1 September 2000)</li> <li>2. CMOS Analog Circuit Design by Phillip Allen and Douglas R. Holberg, OUP USA; Third Edition edition (1 September 2011).</li> <li>3. Operation and Modeling of the MOS Transistor by Yannis Tsividis, Oxford University Press; 2 edition, June 26, 2003</li> <li>4. Microelectronic Circuits-Theory &amp; Applications by A.S. Sedra and K.C. Smith, Adapted by A.N. Chandorkar, 6th Edition, Oxford, 2013.</li> </ol>		

Course Name: <b>Artificial Intelligence and Machine Learning</b>		
Course Code: <b>EC-471</b>		
Course Type: <b>Stream Core-III</b>		
<b>Contact Hours/Week: 2L</b>		<b>Course Credits: 02</b>
<b>Course Objectives</b> The objective of this course is to develop artificial intelligence and machine learning techniques for solving various real-life problems.		
<b>Unit Number</b>	<b>Course Content</b>	<b>Lectures</b>
UNIT-01	<b>INTELLIGENT AGENTS AND PROBLEM-SOLVING</b> The Foundations of Artificial Intelligence. The History of Artificial Intelligence, Risks and Benefits of AI, Agents and Environments, The Concept of Rationality, The Nature of Environments, The Structure of Agents, Problem-Solving Agents, Search Algorithms, Uninformed Search Strategies, Informed (Heuristic) Search Strategies, Heuristic Functions	<b>06L</b>
UNIT-02	<b>INTRODUCTION TO MACHINE LEARNING</b> Performance Measures of Classification, Bayesian Decision Theory, Bayesian Belief Networks, Parameter Estimation and Maximum Likelihood Estimation, Dimensionality Problem and Principal Component Analysis (PCA), Linear Discriminant Analysis	<b>06L</b>
UNIT-03	<b>SELECTED MACHINE LEARNING ALGORITHMS</b> Linear Regression, Decision Tree Classifiers, Overfitting, Notion of Training, Validation and Testing, Support Vector Machine (SVM), Logistic Regression, Kernels (with SVM), Hidden Markov Model (HMM)	<b>06L</b>
UNIT-04	<b>ENSEMBLE LEARNING AND CLUSTERING</b> Ensemble Learning, Boosting, Classifier Evaluation, Unsupervised Learning, K-means clustering, Fuzzy K-means clustering, Hierarchical Agglomerative Clustering and Mean-shift Clustering	<b>06L</b>
<b>Course Outcomes</b> Upon successful completion of the course, the students will be able to CO1: Understand the basics of artificial intelligence and intelligent agents. CO2: Understand the fundamentals of machine learning. CO3: Understand the concepts of selected machine learning algorithms. CO4: Understand the concepts of ensemble learning and clustering.		
<b>Books and References</b> 1. S. Russel and P. Norvig, "Artificial Intelligence – A Modern Approach", Second Edition, Pearson Education 2. Marc Peter Deisenroth, A. Aldo Faisal, Cheng Soon Ong, "Mathematics for Machine Learning", Cambridge University Press		

Course Name: <b>Embedded Systems</b>		
Course Code: <b>EC-472</b>		
Course Type: <b>Stream Core-III</b>		
Contact Hours/Week: <b>2L</b>		Course Credits: <b>02</b>
<b>Course Objectives</b> <ul style="list-style-type: none"> <li>To impart knowledge about the microcontrollers, its programming, interrupts, timers and assembly language.</li> <li>The concepts of ARM architecture and realtime operating system.</li> <li>To provide experience to integrate hardware and software for microcontroller application system.</li> <li>To impart ability to put together processor, peripherals and memory and build a real time system.</li> </ul>		
<b>Unit Number</b>	<b>Course Content</b>	<b>Contact Hours</b>
UNIT-01	<b>Programming:</b> Introduction to Embedded System Architecture, The Embedded Systems Model, Embedded Hardware, Assembly Programming, Timer Registers, Timer Modes, Overflow Flags, Clocking Sources, Timer Counter Interrupts, Baud Rate Generation, Serial Port Register, Modes of Operation, Processing Interrupts, Interrupt Service Routines, Look-up Tables.	<b>10L</b>
UNIT-02	<b>Embedded software development:</b> Software development flow, Polling, Interrupt driven, Multi-tasking systems, Architecture of an RTOS, Important features of RTOS, Embedded Systems Programming, Locks and Semaphores, Operating System Timers and Interrupts, Exceptions, Tasks, Task states and scheduling, Task structures, Synchronization, Communication and concurrency, Semaphores, Mutexes, Real-time clock, Memory management. Scheduling algorithms like Round Robin, Rate monotonic, Earliest Deadline First.	<b>08L</b>
UNIT-03	<b>32-Bit Cortex-M Architecture:</b> CPU architecture, Memory model, Registers, Modes, Exceptions, Interrupts, Exception handlers, interrupt controllers, Power modes, Hardware features and optimizations, Advanced bus standards like AMBA, The NVIC on ARM Cortex-M.	<b>07L</b>
UNIT-04	<b>Instruction set of ARM:</b> Syntax, Addressing modes and operands, Memory access instructions, Logical operations, Shift operations, Arithmetic operations, Stack, Functions and control flow, Assembler directives, Thumb and arm instruction differences, Development with Keil and mbed, Applications like IoT and machine learning with cortex-M.	<b>06L</b>
<b>Course Outcomes</b> Upon successful completion of the course, the students will be able to CO1: Write the programs for microcontrollers. CO2: Understand the role of embedded systems in industry. CO3: Understand the design concept of embedded systems.		
<b>Books and References</b> <ol style="list-style-type: none"> <li>Mazidi Muhammad Ali, "The 8051 Microcontroller and Embedded Systems", 2<sup>nd</sup> Edition, Pearson publications.</li> <li>Joseph Yiu, "The Definitive Guide to ARM Cortex-M3 processors", 3<sup>rd</sup> Edition, Newnes publication.</li> <li>Jonathan W. Valvano, "Volume 1, Introduction to ARM Cortex-M Microcontrollers", 5<sup>th</sup> Edition, CreateSpace.</li> <li>Jonathan W. Valvano, "Volume 2, Real-Time Interfacing to ARM Cortex-M Microcontrollers", 4<sup>th</sup> Edition, CreateSpace.</li> <li>Jonathan W. Valvano, "Volume 3, Real-Time Operating Systems for ARM Cortex-M Microcontrollers", 2<sup>nd</sup> Edition, CreateSpace.</li> </ol>		

## **Stream Elective Courses**

Course Name: <b>Applied Linear Algebra for Signal Processing, Data Analytics and Machine Learning</b>		
Course Code: <b>EC-461</b>		
Course Type: <b>Stream Elective-I</b>		
Contact Hours/Week: <b>3L</b>		Course Credits: <b>03</b>
<b>Course Objectives</b> <ul style="list-style-type: none"> <li>To introduce all the basic and advanced concepts in Linear Algebra with a strong focus on applications. Linear Algebra is one of the fundamental tools that has.</li> <li>To introduce applications of Linear Algebra in diverse fields such as Machine Learning, Data Analytics, Signal Processing, Wireless Communication, Operations Research, Control and Finance.</li> <li>To learn about the novel cutting edge applications of linear algebra in various fields such as Machine Learning, Data Analytics, Signal Processing, Wireless Communication.</li> </ul>		
Unit Number	Course Content	Contact Hours
UNIT-01	Introduction to vectors, properties and applications, Introduction to matrices and Applications Circuits, Graphs, Social Networks, Traffic flow, Eigenvalue decomposition, properties and Applications Principal component analysis (PCA), Eigen faces for facial recognition	<b>08L</b>
UNIT-02	Singular value decomposition (SVD) and Applications Beamforming in MIMO, Dimensionality reduction, Rate maximization in wireless, MUSIC algorithm, Linear regression and Least Squares. Applications: System identification, linear regression, Support vector machines (SVM), kernel SVMs, Optimal linear MMSE estimation. Applications MMSE Receiver, Market prediction and forecasting, ARMA models	<b>08L</b>
UNIT-03	Data analytics: Recommender systems, user rating prediction, Structure of FFT/ IFFT matrices, properties, System model for OFDM/ SC-FDMA, Signal processing in OFDM systems	<b>08L</b>
UNIT-04	Modeling of Dynamical systems Examples, Solution of autonomous linear dynamical systems (LDS), solution of with inputs and outputs, Unsupervised learning: Centroid based clustering, probabilistic model-based clustering and EM algorithm, Linear perceptron. Training a perceptron stochastic gradient. Compressive sensing, orthogonal matching pursuit for sparse signal estimation	<b>08L</b>
UNIT-05	Discrete time Markov chains Applications: supply chain management, forecasting, Operations research resource and inventory management.	<b>04L</b>
<b>Course Outcomes</b> Upon successful completion of the course, the students will be able to CO1: Understand the basic of Linear Algebra CO2: Understand applications of Linear Algebra in diverse fields such as Machine Learning, Data Analytics, Signal Processing, Wireless Communication, Operations Research, Control and Finance		
<b>Books and References</b> <ol style="list-style-type: none"> <li>Introduction to Linear Algebra: Gilbert Strang</li> <li>Fundamentals of Wireless Communication. David Tse, Pramod Viswanath</li> <li>Hands-On Machine Learning with Scikit-Learn, Keras, and TensorFlow: Concepts, Tools, and Techniques to Build Intelligent Systems, Third Edition by Aurélien Géron Shroff/O'Reilly Publisher</li> </ol>		



Course Name: <b>RF and Microwave Circuit Design</b> Course Code: <b>EC-462</b> Course Type: <b>Stream Elective-I</b>		
Contact Hours/Week: <b>3L</b>		Course Credits: <b>03</b>
<b>Course Objectives</b> <ul style="list-style-type: none"> <li>To impart knowledge about the transmission lines theory, network analysis, smith chart, microwave filter circuits, power divider, coupler and their characterization.</li> </ul>		
Unit Number	Course Content	Contact Hours
UNIT-01	<b>REVIEW OF TRANSMISSION LINES THEORY</b> Propagation of voltage and current in transmission lines, Parameters, transmission line equation, reflection and transmission coefficient, lossy transmission line, standing waves, input impedance, Generator and load mismatches, transient analysis of transmission lines.	<b>09 L</b>
UNIT-02	<b>SMITH CHART AND MATCHING NETWORKS</b> Smith chart, Matching with lumped element, single stub matching, double stub matching, quarter wave transformers.	<b>06 L</b>
UNIT-03	<b>NETWORK ANALYSIS</b> Scattering Parameter, Z Parameter, Y parameter, ABCD parameter, conversion from S-parameter in terms of Z parameter, conversion from Z-parameter in terms of S- parameter, S-parameter in terms of Y-parameter, conversion from Y-parameter in terms of S- parameter, S-parameter in terms of ABCD parameter, conversion from ABCD parameter in terms of S- parameter.	<b>05 L</b>
UNIT-04	<b>MICROWAVE RESONATOR AND FILTERS</b> Series and parallel resonant circuits, Transmission Line Resonators, Dielectric Resonators, Filter Parameters, Filter design by Image Parameter method, Filter design by Insertion Loss method, Filter Transformation, Filter Implementation, Impedance and Admittance inverters.	<b>09 L</b>
UNIT-05	<b>MICROWAVE POWER DIVIDER AND COUPLERS</b> Directional couplers, Bethe-hole couplers, coupled line directional coupler, Wilkinson Power Divider.	<b>06 L</b>
<b>Course Outcomes</b> Upon successful completion of the course, the students will be able to CO1: Describe and apply transmission line theory, matching networks in different microwave devices. CO2: Describe and utilize different network analysis parameters for solving two or multiport networks. CO3: Design and characterize different microwave filter, coupler and power divider.		
<b>Books and References</b> <ol style="list-style-type: none"> <li>Microwave Engineering by D. M. Pozar, Wiley</li> <li>Foundations of Microstrip Circuit Design by T. C. Edwards and M. B. Steer, Wiley-IEEE Press.</li> <li>Elements of Electromagnetics by M. N. O. Sadiku, Oxford University Press, New York.</li> <li>Microwave Engineering, by Sushrut Das, Oxford University Press</li> </ol>		

Course Name: <b>Low Power VLSI Design Techniques</b>		
Course Code: <b>EC-463</b>		
Course Type: <b>Stream Elective-I</b>		
Contact Hours/Week: <b>3L</b>		Course Credits: <b>03</b>
<b>Course Objectives</b> <ul style="list-style-type: none"> <li>To impart knowledge about the dominant sources of power dissipation in VLSI circuits.</li> <li>To introduce the fundamental concepts for optimization of power at all design levels: technology, circuit, logic, and architectural level.</li> <li>To enable the students to aware of power estimation by various means.</li> </ul>		
<b>Unit Number</b>	<b>Course Content</b>	<b>Contact Hours</b>
UNIT-01	<b>Introduction:</b> Need for Low Power VLSI Chips, Sources of Power Dissipation in Digital Integrated Circuits, Physics of Power Dissipation in CMOS Devices, Dynamic Dissipation in CMOS, Leakage Power Dissipation, Low Power Figure of Merits, Impact of Technology Scaling, Device Innovation and Channel Engineering	<b>06L</b>
UNIT-02	<b>Power Estimation:</b> Simulation Power Analysis: SPICE Circuit Simulators, Gate Level Logic Simulation, Capacitive Power Estimation, Static State Power, Gate Level Capacitance Estimation, Data Correlation Analysis in DSP Systems, Monte Carlo Simulation, Probabilistic Power Analysis: Random Logic Signals, Probability and Frequency, and Probabilistic Power Analysis Techniques	<b>09L</b>
UNIT-03	<b>Low Power Circuit Level Design:</b> Circuit Level Transistor and Gate Sizing, Circuit Techniques for Leakage Power Reduction, Transistor Stacking, Supply Voltage Scaling Techniques, DTCMOS, MTCMOS, Network Restructure and Reorganization, Flip Flops and Latches Design, and Low Power Digital Cell Library	<b>07L</b>
UNIT-04	<b>Low Power Logic Level Design:</b> Gate Reorganization, Multistage Logic Design, Signal Gating, Logic Encoding, State Machine Encoding, and Pre-Computation Logic	<b>07L</b>
UNIT-05	<b>Low Power Architecture and System Level Design:</b> Power and Performance Management, Switching Activity Reduction, Parallel Architecture with Voltage Reduction, and Flow Graph Transformation.	<b>05L</b>
UNIT-06	<b>Special Techniques:</b> Low Power Clock Distribution, Single Driver vs Distributed Buffers, Various Clock Distribution Networks, Power Reduction in Clock Networks, Low Power Bus, CMOS Floating Nodes, and Adiabatic Logic.	<b>05L</b>
<b>Course Outcomes</b> Upon successful completion of the course, the students will be able to CO1: Identify sources of power dissipation in VLSI systems. CO2: Analyze various circuit and logic design techniques for dynamic and leakage power reduction. CO3: Design arithmetic circuits, latches and flip flops with different logic styles. CO4: Understand the concepts of probability and random logic signals for estimation of capacitance and power dissipation. CO5: Understand power management through architectural level techniques.		
<b>Books and References</b> <ol style="list-style-type: none"> <li>Practical Low Power Digital VLSI Design by Gary K. Yeap, Kluwer Academic Press.</li> <li>Low-Power CMOS VLSI Circuit Design by Kaushik Roy, and Sharat Prasad, Wiley.</li> <li>Low Power VLSI CMOS Circuit Design by A. Bellamour, and M. I. Elmasri, Kluwer Academic Press.</li> <li>Low Power Design Methodologies by J. M. Rabaey and M. Pedram, Kluwer Academic Press.</li> <li>Low-Voltage Low-Power VLSI subsystems by Kait-Seng Yeo and Kaushik Roy, Tata McGraw-Hill.</li> <li>Low Power Digital CMOS Design by Anantha P. Chandrakasan and Robert W. Brodersen, Kluwer Academic Press.</li> </ol>		

Course Name: <b>VLSI Interconnects and Packaging</b>		
Course Code: <b>EC-464</b>		
Course Type: <b>Stream Elective-I</b>		
Contact Hours/Week: <b>3L</b>		Course Credits: <b>03</b>
<b>Course Objectives</b> <ul style="list-style-type: none"> <li>• To impart knowledge about the importance of electrical on-chip interconnects in modern VLSI circuits</li> <li>• To introduce the various equivalent circuit models of interconnects and their comparison</li> <li>• To understand the short-channel model of CMOS repeater driving interconnect and its analysis</li> <li>• To enable the students to understand the advanced interconnect techniques</li> </ul>		
<b>Unit Number</b>	<b>Course Content</b>	<b>Contact Hours</b>
UNIT-01	<b>Interconnects:</b> <i>Interconnect Parameters:</i> Resistance, Inductance, and Capacitance, <i>Interconnect RC Delays:</i> Elmore Delay Calculation. <i>Interconnect Models:</i> Lumped RC Model, Distributed RC Model, Transmission Line Model. <i>SPICE Wire Models:</i> Distributed RC Lines in SPICE, Transmission Line Models in SPICE, Gate and Interconnect Delay	<b>07L</b>
UNIT-02	<b>CMOS Repeater:</b> <i>Static Behavior:</i> Switching Threshold, Noise Margins. <i>Dynamic Behavior:</i> Computing Capacitances. <i>Propagation Delay:</i> First order analysis, Propagation Delay from a Design Perspective. <i>Power, Energy and Energy-Delay:</i> Dynamic Power Consumption, Static Consumption, Analyzing Power Consumption using SPICE	<b>06L</b>
UNIT-03	<b>Driving Interconnects for Optimum Speed and Power:</b> <i>Short Channel Model of CMOS Repeater:</i> Transient Analysis of an RC Loaded CMOS Repeater, Delay Analysis. <i>Analytical Power Expressions:</i> Dynamic Power, Short Circuit Power, Resistive Power Dissipation. <i>CMOS Repeater Insertion:</i> Analytical Expressions for Delay and Power of a Repeater Chain Driving an RC Load	<b>05L</b>
UNIT-04	<b>Advanced Interconnect Techniques:</b> Reduced-swing Circuits, Current Mode Transmission Techniques	<b>05L</b>
UNIT-05	<b>Crosstalk:</b> Theoretical Basis and Circuit Level Modeling of Crosstalk, <i>Energy Dissipation due to Crosstalk:</i> Model for Energy Calculation of two Coupled Lines. Contribution of Driver and Interconnect to Dissipated Energy. <i>Crosstalk effects in logic VLSI circuits:</i> Static Circuits, Dynamic Circuits and Various Remedies	<b>07L</b>
UNIT-06	<b>IC Packaging:</b> Package Types, Packaging Design Considerations, VLSI Assembly Technology, Package Fabrication Technology	<b>06L</b>
<b>Course Outcomes</b> Upon successful completion of the course, the students will be able to CO1: Develop the ability to analyze and design electrical interconnect using equivalent circuit models. CO2: Understand the use of CMOS repeater to predict delay and power in interconnects. CO3: Describe the design trade-offs in driver-interconnect-load system. CO4: Design crosstalk and delay aware repeater driven interconnect system using advanced signaling techniques		
<b>Books and References</b> <ol style="list-style-type: none"> <li>1. Analysis and Design of Digital Integrated Circuits – A Design Perspective by Jan M. Rabaey, Tata Mc-Graw Hill.</li> <li>2. Interconnection Noise in VLSI Circuits by F. Moll and M. Roca, Kluwer Academic Publishers.</li> <li>3. Introduction to VLSI Circuits and Systems by J. P. Uymera, Wiley Student Edition.</li> <li>4. CMOS Digital Integrated Circuits – Analysis and Design by S. M. Kang and L. Yusuf, Tata Mc-Graw Hill.</li> </ol>		

Course Name: <b>Internet of Things and Applications</b>		
Course Code: <b>EC-465</b>		
Course Type: <b>Stream Elective-I</b>		
Contact Hours/Week: <b>3L</b>		Course Credits: <b>03</b>
<b>Course Objectives</b> <ul style="list-style-type: none"> <li>• To understand the basics of Internet of Things (IoT) in different aspects of research, implementation, and business with IoT.</li> <li>• To understand different application domain verticals ranging from civilian to defence sectors including agriculture, space, healthcare, manufacturing, construction, water, and mining.</li> <li>• To envision pervasive connectivity, storage, and computation, which, in turn, gives rise to building different IoT solutions.</li> <li>• To understand implementation of IoT with Arduino, Raspberry Pi</li> </ul>		
<b>Unit Number</b>	<b>Course Content</b>	<b>Contact Hours</b>
UNIT-01	Introduction to IoT: Sensing, Actuation, Basics of IoT Networking, Connectivity Technologies, Communication Protocols, Sensor Networks, UAV Networks, Machine-to-Machine Communications	<b>07L</b>
UNIT-02	Interoperability in IoT, Introduction to Arduino Programming, Integration of Sensors and Actuators with Arduino, Introduction to Python programming, Introduction to Raspberry Pi	<b>07L</b>
UNIT-03	Implementation of IoT with Raspberry Pi, Introduction to Software Defined Networking (SDN), SDN for IoT, Data Handling and Analytics	<b>07L</b>
UNIT-04	Cloud Computing, Cloud Computing-Fundamental, Cloud Computing-Service Model, Cloud Computing-Service Management and Security, Cloud Computing- Case Studies, Sensor-Cloud, Fog Computing,	<b>07L</b>
UNIT-05	Applications: Smart Cities and Smart Homes, Connected Vehicles, Smart Grid, Industrial Internet of Things, Data Handling and Analytics, Case Study: Agriculture, Healthcare, Activity Monitoring	<b>08L</b>
<b>Course Outcomes</b> Upon successful completion of the course, the students will be able to CO1: Give the basic of Internet of Things CO2: Understand basics of Networking, Sensor Networks and Communication Protocols. CO3: To understand Implementation of IoT with Raspberry Pi. CO4: To understand and implement different applications of IoT		
<b>Books and References</b> <ol style="list-style-type: none"> <li>1. S. Misra, A. Mukherjee, and A. Roy, 2020. Introduction to IoT. Cambridge University Press.</li> <li>2. S. Misra, C. Roy, and A. Mukherjee, 2020. Introduction to Industrial Internet of Things and Industry 4.0. CRC Press.</li> <li>3. Internet of Things, An Indian Adaptation: : Concepts And Applications Paperback – 1 May 2021 by Wiley Editorial</li> <li>4. Designing the Internet of Things, by Adrian McEwen (Author), Hakim Cassimally Wiley Publisher</li> <li>5. The Silent Intelligence: The Internet of Things by Daniel Kellmerein, Daniel Obodovski, Lightning Source Inc</li> </ol>		

Course Name: <b>Wavelet Transform and Applications</b> Course Code: <b>EC-466</b> Course Type: <b>Stream Elective-I</b>		
Contact Hours/Week: <b>3L</b>		Course Credits: <b>03</b>
<b>Course Objectives</b> <ul style="list-style-type: none"> <li>• To introduce Time-frequency analysis</li> <li>• To introduce Frames, orthonormal bases of wavelets, multiresolution analysis</li> <li>• To introduce Wavelet packets, Approximation theory of wavelet thresholding</li> <li>• To introduce Statistical estimation with wavelets, and Coding theory</li> </ul>		
Unit Number	Course Content	Contact Hours
UNIT-01	Introduction Stationary and non-stationary signals, Signal representation using basis and frames, Brief introduction to Fourier transform and short time Fourier transform, Instantaneous Frequency, Analytic Signals, Multicomponent Signals, Time-frequency analysis, Bases of time frequency: orthogonal, Filter banks, Multi resolution formulation: Wavelets from filters, Classes of wavelets: Haar, Daubechies, bi-orthogonal.	<b>07L</b>
UNIT-02	Continuous Wavelet Transform Continuous wavelet transform (CWT), Time and frequency resolution of the continuous wavelet transform, Construction of continuous wavelets: Spline, orthonormal, bi-orthonormal, Inverse continuous wavelet transform, Redundancy of CWT, zoom property of the continuous wavelet transform, Filtering in continuous wavelet transform domain, Bases, Orthogonal Bases, Biorthogonal Bases, Frames, Tight Frames, and unconditional Bases	<b>07L</b>
UNIT-03	Discrete Wavelet Transform and Filterbanks, Orthogonal and biorthogonal two-channel filter banks, Design of two-channel filter banks, Tree-structured filter banks, Discrete wavelet transform, Non-linear approximation in the Wavelet domain, multi resolution analysis, Construction and Computation of the discrete wavelet transform, the redundant discrete wavelet transform, Regularity, Moments, and Wavelet System Design	<b>07L</b>
UNIT-04	Multi Resolution Analysis Multirate discrete time systems, Parameterization of discrete wavelets, Bi-orthogonal wavelet bases, Two dimensional, wavelet transforms and Extensions to higher dimensions, Wavelet packet and local cosine bases	<b>07L</b>
UNIT-05	Signal and Image compression, Detection of signal changes, analysis and classification of audio signals using CWT, Wavelet based signal de-noising and energy compaction	<b>07L</b>
UNIT-06	Transform coding, Wavelets in adaptive filtering, Adaptive wavelet techniques in signal acquisition, coding and lossy transmission, Image fusion, Edge Detection and object isolation.	<b>07L</b>
<b>Course Outcomes:</b> Upon successful completion of the course, the students will be able to CO1: Understand the Continuous wavelet transform. CO2: Understand the discrete wavelet transform. CO3: Understand the multiresolution analysis. CO4: Understand the Wavelet packets. CO5: Understand the Applications of wavelet transform. CO6: Analyse the Transform coding.		
<b>Books and References:</b> <ol style="list-style-type: none"> <li>1. A Wavelet Tour of Signal Processing, 2nd edition, S. Mallat, Academic Press, 1999.</li> <li>2. Wavelets and Sub band Coding, M. Vetterli and J. Kovacevic, Prentice Hall, 1995.</li> <li>3. Wavelets and Wavelet Transforms, C. Sidney Burrus, Ramesh Gopinath, Haitao Guo, 2013</li> </ol>		

Course Name: <b>RF Microelectronics</b>		
Course Code: <b>EC-467</b>		
Course Type: <b>Stream Elective-I</b>		
Contact Hours/Week: <b>3L</b>		Course Credits: <b>03</b>
<b>Course Objectives</b> <ul style="list-style-type: none"> <li>To give the student an understanding of the fundamental concepts of RF microelectronics, including electromagnetic waves and design challenges.</li> <li>To enable the student to understand and develop the skills needed to design and analyse RF amplifiers and mixers.</li> <li>Develop skills to design and analyse RF amplifiers, mixers, filters, and oscillators.</li> <li>Learn about RF transceiver architectures and power amplifiers.</li> <li>Gain an understanding of RF system design and trade-offs.</li> </ul>		
<b>Unit Number</b>	<b>Course Content</b>	<b>Contact Hours</b>
UNIT-01	<b>INTRODUCTION TO RF MICROELECTRONICS:</b> Overview of RF systems and applications, Frequency bands and spectrum allocation, RF circuit design challenges, Transmission Lines and Impedance Matching, Transmission line theory, Smith chart analysis, Impedance matching techniques	<b>06L</b>
UNIT-02	<b>RF AMPLIFIERS AND MIXERS:</b> Basic amplifier concepts, Analysis and design of common-source and common-emitter amplifiers, Noise and stability analysis, Frequency mixing fundamentals, Analysis and design of passive and active mixers, Conversion gain and noise figure analysis,	<b>07L</b>
UNIT-03	<b>RF FILTERS AND OSCILLATORS:</b> Filter types and specifications, Filter design techniques, SAW and BAW filters, Oscillator basics and types, Phase noise and jitter analysis, Analysis and design of LC and crystal oscillators	<b>07L</b>
UNIT-04	<b>RF TRANSCEIVER ARCHITECTURES AND POWER AMPLIFIERS:</b> Transceiver architecture overview, Single- and double-conversion architectures, Superheterodyne and direct-conversion architectures, Power amplifier basics and classes, Power amplifier design techniques, Efficiency and linearity analysis	<b>07L</b>
UNIT-05	<b>RF SYSTEM DESIGN AND TESTING:</b> RF system requirements and specifications, Link budget analysis, RF system design trade-offs RF testing and measurement techniques	<b>07L</b>
<b>Course Outcomes</b> Upon successful completion of the course, the students will be able to CO1: Understand and analyse the basic concepts of RF Microelectronics. CO2: Design, simulate, and optimize RF circuits using software tools. CO3: Apply knowledge to the design and implementation of wireless communication systems. CO4: Apply advanced topics in RF Microelectronics. CO5: Evaluate performance of RF systems and components and communicate findings effectively.		
<b>Books and References</b> <ol style="list-style-type: none"> <li>Behzad Razavi, "RF Microelectronics," 2nd edition, Prentice Hall, 2012.</li> <li>Thomas H.Lee, The Design of CMOS Radio-Frequency Integrated Circuits, Cambridge University Press, 2<sup>nd</sup> edition, 2004.</li> </ol>		

Course Name: <b>Embedded Networking and Device Driver Programming</b>		
Course Code: <b>EC-468</b>		
Course Type: <b>Stream Elective-I</b>		
Contact Hours/Week: <b>3L</b>		Course Credits: <b>03</b>
<b>Course Objectives</b> The objective of this course is to explore embedded networking and programming, which are the tools used to investigate and create reliable communication protocols. This course is primarily aimed for designers who want to employ networking ideas like Ethernet, CAN Protocols, USB, and others.		
Unit Number	Course Content	Contact Hours
UNIT-01	<b>BASICS OF EMBEDDED COMMUNICATION</b> Embedded Networking Overview: Serial/Parallel Communication, Serial communication protocols - RS232 standard, Synchronous and Asynchronous Communication - Serial Peripheral Interface (SPI) - Inter Integrated Circuits (I2C) - PC Parallel port Interface -ISA/PCI Bus protocols.	<b>08L</b>
UNIT-02	<b>USB BUS, CAN BUS AND FRAMING</b> Introduction to the USB bus, USB States, USB bus communication: Packets-Data flow types-Descriptors-Enumeration, Speed Identification on the bus, Introduction to the CAN Bus, the CAN Identifier, Data Transmissions on the CAN, Framing, Error Control Coding, Baseband Transmissions, RZ and NRZ techniques.	<b>08L</b>
UNIT-03	<b>ETHERNET</b> Network elements, Bus and Star Topologies, protocol CSMA/CD, IEEE802.3 and its different versions, Ethernet Addressing, Cables, Connections, Hubs, switches, and Bridges. Ethernet Addressing and network speed.	<b>06L</b>
UNIT-04	<b>PROTOCOLS FOR NETWORK LAYER</b> Internetworks, Addressing, Routing, ARP, IP, ICMP, IPV6, Unicast Routing, Unicast Routing Protocol, Multicast Routing, Multicast Routing Protocols, Distance Vector Routing, Link State Routing, Network Address Translator (NAT), Firewall.	<b>05L</b>
UNIT-05	<b>PROTOCOLS FOR TRANSPORT AND APPLICATION LAYER</b> User Datagram Protocol (UDP), Transmission Control Protocol (TCP), Data Traffic, Congestion, Congestion Control, Quality of Service, Techniques to Improve QOS, Integrated Services, QOS in Switched Networks. Concepts of Cryptography, Symmetric cryptography, and public key cryptography.	<b>05L</b>
UNIT-06	<b>EMBEDDED PROGRAMMING</b> Overview of Embedded C programming, Object-oriented programming fundamentals, pointers, functions, dynamic memory allocation and de-allocation, Tools for developing embedded software, Embedded system development fundamentals.	<b>04L</b>
<b>Course Outcomes</b> Upon successful completion of the course, the students will be able to CO1: Understand the Basics of Embedded Communication. CO2: Understand the USB BUS, CAN BUS concepts. CO3: Understand the concept of ETHERNET. CO4: Understand the different layer protocols for embedded networking. CO5: Understand the basics of embedded programming.		
<b>Books and References</b> 1. Frank Vahid, Tony D. Givargis, "Embedded Systems Design: A Unified Hardware Software Introduction", Wiley Publications 2007. 2. Pont Michael J, "Embedded C", Pearson Education ,2007		

Course Name: <b>VLSI Verification</b>		
Course Code: <b>EC-469</b>		
Course Type: <b>Stream Elective-I</b>		
Contact Hours/Week: <b>3L</b>		Course Credits: <b>03</b>
<b>Course Objectives</b> <ul style="list-style-type: none"> <li>• To impart knowledge about the basics of verification techniques for VLSI circuits</li> <li>• To introduce the fundamental concepts of Logic checking and design validation</li> <li>• To enable the students to get accustomed to the verification methodologies.</li> </ul>		
<b>Unit Number</b>	<b>Course Content</b>	<b>Contact Hours</b>
UNIT-01	<b>Basics of UVM:</b> Introduction to UVM, UVM Basics, Advanced UVM, UVM Connect, UVM Debug, UVMF - One Bite at a Time	<b>02L</b>
UNIT-02	<b>Verification standards:</b> Introduction to ISO 26262, Introduction to DO-254, Clock-Domain Crossing Verification, Portable Stimulus Basics, Power Aware CDC Verification, Power Aware Verification, System Verilog OOP for UVM Verification	<b>8L</b>
UNIT-03	<b>System Verilog:</b> System Verilog Testbenches, Co-Emulation, functional verification, Assertion-Based Verification, Evolving FPGA Verification Capabilities	<b>9L</b>
UNIT-04	<b>Formal-based techniques:</b> Formal Assertion-Based Verification, Formal-Based Technology: Automatic Formal Solutions, Formal Coverage, Getting Started with Formal-Based Technology, Handling Inconclusive Assertions in Formal Verification, Sequential Logic Equivalence Checking	<b>12L</b>
<b>Course Outcomes</b> Upon successful completion of the course, the students will be able to CO1: Apply the concepts in verification which can help them design a better yield in IC design. CO2: Tackle the problems associated with verification of semiconductor circuit design including several critical cases. CO3: Identify the methodologies and design guidelines suitable for better circuit design		
<b>Books and References</b> <ol style="list-style-type: none"> <li>1. System Verilog for Verification: A Guide to Learning the Testbench Language Features by Chris Spear &amp; Greg Tumbush (3rd Edition)</li> <li>2. A Practical Guide to Adopting Universal Verification Methodology (UVM) by Sharon Rosenberg &amp; Kathleen A Meade (2nd Edition)</li> <li>3. The UVM Primer: A Step-by-Step Introduction to the Universal Verification Methodology by Ray Salemi</li> <li>4. Getting Started with UVM: A Beginner's Guide by Vanessa R. Copper</li> <li>5. A Practical Guide for System Verilog Assertions by Srikanth Vijayaraghavan &amp; Meyyappan Ramanathan</li> </ol>		



Course Name: <b>Compound Semiconductors: Properties &amp; Applications</b>		
Course Code: <b>EC-480</b>		
Course Type: <b>Stream Elective-II</b>		
Contact Hours/Week: <b>3L</b>		Course Credits: <b>03</b>
<b>Course Objectives</b> <ul style="list-style-type: none"> <li>● Understand parameters governing high-speed and high-power performance in compound semiconductor devices.</li> <li>● Explore materials properties and processing techniques of compound semiconductors compared to silicon-based devices.</li> <li>● Analyze metal semiconductor contacts and compound semiconductor device characteristics.</li> <li>● Examine advanced compound semiconductor devices for high-speed applications and understand their operational principles.</li> </ul>		
<b>Unit Number</b>	<b>Course Content</b>	<b>Contact Hours</b>
UNIT-01	Important parameters governing the high-speed performance of devices and circuits: Transit time of charge carriers, junction capacitances, ON-resistances and their dependence on the device geometry and size, carrier mobility, doping concentration and temperature; important parameters governing the high-power performance of devices and circuits: Break down voltage, resistances, device geometries, doping concentration and temperature.	<b>06L</b>
UNIT-02	Materials properties: Merits of III –V binary and ternary compound semiconductors (GaAs, InP, InGaAs, AlGaAs, SiC, GaN etc.), different SiC structures, silicon-germanium alloys and silicon carbide for high speed devices, as compared to silicon based devices, outline of the crystal structure, dopants and electrical properties such as carrier mobility, velocity versus electric field characteristics of these materials, electric field characteristics of materials and device processing techniques, Band diagrams, homo and hetero junctions, electrostatic calculations, Band gap engineering, doping, Material and device process technique with these III-V and IV – IV semiconductors.	<b>16L</b>
UNIT-03	Metal semiconductor contacts and Metal Insulator Semiconductor and MOS devices: Native oxides of Compound semiconductors for MOS devices and the interface state density related issues. Metal semiconductor contacts, Schottky barrier diode, Metal semiconductor Field Effect Transistors (MESFETs): Pinch off voltage and threshold voltage of MESFETs. D.C. characteristics and analysis of drain current. Velocity overshoot effects and the related advantages of GaAs, InP and GaN based devices for high-speed operation. Sub threshold characteristics, short channel effects and the performance of scaled down devices.	<b>08L</b>
UNIT-04	High Electron Mobility Transistors (HEMT): Hetero-junction devices. The generic Modulation Doped FET(MODFET) structure for high electron mobility realization. Principle of operation and the unique features of HEMT, InGaAs/InP HEMT structures: Hetero junction Bipolar transistors (HBTs): Principle of operation and the benefits of hetero junction BJT for high-speed applications. GaAs and InP based HBT device structure and the surface passivation for stable high gain high frequency performance. SiGe HBTs and the concept of strained layer devices; High Frequency resonant –tunneling devices, Resonant-tunneling hot electron transistors	<b>12L</b>
<b>Course Outcomes</b> Upon successful completion of the course, the students will be able to CO1: Demonstrate comprehension of parameters impacting compound semiconductor device performance. CO2: Evaluate suitability of compound semiconductor materials for specific applications. CO3: Apply knowledge to analyze metal semiconductor contacts and compound semiconductor device characteristics. CO4: Synthesize understanding to design solutions using advanced compound semiconductor devices for high-speed applications.		
<b>Books and References</b> <ol style="list-style-type: none"> <li>1. C.Y. Chang, F. Kai, GaAs High-Speed Devices: Physics, Technology and Circuit Applications, Wiley &amp; Sons.</li> <li>2. Cheng T. Wang, Ed., Introduction to Semiconductor Technology: GaAs and Related Compounds, John Wiley &amp; Sons.</li> <li>3. David K. Ferry, Ed., Gallium Arsenide Technology, Howard W. Sams &amp; Co., 1985</li> <li>4. Avishay Katz, Indium Phosphide and Related materials: Processing, Technology and Devices, Artech House, 1992.</li> <li>5. S.M. Sze, High Speed Semiconductor Devices, Wiley (1990) ISBN 0-471-62307-5</li> <li>6. Ralph E. Williams, Modern GaAs Processing Methods, Artech (1990), ISBN 0-89006-343-5,</li> <li>7. Sandip Tiwari, Compound Semiconductor Device Physics, Academic Press (1991), ISBN 0-12-691740-X.</li> </ol>		

Course Name: <b>Adaptive Signal Processing</b>		
Course Code: <b>EC-481</b>		
Course Type: <b>Stream Elective-II</b>		
Contact Hours/Week: <b>3L</b>		Course Credits: <b>03</b>
<b>Course Objectives</b> <ul style="list-style-type: none"> <li>To introduce signal processing techniques for adaptive systems.</li> <li>To understand the applications of adaptive systems in the fields of communications, radar, sonar, seismology, navigation systems and biomedical engineering.</li> <li>To study the basic principles of adaptation, various adaptive signal processing algorithms for applications like adaptive noise cancellation, interference cancelling, system identification, etc.</li> </ul>		
Unit Number	Course Content	Contact Hours
UNIT-01	Review of Probability, Random variables, random processes, filtered random processes, Ensemble averages, correlation, covariance, power spectrum, cross power spectrum, Ergodicity, time averages, biased & unbiased estimators, consistent estimators.	07L
UNIT-02	Linear Optimum Filtering and Adaptive Filtering, Linear Filter Structures, Adaptive Equalization, Active Noise Cancellation, and Beam Forming, Optimum Linear Combiner and Wiener-Hopf Equations	07L
UNIT-03	Orthogonality Principle, Minimum Mean Square Error and Error Performance Surface, Steepest – Descent Algorithm and its Stability. LMS Algorithm and its Applications, Learning Characteristics and Convergence Behaviour	07L
UNIT-04	Mis- Adjustment, Normalized LMS and Affine Projection Adaptive Filters, Frequency Domain Block LMS Algorithm. Least Squares Estimation Problem and Normal Equations, Projection Operator	07L
UNIT-05	Exponentially Weighted RLS Algorithm, Convergence Properties of RLS Algorithm, Kalman Filter as the Basis for RLS Filter, Square-Root Adaptive Filtering and QR- RLS Algorithm, Systolic-Array Implementation of QR – RLS Algorithm	07L
UNIT-06	Forward and Backward Linear Prediction, Levinson-Durbin Algorithm, Lattice Predictors, Gradient-Adaptive Lattice Filtering, Least-Squares Lattice Predictor, QR-Decomposition Based Least-Squares Lattice Filters. Neural networks and multi-layer perceptrons, Adaptive IIR filtering, the constant modulus algorithm	07L
<b>Course Outcomes</b> Upon successful completion of the course, the students will be able to CO1: Comprehend design criteria and modeling of adaptive systems and theoretical performance evaluation. CO2: Design a linear adaptive processor. CO3: Apply mathematical models for error performance and stability. CO4: Comprehend the estimation theory for linear systems and modeling algorithms.		
<b>Books and References</b> <ol style="list-style-type: none"> <li>Adaptive Filter Theory by Haykin, S., Pearson Education.</li> <li>Adaptive Signal Processing by Widrow, B. and Stearns, S.D., Pearson Education.</li> <li>Statistical and Adaptive Signal Processing by Manolakis, D.G., Ingle, V.K. and Kogon, M.S., Artech House.</li> <li>Statistical Signal Processing: Detection, Estimation, and Time Series Analysis by Scharf, L.L., Pearson.</li> </ol>		

Course Name: <b>Advanced Antenna and Electromagnetic Metamaterials: Concept and Applications</b> Course Code: <b>EC-482</b> Course Type: <b>Stream Elective-II</b>		
Contact Hours/Week: <b>3L</b>		Course Credits: <b>03</b>
<b>Course Objectives</b> <ul style="list-style-type: none"> <li>To impart knowledge about the advanced concepts of antenna engineering.</li> <li>To introduce the design principle relevant to Advanced Antennas for different applications.</li> <li>To enable the students to understand concept of Electromagnetic Metamaterials.</li> <li>To introduce the design principle relevant to Metamaterials for various applications.</li> </ul>		
Unit Number	Course Content	Contact Hours
UNIT-01	<b>Definitions &amp; Preliminary Information:</b> Application areas of Antennas, Maxwell's Equations and Boundary conditions, Wave Equations, Infinitesimal (Hertzian) Dipoles, Monopoles, Inverted-F Antennas	06L
UNIT-02	<b>Aperture-type and Microstrip Antennas:</b> Radiation from apertures, aperture distribution, Transmission line and Cavity model for patch antennas, circularly polarized microstrip antenna, Microstrip patch and printed dipole arrays	8L
UNIT-03	<b>Advanced Antennas:</b> Travelling wave and Broadband Antennas, Frequency Independent Antennas, Antenna Miniaturization, and Fractal Antennas, Smart Antennas: Concept and Benefits of Smart Antennas, MIMO Antenna configurations, Pattern and polarization diversity, Mutual coupling reduction techniques, correlation coefficients, Massive MIMO system design.	8L
UNIT-04	<b>Metamaterials Background and Spatial Metamaterials:</b> General Historical perspective and idea of Metamaterials (MTMs), Plane-wave propagation in simple medium, Dispersive model for the dielectric permittivity, Phase velocity and group velocity. Spatial Metamaterials: Metamaterials and homogenization procedure, Split-ring resonator media, Media with negative permittivity and permeability: theory and properties, Origins of negative refraction and other properties.	10L
UNIT-05	<b>Different Metamaterials and applications:</b> Transmission Line Metamaterials: Ideal Homogeneous CRLH TLs (Composite Right-Left Handed Transmission Lines), LC Network Implementation and distributed 1D CRLH Structures, Conversion from Transmission Line to constitutive Parameters, Negative and Zeroth-Order Resonators. Meta-surface concepts: Artificial High-Impedance Surface design, EBG (Electromagnetic Bandgap Structures), Gain-enhancement in antennas using MTM, Miniaturization using MTM, Design of FSS Radomes for EMI Shielding and Absorbers, Beam-steering using Intelligent Reflecting Surfaces (IRS).	10L
<b>Pre-requisites:</b> Basic Electromagnetic theory and transmission line, Basic Antennas Theory <b>Course Outcomes</b> Upon successful completion of the course, the students will be able to CO1: Describe the antenna design principle for advanced antennas. CO3: Apply principles of radiation from the conducting surfaces and aperture surfaces at RF frequencies and assess the numerical techniques for the analysis of the antennas. CO4: Describe the need for metamaterials and their design principle. CO5: Apply and design different metamaterials for specific applications.		
<b>Books and References</b> <ol style="list-style-type: none"> <li>Field and Wave Electromagnetics, D. K. Cheng, Pearson Education Asia Ltd, Second Edition, 2006.</li> <li>Antenna Theory: Analysis and Design, C. A. Balanis, John Wiley, Fourth Edition, 2016.</li> <li>Antenna Theory and Design, W. L. Stutzman and G. A. Thiele, John Wiley &amp; Sons Inc, 1981.</li> <li>Antennas, J. D. Karus, McGraw Hill, 1988</li> <li>Microstrip antennas, I. J. Bahl and P. Bhartia, Artech house, 1980.</li> <li>Electromagnetic Metamaterials: Transmission Line Theory and Microwave Applications, The Engineering Approach, C. Caloz and T. Itoh, John Wiley &amp; Sons, Inc., Hoboken, New Jersey, 2006.</li> <li>Physics and Applications of Negative Refractive Index Materials, S. A. Ramakrishna and T. M. Grzegorzczuk, CRC Press, Taylor &amp; Francis Group and SPIE Press, 2009.</li> <li>Negative Refraction Metamaterials: Fundamental Principles and Applications, G. V. Eleftheriades and K. G. Balmain Copyright: IEEE, John Wiley &amp; Sons, Inc., Hoboken, New Jersey, 2005.</li> </ol>		

Course Name: <b>Nano Electronics: Devices and Materials</b> Course Code: <b>EC-483</b> Course Type: <b>Stream Elective-II</b>		
Contact Hours/Week: <b>3L</b>		Course Credits: <b>03</b>
<b>Course Objectives</b> <ul style="list-style-type: none"> <li>Understand nanotechnology and its impact on the electronics industry and different types of nanomaterials and their properties.</li> <li>Learn principles of quantum mechanics and design quantum dot-based devices.</li> <li>Gain knowledge of carbon-based nanomaterials and design carbon-based nanoelectronic devices.</li> <li>Gain knowledge of semiconductor nanomaterials and design semiconductor nanoelectronic devices.</li> <li>Understand emerging trends in nanoelectronics and critically evaluate challenges and opportunities in scaling down devices to the nanoscale, and their potential impact on various industries</li> </ul>		
<b>Unit Number</b>	<b>Course Content</b>	<b>Contact Hours</b>
UNIT-01	<b>INTRODUCTION TO NANOELECTRONICS:</b> Physical and Technological Limitations of Microelectronics, Transitioning from Microelectronics to Nanoelectronics, MOS Scaling Theory- Issues in Scaling MOS Transistors, Short Channel Effects; Free Electron Theory & The New Ohm's Law: Why Electrons flow, Classical free electron theory, Sommerfeld's theory, The quantum of conductance, Coulomb blockade, Towards Ohm's law.	07L
UNIT-02	<b>EFFECT OF TEMPERATURE:</b> The Elastic Resistor: Conductance of an Elastic Resistor, Elastic Resistor- Semiconductor heterostructures, Lattice-matched and pseudomorphic heterostructures, Inorganic nanowires, Organic semiconductors, Carbon nanomaterials: nanotubes and fullerenes, Graphene	07L
UNIT-03	<b>MATERIALS FOR NANOELECTRONICS:</b> Semiconductors, Crystal lattices: bonding in crystals, Electron energy bands, Semiconductor heterostructures, Lattice-matched and pseudomorphic heterostructures, Inorganic nanowires, Organic semiconductors, Carbon nanomaterials: nanotubes and fullerenes fabrication of nanostructures Crystal growth, Nanolithography, Clusters and nanocrystals, Nanotube growth,- Characterization of nanostructures	07L
UNIT-04	<b>BALLISTIC AND DIFFUSIVE TRANSPORT:</b> Ballistic and Diffusive Transfer Times, Channels for Conduction Conductivity, Conductivity: $E(p)$ or $E(k)$ Relations, Counting States, Drude Formula, Quantized Conductance, Electron Density –Conductivity; Electron transport in semiconductors and nanostructures: Time and length scales of the electrons in solids, Statistics of the electrons in solids and nanostructures, Fermi statistics for electrons, the density of states of electrons in nanostructures, Electron transport in nanostructures.	07L
UNIT-05	<b>ELECTRONS IN TRADITIONAL LOW-DIMENSIONAL STRUCTURES:</b> Electrons in quantum wells: Single modulation-doped heterojunctions, Numerical analysis of a single heterojunction, Control of charge transfer, Electrons in quantum wires, Electron transport in quantum wires, Electrons in quantum dots; Nanostructure devices: Introduction, MODFETS, heterojunction bipolar transistors, Resonant-tunneling diodes, Field-effect transistors, Single-electron-transfer devices, Potential-effect transistors, Carbon Nanotube Transistors, Semiconductor Nanowire FETs etc.	07L
<b>Course Outcomes</b> Upon successful completion of the course, the students will be able to CO1: Demonstrate knowledge of nanotechnology and its impact on the electronics industry and different types of nanomaterials and their properties. CO2: Design semiconductor nanoelectronic devices using knowledge of semiconductor nanomaterials and fabrication technique. CO3: Understanding the applications of quantum physics in Semiconductor Devices. CO4: Identifying different devices so as to meet out the present design, health, safety and environmental challenges		
<b>Books and References</b> <ol style="list-style-type: none"> <li>Fundamentals of Nanoelectronics by George W. Hanson, Pearson Education.</li> <li>Nanoelectronics and Nanosystems: From Transistors to Molecular and Quantum Devices by Karl Gosser, Springer</li> <li>Introduction to Nanoelectronics: Science, Nanotechnology, Engineering &amp; Applications by Vladimir. V. Mitin, Cambridge University Press.</li> <li>Introduction to Nano Science and Technology by S.M. Lindsay, World Scientific.</li> <li>Lessons from Nanoscience: A Lecture Note Series, By Supriyo Dutta, World Scientific.</li> <li>Quantum Transport- Atom to Transistor, By Supriyo Dutta, Cambridge University Press.</li> <li>Nanotechnology for Microelectronics and optoelectronics, By J.M. Martinez-Duart, R. J. Martin Palma, F. Agulle Rueda, Elsevier.</li> </ol>		

Course Name: <b>Characterization of Semiconductor Materials &amp; Devices</b>		
Course Code: <b>EC-484</b>		
Course Type: <b>Stream Elective-II</b>		
Contact Hours/Week: <b>3L</b>		Course Credits: <b>03</b>
<b>Course Objectives</b> <ul style="list-style-type: none"> <li>• To impart knowledge about properties of various semiconductor materials</li> <li>• To introduce the semiconductor physics behind the materials</li> <li>• To enable the students to understand the design of semiconductor devices using electronic materials</li> <li>• To understand how various semiconductor devices behave and how to optimize them</li> </ul>		
<b>Unit Number</b>	<b>Course Content</b>	<b>Contact Hours</b>
UNIT-01	<b>INTRODUCTION AND PROPERTIES OF SEMICONDUCTOR MATERIALS</b> Various Semiconductor materials and their advantages & disadvantages applied to VLSI and Nano-electronics Crystal structure, Band theory, Carrier concentration at thermal equilibrium, Density of states, Fermi energy, Ionization of impurity in semiconductor, Quantum aspect of semiconductors.	07 L
UNIT-02	<b>SEMICONDUCTOR CARRIER DYNAMICS</b> Scattering of carrier in semiconductors, Low field effect in semiconductor, Very high field effect in semiconductor, Carrier transport phenomena, Charge injection and quasi equilibrium, Generation and recombination of electron and holes and Basic equation for semiconductor device operation.	07 L
UNIT-03	<b>MEASUREMENT OF SEMICONDUCTOR PROPERTIES</b> Resistivity, conductivity, and Band gap of Semiconductor materials.	03 L
UNIT-04	<b>SEMICONDUCTOR JUNCTION WITH METALS, INSULATORS AND SEMICONDUCTORS</b> Procedure for analyzing semiconductor devices, Basic equations and approximations, Characteristics, and energy band diagrams of PN Junction diodes-step and graded junction, Schottky barrier diode, Ohmic contact, Insulator-semiconductor junction.	09 L
UNIT-05	<b>COMPOUND SEMICONDUCTOR DEVICES</b> Electronic properties of compound Semiconductor materials, Optoelectronic Devices; LED, PIN Photodetector, Semiconductor laser, Microwave Devices.	08 L
<b>Course Outcomes</b> Upon successful completion of the course, the students will be able to CO1: Identify various Materials useful in designing Semiconductor Devices CO2: Understand the semiconductor physics of the materials CO3: Apply different material characteristics for designing of Semiconductor devices CO4: Understand the theoretical concepts of various semiconductor devices and their applications		
<b>Books and References</b> <ol style="list-style-type: none"> <li>1. Physics of Semiconductor Devices by S. M. Sze, Wiley Publication</li> <li>2. Semiconductor Materials and Devices by M. S. Tyagi, Wiley Publication</li> <li>3. Solid State Electronics Devices by B. G. Streetman, Prentice Hall</li> <li>4. Semiconductor Devices by Kanaan Kano, Prentice Hall</li> </ol>		

Course Name: <b>Embedded Hardware Design</b>		
Course Code: <b>EC-485</b>		
Course Type: <b>Stream Elective-II</b>		
Contact Hours/Week: <b>3L</b>		Course Credits: <b>03</b>
<b>Course Objectives</b> <ul style="list-style-type: none"> <li>To study the role of microcontrollers in embedded systems using the basic programming and interfacing techniques.</li> </ul>		
Unit Number	Course Content	Contact Hours
UNIT-01	<b>FUNDAMENTALS OF EMBEDDED SYSTEM</b> Introduction to the embedded system, Memory, Sensors and Actuator types, Communication Interface, Embedded firmware (RTOS, Drivers, Application programs), Power-supply (Battery technology, Solar), PCB and Passive components, Safety and reliability, environmental issues. Ethical practice. Characteristics and quality attributes (Design Metric) of embedded system. Real time system's requirements, real time issues, interrupt latency. Embedded Product development life cycle, Program modelling concepts: DFG, FSM, Petri-net, and UML.	<b>07L</b>
UNIT-02	<b>EMBEDDED HARDWARE, DESIGN AND SERIAL COMMUNICATION</b> Familiarization with ARM-v7-M (Cortex-M3), ARM-v7-R (CortexR4) architectures, basic communication protocols like SPI, SCI (RS232, RS485), I2C, 10 CAN, Fieldbus (Profibus), USB (v2.0), Bluetooth, Zig-Bee, Wireless sensor network	<b>06L</b>
UNIT-03	<b>EMBEDDED SOFTWARE, FIRMWARE CONCEPTS AND DESIGN</b> Introduction to embedded C-programming: Optimizing for Speed/Memory needs, Interrupt service routines, macros, functions, modifiers, data types, device drivers, Multithreading programming (Laboratory work on J2ME Java mobile application). Basic embedded C programs/applications for ARM-v7, using ARM-GCC-toolchain, Emulation of ARM-v7 (e.g., using QEMU), and Linux porting on ARM-v7 (emulation) board. A case study on "Medical monitoring systems" "Process control system (temp, pressure)" & "Communication: Wireless (sensor) networks.	<b>07L</b>
UNIT-04	<b>REAL TIME OPERATING SYSTEM:</b> Need of POSIX Compliance, Role of RTOS in Embedded system software, Foreground/Background systems, multitasking, context switching, IPC, Scheduler policies, Architecture of kernel, task scheduler, ISR, Semaphores, mailbox, message queues, pipes, events, timers, memory management, RTOS services in contrast with traditional OS.	<b>07L</b>
UNIT-05	<b>INTRODUCTION TO MCOS-II RTOS Basics</b> of kernel structure of $\mu$ COS-II, Synchronization in $\mu$ COS-II, Inter-task communication in $\mu$ COS-II, Memory management in $\mu$ COS-II, porting of RTOS on ARM-v7 (emulation) board, Application developments using $\mu$ COS-II, basic device (drivers) usage.	<b>05L</b>
<b>Course Outcomes</b> Upon successful completion of the course, the students will be able to CO1: Acquire knowledge and understand fundamental embedded systems design paradigms, architectures, possibilities and challenges, both with respect to software and hardware CO2: Analyse a system both as whole and in the included parts, to understand how these parts interact in the functionality and properties of the system. CO3: Practically apply gained theoretical knowledge in order to design, analyze and implement embedded systems, e.g. integrating embedded subsystems and applications CO4: Apply formal method, testing, verification, validation and simulation techniques and tools in order to engineer reliable and safe embedded systems. CO5: Demonstrate a deeper understanding of the electronics and physical principles used for embedded biomedical measuring systems		
<b>Books and References</b> <ol style="list-style-type: none"> <li>Introduction to Embedded Systems by Shibu K. V. (TMH)</li> <li>Embedded System Design – A unified hardware and software introduction: by F. Vahid (John Wiley)</li> <li>Embedded Systems by Rajkamal (TMH)</li> <li>Embedded Systems by L. B. Das (Pearson)</li> <li>Embedded System design by S. Heath (Elsevier)</li> <li>Embedded microcontroller and processor design: by G. Osborn (Pearson)</li> <li>Embedded Systems by Frank Vahid, Wiley India, 2002</li> <li>Embedded Microcomputer Systems – Real Time Interfacing by Jonathan W. Valvano; Cengage Learning; Third or later edition.</li> </ol>		

Course Name: <b>Electromagnetic Interference and Compatibility</b>		
Course Code: <b>EC-486</b>		
Course Type: <b>Stream Elective-II</b>		
Contact Hours/Week: <b>3L</b>		Course Credits: <b>03</b>
<b>Course Objectives</b> To introduce the basic concepts of Electromagnetic Interference, teach the importance of Electromagnetic Compatible designs and explain the existing standards for Electromagnetic Compatibility and Measurement Techniques.		
<b>Unit Number</b>	<b>Course Content</b>	<b>Contact Hours</b>
UNIT-01	<b>EMI/EMC CONCEPTS</b> EMI-EMC definitions, Sources and Victims of EMI; Conducted and Radiated EMI Emission and Susceptibility; Case Histories; Radiation Hazards to humans. EMC regulations	<b>06L</b>
UNIT-02	<b>EMI COUPLING PRINCIPLES</b> Conducted, radiated and transient coupling; Common ground impedance coupling; Common mode and ground loop coupling; Differential mode coupling; Near field cable to cable coupling; Field to cable coupling; Power mains and Power supply coupling; Transient EMI, ESD	<b>08L</b>
UNIT-03	<b>EMI CONTROL</b> Shielding; EMI Filters; Grounding; Bonding; Isolation transformer; Transient suppressors; EMI Suppression Cables.	<b>08L</b>
UNIT-04	<b>EMC DESIGN FOR CIRCUITS AND PCBs</b> Noise from Relays and Switches; Nonlinearities in Circuits; Cross talk in transmission line and cross talk control; Component selection and mounting; PCB trace impedance; Routing; Power distribution decoupling; Zoning; Grounding; VIAs; Terminations	<b>10L</b>
UNIT-05	<b>EMI MEASUREMENTS AND STANDARDS</b> Open area test site; TEM cell; EMI test shielded chamber and shielded ferrite lined anechoic chamber; Line impedance stabilization networks; EMI Rx and spectrum analyzer; Civilian standards - CISPR, FCC, IEC, EN; Military standards-MIL461E/462.	<b>10L</b>
<b>Course Outcomes</b> Upon successful completion of the course, the students will be able to CO1: Understand EMI/EMC and its hazardous effect. CO2: Identify the various types and mechanisms of Electromagnetic Interference CO3: Propose a suitable EMI mitigation technique. CO4: design EMI mitigated Circuits and PCBs CO5: Describe the various EMC Standards and methods to measure them		
<b>Books and References</b> 1. Electromagnetic Compatibility Engineering by Henry Ott., John Wiley & Sons Inc; 1st edition 2009 2. Introduction to Electromagnetic Compatibility by C.R.Paul , John Wiley and Sons, Inc, 2 <sup>nd</sup> edition 2010		

Course Name: <b>Nanophotonics and Plasmonics: Concept and Applications</b>		
Course Code: <b>EC-487</b>		
Course Type: <b>Stream Elective-II</b>		
Contact Hours/Week: <b>3L</b>		Course Credits: <b>03</b>
<b>Course Objectives</b> This course explains the concepts and applications of Nanophotonic and plasmonics for future communication devices. It focuses on the Photonic Crystals, Nanophotonics in metals, Transmission through apertures and films, Simulation and Design of different photonic and plasmonics components.		
Unit Number	Course Content	Contact Hours
UNIT-01	<b>Introduction to Photonics:</b> Electromagnetic waves; light; Maxwell equations; Wave equation; Modes, laser sources, semiconductor quantum wells, photo detectors, quantum dots, nanowires, Dielectric optical waveguides, directional coupler, Machzehnder interferometer, Optical microresonators etc.	07L
UNIT-02	<b>Photonic Crystals:</b> Photonic bandgap (PGB). PBG structures, wave propagation, Construction methods, Applications: wave guides and photonic crystals fibres, optical microcavities, Photonic VLSI	8L
UNIT-03	<b>Nanophotonics in metals:</b> Electromagnetics of Metals, Electromagnetic Wave Propagation, Dielectric function and dispersion, Surface Plasmon polaritons, Single and multilayer systems, Exaction of surface Plasmon, plasmonic waveguides and resonators, localized surface plasmons, Nanoantennas. Metamaterials and Negative Index at Optical Frequencies.	8L
UNIT-04	<b>Transmission through apertures and films:</b> Theory of Diffraction by Sub-Wavelength Aperture, Extraordinary Transmission, Directional Emission via Exit Surface Patterning, Localized Surface Plasmons and Light Transmission Through Single Apertures, Emerging Applications of Extraordinary Transmission, Transmission of Light Through a Film Without Apertures.	9L
UNIT-05	<b>Simulation and Design:</b> Optical microresonators, guiding bending and splitting of light through photonic crystals, microcavity based MUX and DEMUX, photonic crystal fiber, plasmonic waveguides and resonators, Nanoantennas, Extraordinary transmission, Bull's eye structures, Metamaterials.	10L
<b>Course Outcomes</b> Upon successful completion of the course, the students will be able to CO1: Understand the basic of photonics. CO2: Understand the photonic crystals and their application. CO3: Understand the interaction of light with metal. CO4: Understand and analyse the transmission through apertures and films. CO5: Design and analyse the different photonics and plasmonic components for different applications.		
<b>Books and References</b> 1. Fundamentals and Applications, Stefen A. Maer, Springer 2007. 2. Nanophotonics with Surface Plasmon, Vladimir M. Salaev, Part II, 2006, Photonic Spectra. 3. Photonic crystals: Molding the flow of light, J.D. Joannopoulos, 2nd Edition, 2008 Princeton University Press 4. Integrated Photonics: fundamentals, G. Lifante, Wiley.		



Course Name: <b>Advanced IC Design</b>		
Course Code: <b>EC-488</b>		
Course Type: <b>Stream Elective-II</b>		
Contact Hours/Week: <b>3L</b>		Course Credits: <b>03</b>
<b>Course Objectives</b> <ul style="list-style-type: none"> <li>To impart a strong foundation of IC based design.</li> <li>To introduce the various applications of operational amplifiers and its integration with other devices.</li> <li>To learn biomedical application of op-amp design and other related applications.</li> </ul>		
<b>Unit Number</b>	<b>Course Content</b>	<b>Contact Hours</b>
UNIT-01	<b>Operational Amplifier Design</b> using CMOS as well as Bipolar technologies. Linear and non-linear applications of operational amplifiers. Active filters, response characteristics of Butter worth, Chebyshev and causal filters. Design and analysis of higher order filters of all types.	<b>07L</b>
UNIT-02	<b>Design of Super Buffer Circuits</b> for driving large capacitive loads. Design and analysis of CMOS Schmitt trigger circuit.	<b>06L</b>
UNIT-03	<b>Comparators</b> and their characteristics zero crossing detector, voltage limiters, absolute value detectors, sample and hold circuit.	<b>07L</b>
UNIT-04	<b>Biomedical applications</b> of instrumentation amplifier. Design and analysis of multi-vibrator circuits using transistors, Op-Amps and 555 Timer.	<b>05L</b>
UNIT-05	<b>Design and analysis of oscillator circuits</b> using transistors and Op-Amps. Phase shift, Wien Bridge and quadrature oscillators. Square wave, triangular wave, saw tooth wave generators and voltage-controlled oscillator.	<b>06L</b>
UNIT-06	<b>Differential and Feedback Amplifiers</b>	<b>05L</b>
<b>Course Outcomes</b> Upon successful completion of this course students will be able to: CO1: Understand and design the advanced ICs using op-amp and perform operations and their troubleshooting. CO2: To learn how to detect, amplify, store, create and manipulate signals using operational amplifiers. CO3: To design and analyze the responses of IC based designed circuits in the area of power management, signal conditioning, analog and digital communication. CO4: To develop IC based projects in the above areas.		
<b>Books and References</b> 1. CMOS Analog Circuit Design by P. E. Allen and D. R. Holberg 2. CMOS Digital Integrated Circuits-Analysis & Design by S. M. Kang & Y. Leblebici. TMH 2004 3. Design of Analog CMOS Integrated Circuits by B. Razavi. Tata McGraw Hill, 2005.		

Course Name: <b>Electronic Packaging</b> Course Code: <b>EC-489</b> Course Type: <b>Stream Elective-II</b>		
Contact Hours/Week: <b>3L</b>		Course Credits: <b>03</b>
<b>Course Objectives</b> <ul style="list-style-type: none"> <li>To introduce and discuss various issues related to the system packaging.</li> <li>To enable the students to understand the various issues in packaging.</li> </ul>		
Unit Number	Course Content	Contact Hours
UNIT-01	<b>OVERVIEW OF ELECTRONIC SYSTEMS PACKAGING:</b> Functions of an Electronic Package, Packaging Hierarchy, IC packaging: MEMS packaging, consumer electronics packaging, medical electronics packaging, Trends, Challenges, Driving Forces on Packaging Technology, Materials for Microelectronic packaging, Packaging Material Properties, Ceramics, Polymers, and Metals in Packaging, Material for high density interconnect substrates	<b>09L</b>
UNIT-02	<b>ELECTRICAL ISSUES IN PACKAGING:</b> Electrical Issues of Systems Packaging, Signal Distribution, Power Distribution, Electromagnetic Interference, Transmission Lines, Clock Distribution, Noise Sources, Digital and RF Issues. Design Process Electrical Design: Interconnect Capacitance, Resistance and Inductance fundamentals; Packaging roadmaps – Hybrid circuits – Resistive, Capacitive and Inductive parasitic	<b>09L</b>
UNIT-03	<b>CHIP PACKAGES:</b> IC Assembly – Purpose, Requirements, Technologies, Wire bonding, Tape Automated Bonding, Flip Chip, Wafer Level Packaging, reliability, wafer level burn – in and test. Single chip packaging: functions, types, materials processes, properties, characteristics, trends. Multi-chip packaging: types, design, comparison, trends. System – in - package (SIP); Passives: discrete, integrated, and embedded	<b>09L</b>
UNIT-04	<b>PCB, SURFACE MOUNT TECHNOLOGY AND THERMAL CONSIDERATIONS:</b> Printed Circuit Board: Anatomy, CAD tools for PCB design, Standard fabrication, Micro via Boards. Board Assembly: Surface Mount Technology, Through Hole Technology, Process Control and Design challenges. Thermal Management, Heat transfer fundamentals, Thermal conductivity and resistance, Conduction, convection and radiation – Cooling requirements	<b>09L</b>
UNIT-05	<b>TESTING:</b> Reliability, Basic concepts, Environmental interactions. Thermal mismatch and fatigue – failures – thermo mechanically induced –electrically induced – chemically induced. Electrical Testing: System level electrical testing, Interconnection tests, Active Circuit Testing, Design for Testability	<b>09L</b>
<b>Course Outcomes</b> Upon successful completion of the course, the students will be able to CO1: Give a comprehensive introduction to the various packaging types used along with the associated thermal, speed, signal and integrity power issues. CO2: Enable design of packages which can withstand higher temperature, vibrations and shock. CO3: Design of PCBs which minimize the EMI and operate at higher frequency. CO4: Analyze the concepts of Testing and testing methods.		
<b>Books and References</b> <ol style="list-style-type: none"> <li>Fundamentals of Microsystems Packaging by Tummala, Rao R., McGraw Hill, 2001</li> <li>The electronic packaging handbook by Blackwell (Ed), CRC Press, 2000.</li> <li>Microelectronics packaging handbook by Tummala, Rao R, McGraw Hill, 2008.</li> <li>Printed Circuit Boards Design and Technology by Bosshart, Tata McGraw Hill, 1988.</li> <li>Electronic Product design by R.G. Kaduskar and V.B. Baru, Wiley India, 2011</li> <li>Printed Circuit Board by R.S. Khandpur, Tata McGraw Hill, 2005</li> <li>Recent literature in Electronic Packaging</li> <li>Essentials of Electronic Testing for Digital, memory &amp; Mixed signal VLSI Circuits by Michael L. Bushnell &amp; Vishwani D. Agarwal, Kluwer Academic Publishers. 2000.</li> <li>Digital System Testing and Testable Design by M. Abramovici, M. A. Breuer, and A.D. Friedman, Computer Science Press, 1990</li> </ol>		