

e-STC

On

**Computational Intelligence in VLSI and
Communication Systems**

17th August 2026 – 21st August 2026

An Initiative of

National Institute of Technology Hamirpur

(An Institute of National Importance)

Hamirpur – 177005, Himachal Pradesh, India



Organized by

Department of Electronics & Communication

Engineering

National Institute of Technology Hamirpur

Hamirpur – 177005, Himachal Pradesh, India

Web: www.nith.ac.in

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**National Institute of Technology Hamirpur
Hamirpur – 177005, Himachal Pradesh, India.**

About the Institution

National Institute of Technology Hamirpur is one of the thirty-one NITs of the country, established in 1986 as Regional Engineering College, as a joint and cooperative enterprise of the Govt. of India and Govt. of Himachal Pradesh. The goals of the institute as embodied in the logo are truly remarkable in their scope of vision. The Institute offers Bachelor, Master and Doctoral programmes in Engineering, Sciences, Architecture, Management and Humanities. The Institute spares no effort to foster the spirit of national integration among students, a close interaction with industry and laying strong emphasis on research. The institute has the flexibility to evolve and change in response to requirements of the Industry and happenings in technical world. Various programmes serve the purpose of building a comprehensive foundation of knowledge and of enhancing confidence, creativity and innovation in its students.

About the Department

Established in the year 1988, the Electronics & Communication Engineering (E&CE) Department NIT Hamirpur HP, has built an international reputation for excellence in teaching, research, and service. E&CED is making exhilarating progress in areas ranging from signal processing, artificial intelligence, microelectronics, mobile communications to VLSI Design Automation. In labs and classrooms, students draw on the expertise and knowledge of our able faculty, integrating practical, hands-on research experience with challenging and interesting coursework. The team approach is very warm in the Department. A palpable excitement surrounds the Department, an enthusiasm pervades every classroom and lab, invigorating our students and spurring on our faculty to fresh innovations.

About the e-STC

In VLSI design, computational intelligence techniques play a vital role in design automation, optimization, fault detection, and testing, enabling the development of complex and efficient integrated circuits. Similarly, in communication systems, intelligent methods are widely used for signal processing, channel estimation, resource allocation, and network optimization, especially in emerging technologies such as 5G/6G, IoT, and software-defined networks. This STC will provide participants with a comprehensive understanding of the application of computational intelligence techniques in the domain of VLSI design and Communication systems.

Resource Persons/Speakers

Faculties/Experts from IITs, NITs, IIITs, Industries and other premier Institutions/Organizations will deliver the lectures.

Targeted Participants

Faculty members from educational institutes, research scholars, UG/PG/PhD students, and employees from the industries.

Objective of the e-STC

- To provide a comprehensive understanding of computational intelligence techniques and their relevance to VLSI and communication domains.
- To explore the application of AI and machine learning methods in solving complex problems in VLSI design, testing, and optimization.
- To familiarize participants with advanced communication and signal processing systems and the role of intelligent algorithms in enhancing performance, reliability, and efficiency.

Registration Fee Details

Participants	Amount (in Rs.)
Participants from Academia/R&D Labs	500/-
Students (UG, PG, PhD)	200/-
Participants from Industries	1000/-

Certificate

e-Certificate will be issued to the participants after **successfully** submitting the feedback form on completion of the online workshop.

Last Date of Registration: 16 August 2026

How to Apply

The interested participants should register by paying the registration fee at **SBI-collect** and filling the Google form through the below link:

<https://forms.gle/GSrg9yv1zBsKEqpYA>

The SBI Collect has the following steps:

- Visit SBI **Collect** at
<https://onlinesbi.sbi.bank.in/sbicollect/>
- Select **Educational Institutes**.
- Search and select Educational Institutions Name: **NIT Hamirpur**
- Select Payment Category: **WORKSHOP STC FDP CONFERENCE**
- Fill up all the details.
 - Write **TITLE** of e-STC: *Computational Intelligence in VLSI and Communication Systems.*
 - Organizing Department: **DoECE**
- Address: Address of the participant.
- Submit the form and generate the receipt.
- Upload the SBI Collect receipt in the Google form mentioned above.