



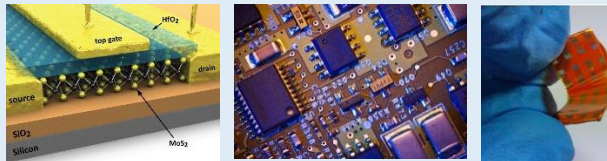
Five Days Online Faculty Development Programme



Sponsored by AICTE Training and
Learning (ATAL) Academy
On

“Recent Advances and Challenges in Nanoscale Devices: Design, Materials, and Applications Perspective”

1st to 5th June 2021
NIT Hamirpur, (H.P.).



Coordinator
Dr. Gopal Rawat

Organized by



Department of Electronics & Communication
Engineering (E&CED),
NIT Hamirpur, Himachal Pradesh.

About the Institute

National Institute of Technology Hamirpur is one of the thirty-one NITs of the country, which came into existence on 7th August 1986 as Regional Engineering College, a joint and cooperative enterprise of the Govt. of India and Govt. of Himachal Pradesh. On 26th June 2002, REC Hamirpur was awarded the status of Deemed University and upgraded to National Institute of Technology. The Institute offers Bachelor, Master and Doctoral programmes in Engineering, Sciences, Architecture, Management and Humanities. Various programmes serve the purpose of building a comprehensive foundation of knowledge and of enhancing confidence, creativity and innovation in its students.

About the Department

Electronics & Communication Engineering (E&CE) Department, established in the year 1988, is one of the oldest departments of NIT Hamirpur. The Department has been imparting quality education at undergraduate and post-graduate level. The faculty members have been active in teaching and research activities. The course curriculum is being revised from time to time so as to keep students abreast with latest emerging technologies. Department of E&CE has built an international reputation for excellence in teaching, research and service. E&CED is making exhilarating progress in areas ranging from VLSI Design to Communication Systems & Networks.

About ATAL Academy

AICTE Training and Learning (ATAL) Academy is established with the vision “to empower faculty to achieve goals of higher education such as access, equity and quality”. AICTE is committed for development of quality technical education in the county by initiating various schemes launched by Gov. of India, Ministry of Education. Council understands that there is a need of the day to train the young generation in skill sector and having faculty and technicians to be trained in their respective disciplines. Training is required for increasing the knowledge and skills of students to make them more employable to acquire global competencies.

COMMITTEE

Patron

Prof. (Dr.) Lalit Kumar Awasthi,
Director, NIT Hamirpur (H.P.).

Chairman

Dr. Ashok Kumar
HOD, E&CED, NIT Hamirpur (H.P.).

Convener and Coordinator

Dr. Gopal Rawat
Assistant Professor, E&CED, NIT Hamirpur.

Organizing Committee(s)

Faculty Members of E&CE,
Department, NIT Hamirpur (H.P.).

About the e-FDP

The key objective of this program is to discuss some critical concepts for understanding the state-of-the-art research work. The course will offer learning relevant to nanoscale device design and development including modelling, simulation, device fabrication starting from nanomaterial synthesis to material characterizations and etc. Their intended use in real life application shall be explained. This program will also cover the concepts from fundamentals to advanced level, giving thrust in nanoelectronics. The topics will focus on basics, advances and applications to benefit people from academic and research communities associated with Science and Engineering.

e-FDP Contents:

- ✓ Overview of new materials and devices.
- ✓ Device/Circuit modelling and simulation.
- ✓ Wide bandgap semiconductor devices.
- ✓ Emerging technologies and sensing applications.
- ✓ Fabrication and characterization techniques.
- ✓ VLSI Design. Nanoscience and Nanotechnology.
- ✓ Energy and Environment Applications.
- ✓ Flexible Electronics.
- ✓ Organic and Inorganic material synthesis, devices and applications.
- ✓ Hybrid heterostructures/ nanostructures.
- ✓ Recent developments and Challenges of Nanoelectronics.

Eligibility

The faculty members of the AICTE approved institutions, Research scholars, PG Scholars, participants from Government, Industry (Bureaucrats/Technicians/Participants from Industry etc.)/School Teachers and staff of host institutions.

Registration Process

All the registrations will be done from the below mentioned portal/link:

<https://www.aicte-india.org/atal>

Registration Fee: Nil.

Number of Seats:

Limited to maximum 200. Participants are selected by organizers on the first come first serve basis.

Certification

E-certificates will be issued by AICTE Training and Learning (ATAL) Academy to those participants who are registered on ATAL portal www.aicte-india.org/atal and attend the program with minimum 80% attendance and scored minimum 60% marks in the test.

Contact Details of Coordinator

Dr. Gopal Rawat

Assistant Professor, E&CED, NIT Hamirpur.

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Mob.: +91-9808205656

Resource Persons (Tentative)

- ❖ **Prof. (Dr.) Parthasarathi Chakrabarti**, Professor (HAG) & Director IEST, Shibpur.
- ❖ **Prof. (Dr.) Satyabrata Jit**, Professor (HAG), IIT BHU Varanasi.
- ❖ **Prof. (Dr.) Rajiv Prakash**, Dean(R&D) IIT (BHU) Varanasi.
- ❖ **Prof. (Dr.) Sudeb Dasgupta**, HOD ECE, IIT Roorkee.
- ❖ **Prof. (Dr.) Rajeevan Chandel**, Dean(Academic), NIT Hamirpur.
- ❖ **Dr. Shree Prakash Tiwari**, IIT Jodhpur.
- ❖ **Dr. N N Murty**, IIT Tirupati.
- ❖ **Dr. Pramod Kumar Tiwari**, IIT Patna.
- ❖ **Dr. (Mrs.) Shweta Tripathi**, MNNIT Allahabad.
- ❖ **Dr. Ravikant Saini**, IIT Jammu.
- ❖ **Dr. Shubham Sahay**, IIT Kanpur.
- ❖ **Dr. Soumya Jyoti Ray**, IIT Patna.
- ❖ **Dr. Kumar Prasannajit Pradhan**, IIITDM Kancheepuram.
- ❖ **Dr. Sunil Kumar Pandey**, Sr. Analog Design Engineer, Intel.
- ❖ **Mr. Amit Saini**, Director, Cadre Design Systems, EDA & TCAD Technology.



<https://www.aicte-india.org/atal>



<https://nith.ac.in/>